

### Micropower Regulator with Comparator and Shutdown

## FEATURES

- 20µA Supply Current
- 125mA Output Current
- 2.5V Reference
- Reference Output Sources 4mA and Sinks 4mA
- Open Collector Comparator Sinks 10mA
- Logic Shutdown
- 0.2V Dropout Voltage
- Thermal Limiting
- 8-Lead SO Package

# **APPLICATIONS**

- Battery Systems
- Battery Backup System
- Portable Terminals
- Portable Instruments
- Memory Keep Alive

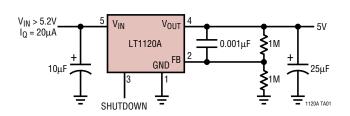
## DESCRIPTION

The LT<sup>®</sup>1120A is a combination micropower positive regulator and free collector comparator on a single monolithic chip. With only  $20\mu$ A supply current, the LT1120A can supply over 125mA of output current. Input voltage range is from 4.5V to 36V and dropout voltage is 0.6V at 125mA. Dropout voltage decreases with lower load currents. Also included on the chip is a class B output 2.5V reference that can either source or sink current. This allows it to be used as a supply splitter or auxiliary output. A shutdown pin allows logic shutdown of the output.

The comparator can be used for system or battery monitoring. For example, the comparator can be used to warn of low system voltage. Frequency compensation of the comparator for amplifier applications can be obtained by adding external output capacitance.

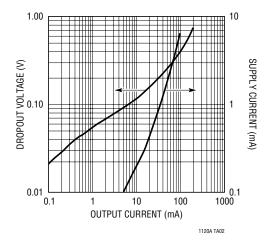
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# TYPICAL APPLICATION



**5V Regulator** 

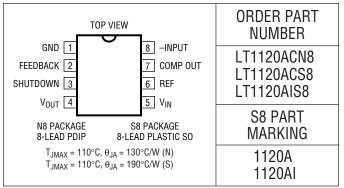
#### **Dropout Voltage and Supply Current**



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(NULE I)	
Input Voltage	36V
NPN Collector Voltage	
Output Short-Circuit Duration	Indefinite
Power Dissipation	Internally Limited
Operating Temperature Range	-
LT1120AC	0°C to 100°C
LT1120AI	40°C to 100°C
Storage Temperature Range	65°C to 150°C

# PACKAGE/ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges.

# **ELECTRICAL CHARACTERISTICS** $T_J = 25 \degree C$

PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Reference						·
Reference Voltage	$4.5V \le V_{IN} \le 36V$		2.46	2.50	2.54	V
Line Regulation	$4.5V \le V_{IN} \le 36V$			0.01	0.015	%/V
Load Regulation	$-2.0$ mA $\leq I_{REF} \leq 2$	$-2.0\text{mA} \le I_{\text{REF}} \le 2\text{mA}, V_{\text{IN}} = 12\text{V}$		0.3	0.6	%
Output Source Current	V <sub>IN</sub> = 5V			4		mA
Output Sink Current	V <sub>IN</sub> = 5V			4		mA
Temperature Stability				1		%
Regulator						·
Supply Current	$\begin{array}{c c} V_{IN} = 6V, \ I_{OUT} \leq 1 \\ V_{IN} = 36V, \ I_{OUT} \leq \\ V_{IN} = 12V, \ I_{OUT} = \end{array}$	$\begin{array}{l} V_{IN} = 6V, \ I_{OUT} \leq 100 mA \\ V_{IN} = 36V, \ I_{OUT} \leq 100 mA \\ V_{IN} = 12V, \ I_{OUT} = 125 mA \end{array}$		20 30 11	25 40 20	μΑ μΑ mA
Output Current	$(V_{IN} - V_{OUT}) \ge 1V$		125			mA
Load Regulation	$(V_{IN} - V_{OUT}) \ge 1V$	, V <sub>IN</sub> ≥ 6V		0.2	0.5	%
Line Regulation	$6V \le V_{IN} \le 36V$			0.01	0.015	%/V
Dropout Voltage	$I_{OUT} = 100$ mA $I_{OUT} = 125$ mA			0.02 0.40	0.05 0.65	V V
Feedback Sense Voltage	V <sub>IN</sub> = 12V		2.44	2.5	2.56	V
Shutdown Pin Voltage		Normal			0.4	V
	$V_{OUT} \le 0.5V$	Shutdown	2.2	1.4		V
Shutdown Pin Current	V <sub>IN</sub> = 1.4V	V <sub>IN</sub> = 1.4V		10		μA
Feedback Bias Current				15	40	nA
Minimum Load Current	V <sub>IN</sub> = 36V	V <sub>IN</sub> = 36V		1	5	μA
Short-Circuit Current	V <sub>IN</sub> = 36V			250	400	mA
Comparator						
Offset Voltage	V <sub>IN</sub> = 36V			3	7	mV
Bias Current	V <sub>IN</sub> = 36V	V <sub>IN</sub> = 36V		15	40	nA
Gain	$\Delta V_{OUT} = 29V, R_L$	$\Delta V_{OUT} = 29V, R_L = 20k$		10000		V/V
Power Supply Rejection	$4.5V \le V_S \le 36V$	$4.5V \le V_S \le 36V$		96		dB
Output Sink Current	V <sub>IN</sub> = 4.5V	V <sub>IN</sub> = 4.5V		18		mA
Saturation Voltage	I <sub>OUT</sub> = 1mA	I <sub>OUT</sub> = 1mA		0.4	0.6	V
Input Voltage Range			0		V <sub>IN</sub> – 1	V
Response Time				5		μs
Leakage Current	V <sub>IN</sub> = 36V, V(Pin 7) = 36V				2	μA
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## **ELECTRICAL CHARACTERISTICS**

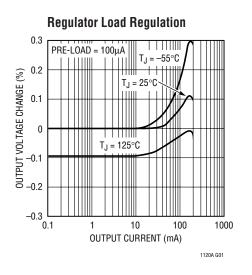
The  $\bullet$  denotes the specifications which apply over the full operating temperature range of T<sub>J</sub> = 25°C.

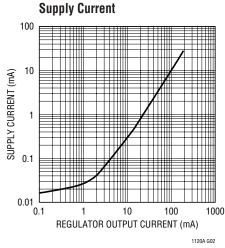
PARAMETER	CONDITIONS		MIN	ТҮР	MAX	UNITS
Reference	I	I				1
Reference Voltage	$4.5V \le V_{IN} \le 36V$		2.40	2.50	2.55	V
Line Regulation	$4.5V \le V_{IN} \le 36V$	•		0.01	0.02	%/\
Load Regulation	$-2.0$ mA $\leq I_{REF} \leq 2$ mA, $V_{IN} = 12V$			0.4	0.8	%
Output Source Current	$V_{IN} = 5V$		2			mA
Output Sink Current	V <sub>IN</sub> = 5V	•	2			mA
Regulator						-
Supply Current	$V_{IN} = 6V, I_{OUT} \le 100 \mu A$			30	40	μA
	$V_{IN} = 36V, I_{OUT} \le 100 \mu A$			40	50	μA
	V <sub>IN</sub> = 12V, I <sub>OUT</sub> = 125mA			11	20	mA
Output Current	$(V_{IN} - V_{OUT}) \ge 1V, V_{IN} \ge 6V$		125			mA
Load Regulation	$(V_{IN} - V_{OUT}) \ge 1V, V_{IN} \ge 6V$				1	%
Line Regulation	$6V \le V_{IN} \le 36V$				0.02	%/\
Dropout Voltage	I <sub>OUT</sub> = 100μA				0.06	V
	$I_{OUT} = 125 \text{mA}$				0.85	V
Feedback Sense Voltage	V <sub>IN</sub> = 12V	•	2.38	2.5	2.57	V
Feedback Bias Current					50	nA
Minimum Load Current	V <sub>IN</sub> = 36V				50	μA
Short-Circuit Current	V <sub>IN</sub> = 36V			240	400	mA
Comparator	·					
Offset Voltage	Offset Voltage V <sub>IN</sub> = 36V				10	mV
Bias Current	V <sub>IN</sub> = 36V			15	60	nA
Gain	$\Delta V_{OUT} = 29V, R_L = 20k$		1000			V/V
Output Sink Current (Note 2) V <sub>IN</sub> = 4.5V			5	10		mA
Leakage Current V <sub>IN</sub> = 36V, V(Pin 7) = 36V					8	μA

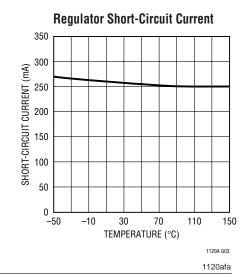
**Note 1:** Absolute Maximum Ratings are those values beyond which the life to the device may be impaired.

Note 2: For  $T_A \leq -40^{\circ}$ C output sink current drops to 2.5mA.

# **TYPICAL PERFORMANCE CHARACTERISTICS**



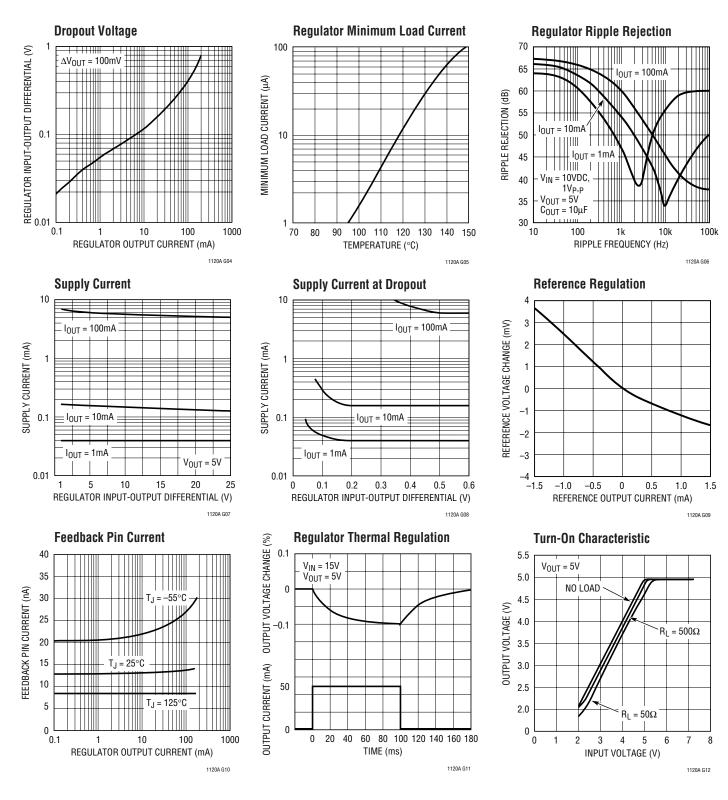






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# **TYPICAL PERFORMANCE CHARACTERISTICS**





## **PIN FUNCTIONS**

GND (Pin 1): Ground

**FEEDBACK (Pin 2):** This is the feedback point of the regulator. When operating, it is nominally at 2.5V. Optimum source resistance is 200k to 500k. The feedback pin should not be driven below ground or more positive than 5V.

**SHUTDOWN (Pin 3):** A logic 1 shuts off main regulator. Caution: noise or leakage into the shutdown pin can affect output voltage.

 $V_{OUT}$  (Pin 4): Main output, requires  $10\mu\text{F}$  output capacitor. Can be shorted to  $V_{IN}$  or ground without damaging device.

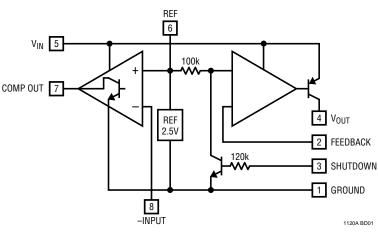
 $V_{IN}~(Pin~5):$  Input Supply. Bypass with  $10\mu F$  capacitor. Must always be more positive than ground.

**REF (Pin 6):** Reference. 2.5V can source or sink current. May be shorted to ground or up to 5V. Voltages in excess of 5V can damage the device.

**COMP OUT (Pin 7):** Open Collector NPN Output. May be connected to any voltage from ground to 36V more positive than ground (operates above  $V_{\text{IN}}$ ). Short-circuit protected.

-INPUT (Pin 8): Inverting Comparator Input.

## **BLOCK DIAGRAM**



# **APPLICATIONS INFORMATION**

The LT1120A is especially suited for micropower system applications. For example, the comparator section of the LT1120A may be used as a battery checker to provide an indication of low battery. Another type of system application for the LT1120A would be to generate the equivalent of split supplies off of a single power input. The regulator section provides regulated output voltage and the reference, which can both source and sink current is then an artificial system ground providing a split supply for the system. For many applications the comparator can be frequency compensated to operate as an amplifier. Compensation values for various gains are given in the data sheet. The comparator gain is purposely low to make it easier to frequency compensate as an amplifier. The NPN output is capable of sinking 10mA and can drive loads connected to voltages in excess of the positive power supply. This is useful for driving switches or linear regulators off of a higher input voltage.

# **APPLICATIONS INFORMATION**

#### Reference

Internal to the LT1120A is a 2.5V trimmed class B output reference. The reference was designed to be able to source or sink current so it could be used in supply splitting applications as well as a general purpose reference for external circuitry. The design of the reference allows it to source and sink typically 4mA. The available source and sink current decreases as temperature increases. It is sometimes desirable to decrease the AC output impedance by placing an output capacitor on Pin 6. The reference in the LT1120A becomes unstable with large capacitive loads placed directly on it. When using an output capacitor, about  $20\Omega$  should be used to isolate the capacitor from the reference pin. This  $20\Omega$  resistor can be placed directly in series with the capacitor or alternatively the reference line can have  $20\Omega$  placed in series with it and then a capacitor to ground. This is shown in Figure 1. Other than placing large capacitive loads on the reference, no other precautions are necessary and the reference is stable with nominal stray capacitances.

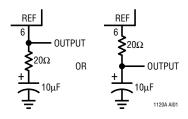


Figure 1. Bypassing Reference

#### **Overload Protection**

The main regulator in the LT1120A is current limited at approximately 250mA. The current limit is stable with both input voltage and temperature.

Like most other IC regulators, a minimum load is required on the output of the LT1120A to maintain regulation. For most standard regulators this is normally specified at 5mA. Of course, for a micropower regulator this would be a tremendously large current. The output current must be large enough to absorb all the leakage current of the pass transistor at the maximum operating temperature. It also affects the transient response; low output currents have long recovery times from load transients. At high operating temperatures the minimum load current increases and having too low of a load current may cause the output to go unregulated. Devices are tested for minimum load current at high temperature. The output voltage setting resistors to the feedback terminal can usually be used to provide the minimum load current.

#### **Frequency Compensation**

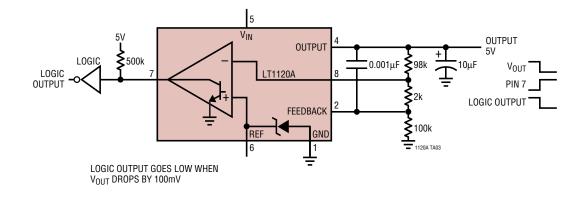
The LT1120A is frequency compensated by a dominant pole on the output. An output capacitor of  $10\mu$ F is usually large enough to provide good stability. **Increasing the output capacitor above 10\muF further improves stability**. In order to ensure stability, a feedback capacitor is needed between the output pin and the feedback pin. This is because stray capacitance can form another pole with the large value of feedback resistors used with the LT1120A. Also, a feedback capacitor minimizes noise pickup and improves ripple rejection.

With the large dynamic operating range of the output current, 10000:1, frequency response changes widely. Low AC impedance capacitors are needed to insure stability. While solid tantalum are best, aluminum electrolytics can be used but larger capacitor values may be needed.



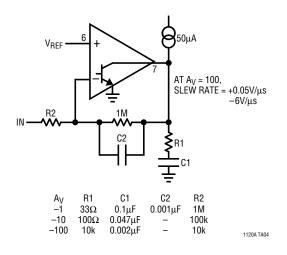
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#### TYPICAL APPLICATIONS

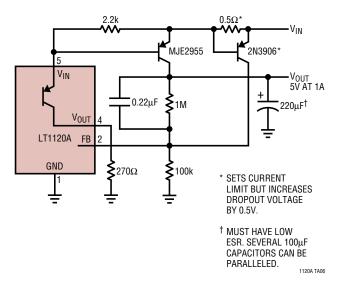


**Regulator with Output Voltage Monitor** 

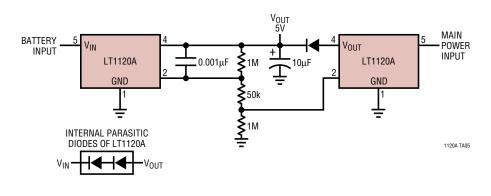
#### Compensating the Comparator as an Op Amp



**Current Limited 1 Amp Regulator** 



#### **Battery Backup Regulator**

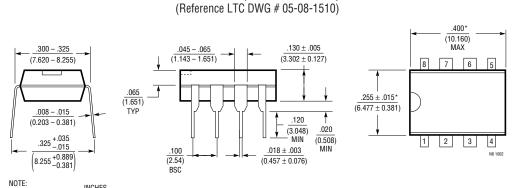




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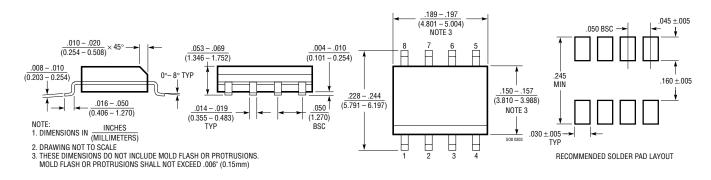
## PACKAGE DESCRIPTION



**N8 Package** 8-Lead PDIP (Narrow .300 Inch)

NOTE: 1. DIMENSIONS ARE <u>MILLIMETERS</u> \*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)





# **RELATED PARTS**

PART NUMBER	DESCRIPTION	COMMENTS
LT1761/LT1762	100mA/150mA, Low Noise Micropower, LDO	$V_{IN}$ : 1.8V to 20V; $V_{OUT(MIN)}$ = 1.22V; Dropout Voltage = 0.30V; $I_0$ = 20µA; $I_{SD}$ < 1µA; $V_{OUT}$ = Adj, 1.5V, 1.8V, 2V, 2.5V, 2.8V, 3V, 3.3V, 5V; ThinSOT <sup>TM</sup> /MS8 Packages. Low Noise <20µV <sub>RMSP-P</sub>
LT1763	500mA, Low Noise Micropower, LDO	$V_{\text{IN}}$ : 1.8V to 20V; $V_{\text{OUT}(\text{MIN})}$ = 1.22V; Dropout Voltage = 0.30V; $I_{\text{Q}}$ = 30µA; $I_{\text{SD}}$ < 1µA; $V_{\text{OUT}}$ = 1.5V, 1.8V, 2.5V,3V, 3.3V, 5V; SO-8 Package. Low Noise <20µV <sub>RMSP-P</sub>
LT1764/LT1764A	3A, Low Noise, Fast Transient Response, LDO	$V_{IN}$ : 2.7V to 20V; $V_{OUT(MIN)}$ = 1.21V; Dropout Voltage = 0.34V; $I_Q$ = 1mA; $I_{SD}$ < 1µA; $V_{OUT}$ = 1.8V, 2.5V, 3.3; DD, TO220, Packages. Low Noise <40µV <sub>RMSP-P</sub> , "A" Version Stable with Ceramic Capacitors
LT1962	300mA, Low Noise Micropower, LDO	$V_{IN}$ : 1.8V to 20V; $V_{OUT(MIN)}$ = 1.22V; Dropout Voltage = 0.27V; $I_Q$ = 30µA; $I_{SD}$ < 1µA; $V_{OUT}$ = 1.5V, 1.8V, 2.5V,3V, 3.3V, 5V; MS8 Package. Low Noise <20µ $V_{RMSP-P}$
LT1963/LT1963A	1.5A, Low Noise, Fast Transient Response, LDO	$V_{IN}$ : 2.1V to 20V; $V_{OUT(MIN)}$ = 1.21V; Dropout Voltage = 34V; $I_Q$ = 1mA; $I_{SD}$ < 1µA; $V_{OUT}$ = 1.5V 1.8V, 2.5V, 3.3; DD, TO220, SOT-223, SO-8 Packages. Low Noise <40µV <sub>RMSP-P</sub> , "A" Version Stable with Ceramic Capacitors
LT1964	200mA, Low Noise Micropower Negative, LDO	$V_{IN}$ : –0.9V to –20V; $V_{OUT(MIN)}$ = –1.21V; Dropout Voltage = 0.34V; $I_Q$ = 30µA; $I_{SD}$ < 3µA; $V_{OUT}$ = Adj, –5V; ThinSOT Package. Low Noise <30µV_{RMSP-P}, Stable with Ceramic Capacitors
LT3010	80V, 50mA, Low Noise Linear Regulator	$V_{IN}$ : 1.5V to 80V; $V_{OUT(MIN)}$ = 1.28V; Dropout Voltage = 0.30V; $I_Q$ = 30µA; $I_{SD}$ < 1µA; $V_{OUT}$ = Adj, -5V; MS8E Package. Low Noise <100µV <sub>RMSP-P</sub> , Stable with Ceramic Capacitors
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