

### Triple 600MHz Current Feedback Amplifier with Enable

The EL5392A is a triple current feedback amplifier with a very high bandwidth of 600MHz. This makes this amplifier ideal for today's high speed video and monitor applications.

With a supply current of just 6mA per amplifier and the ability to run from a single supply voltage from 5V to 10V, the EL5392A is also ideal for hand held, portable or battery powered equipment.

The EL5392A also incorporates an enable and disable function to reduce the supply current to 100µA typical per amplifier. Allowing the CE pin to float or applying a low logic level will enable the amplifier.

For applications where board space is critical, the EL5392A is offered in the 16 Ld QSOP package, as well as an industry-standard 16 Ld SO (0.150"). The EL5392A operates over the industrial temperature range of -40°C to +85°C.

### Ordering Information

| PART NUMBER           | PART MARKING | TAPE & REEL | PACKAGE              | PKG. DWG. # |
|-----------------------|--------------|-------------|----------------------|-------------|
| EL5392ACS             | EL5392ACS    | -           | 16 Ld SOIC (0.150")  | MDP0027     |
| EL5392ACS-T7          | EL5392ACS    | 7"          | 16 Ld SOIC (0.150")  | MDP0027     |
| EL5392ACS-T13         | EL5392ACS    | 13"         | 16 Ld SOIC (0.150")  | MDP0027     |
| EL5392ACU             | 5392ACU      | -           | 16 Ld QSOP           | MDP0040     |
| EL5392ACU-T13 (Note)  | 5392ACU      | 13"         | 16 Ld QSOP (Pb-free) | MDP0040     |
| EL5392ACUZ (Note)     | 5392ACUZ     | -           | 16 Ld QSOP (Pb-free) | MDP0040     |
| EL5392ACUZ-T7 (Note)  | 5392ACUZ     | 7"          | 16 Ld QSOP (Pb-free) | MDP0040     |
| EL5392ACUZ-T13 (Note) | 5392ACUZ     | 13"         | 16 Ld QSOP (Pb-free) | MDP0040     |

NOTE: Intersil Pb-free plus anneal products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

### Features

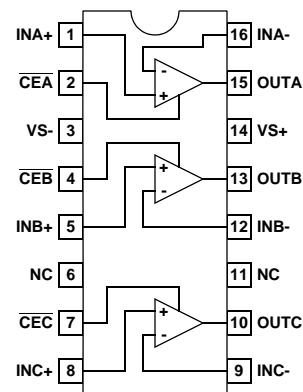
- 600MHz -3dB bandwidth
- 6mA supply current (per amplifier)
- Single and dual supply operation, from 5V to 10V
- Fast enable/disable
- Available in 16 Ld QSOP package
- Single (EL5192) and dual (EL5292) available
- High speed, 1GHz product available (EL5191)
- Low power, 4mA, 300MHz product available (EL5193, EL5293, and EL5393)
- Pb-free plus anneal available (RoHS compliant)

### Applications

- Video amplifiers
- Cable drivers
- RGB amplifiers
- Test equipment
- Instrumentation
- Current to voltage converters

### Pinout

EL5392  
[16 LD SOIC (0.150") & QSOP]  
TOP VIEW



# EL5392A

## Absolute Maximum Ratings (T<sub>A</sub> = 25°C)

Supply Voltage Between V<sub>S+</sub> and V<sub>S-</sub> ..... 11V  
 Maximum Continuous Output Current ..... 50mA  
 Operating Junction Temperature ..... 125°C  
 Power Dissipation ..... See Curves

Pin Voltages ..... V<sub>S-</sub> - 0.5V to V<sub>S+</sub> + 0.5V  
 Storage Temperature ..... -65°C to +150°C  
 Operating Temperature ..... -40°C to +85°C

**CAUTION:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

**IMPORTANT NOTE:** All parameters having Min/Max specifications are guaranteed. Typical values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: T<sub>J</sub> = T<sub>C</sub> = T<sub>A</sub>

**Electrical Specifications** V<sub>S+</sub> = +5V, V<sub>S-</sub> = -5V, R<sub>F</sub> = 750Ω for A<sub>V</sub> = 1, R<sub>F</sub> = 375Ω for A<sub>V</sub> = 2, R<sub>L</sub> = 150Ω, T<sub>A</sub> = 25°C unless otherwise specified.

| PARAMETER                      | DESCRIPTION                                  | CONDITIONS   | MIN  | TYP   | MAX | UNIT   |
|--------------------------------|--|--|------|-------|-----|--------|
| <b>AC PERFORMANCE</b>          |  |  |      |       |     |        |
| BW                             | -3dB Bandwidth                               | A <sub>V</sub> = +1                                    |      | 600   |     | MHz    |
|                                |  | A <sub>V</sub> = +2                                    |      | 300   |     | MHz    |
| BW1                            | 0.1dB Bandwidth                              |  |      | 25    |     | MHz    |
| SR                             | Slew Rate                                    | V <sub>O</sub> = -2.5V to +2.5V, A <sub>V</sub> = +2   | 2000 | 2300  |     | V/μs   |
| t <sub>S</sub>                 | 0.1% Settling Time                           | V <sub>OUT</sub> = -2.5V to +2.5V, A <sub>V</sub> = -1 |      | 9     |     | ns     |
| C <sub>S</sub>                 | Channel Separation                           | f = 5MHz   |      | 60    |     | dB     |
| e <sub>N</sub>                 | Input Voltage Noise                          |  |      | 4.1   |     | nV/√Hz |
| i <sub>N-</sub>                | IN- Input Current Noise                      |  |      | 20    |     | pA/√Hz |
| i <sub>N+</sub>                | IN+ Input Current Noise                      |  |      | 50    |     | pA/√Hz |
| dG                             | Differential Gain Error (Note 1)             | A <sub>V</sub> = +2                                    |      | 0.015 |     | %      |
| dP                             | Differential Phase Error (Note 1)            | A <sub>V</sub> = +2                                    |      | 0.04  |     | °      |
| <b>DC PERFORMANCE</b>          |  |  |      |       |     |        |
| V <sub>OS</sub>                | Offset Voltage                               |  | -10  | 1     | 10  | mV     |
| T <sub>C</sub> V <sub>OS</sub> | Input Offset Voltage Temperature Coefficient | Measured from T <sub>MIN</sub> to T <sub>MAX</sub>     |      | 5     |     | μV/°C  |
| R <sub>OL</sub>                | Transimpedance                               |  | 200  | 400   |     | kΩ     |
| <b>INPUT CHARACTERISTICS</b>   |  |  |      |       |     |        |
| CMIR                           | Common Mode Input Range                      |  | ±3   | ±3.3  |     | V      |
| CMRR                           | Common Mode Rejection Ratio                  |  | 42   | 50    |     | dB     |
| +I <sub>IN</sub>               | + Input Current                              |  | -60  | 3     | 60  | μA     |
| -I <sub>IN</sub>               | - Input Current                              |  | -40  | 4     | 40  | μA     |
| R <sub>IN</sub>                | Input Resistance                             |  |      | 37    |     | kΩ     |
| C <sub>IN</sub>                | Input Capacitance                            |  |      | 0.5   |     | pF     |
| <b>OUTPUT CHARACTERISTICS</b>  |  |  |      |       |     |        |
| V <sub>O</sub>                 | Output Voltage Swing                         | R <sub>L</sub> = 150Ω to GND                           | ±3.4 | ±3.7  |     | V      |
|                                |  | R <sub>L</sub> = 1kΩ to GND                            | ±3.8 | ±4.0  |     | V      |
| I <sub>OUT</sub>               | Output Current                               | R <sub>L</sub> = 10Ω to GND                            | 95   | 120   |     | mA     |
| <b>SUPPLY</b>                  |  |  |      |       |     |        |
| I <sub>SON</sub>               | Supply Current - Enabled                     | No load, V <sub>IN</sub> = 0V                          | 5    | 6     | 7.5 | mA     |
| I <sub>SOFF</sub>              | Supply Current - Disabled                    | No load, V <sub>IN</sub> = 0V                          |      | 100   | 150 | μA     |

## EL5392A

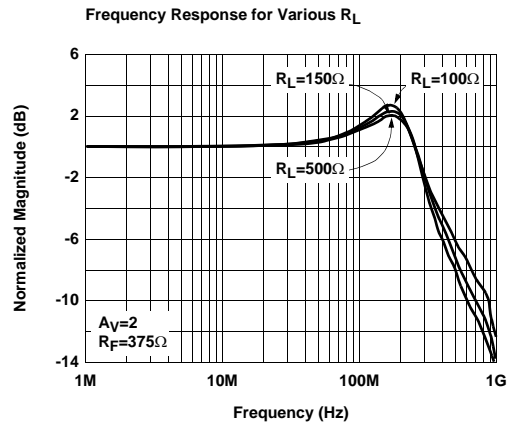
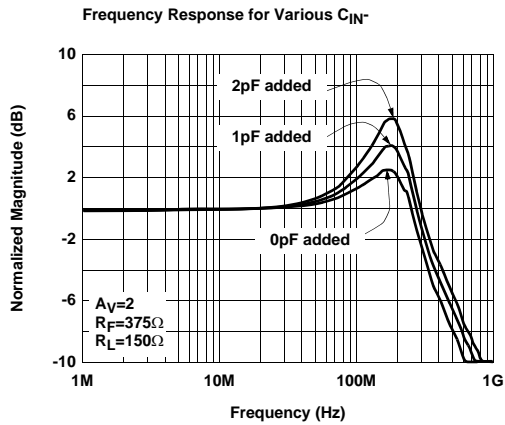
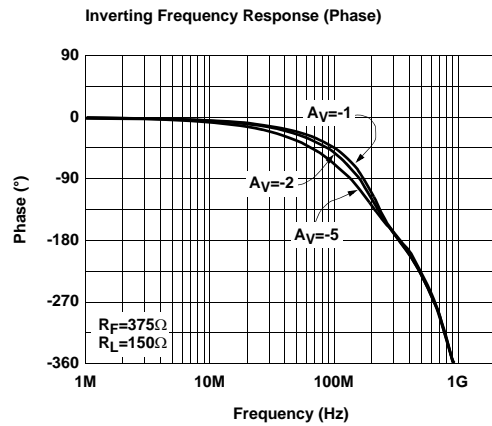
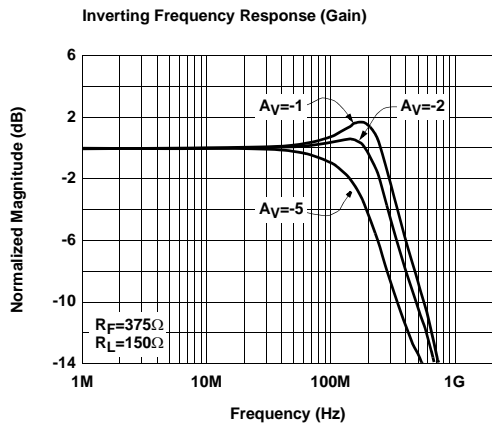
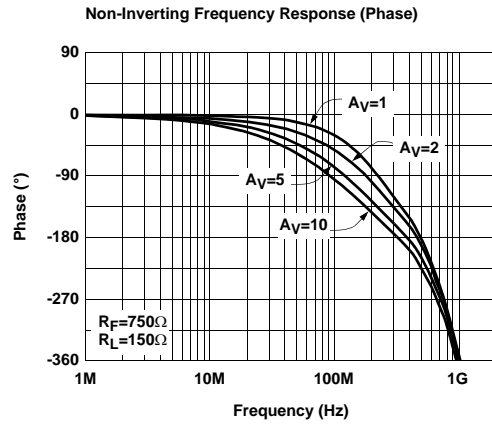
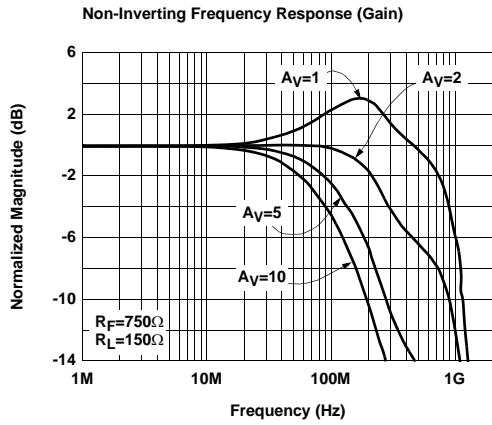
**Electrical Specifications**  $V_{S+} = +5V$ ,  $V_{S-} = -5V$ ,  $R_F = 750\Omega$  for  $A_V = 1$ ,  $R_F = 375\Omega$  for  $A_V = 2$ ,  $R_L = 150\Omega$ ,  $T_A = 25^\circ C$  unless otherwise specified. **(Continued)**

| PARAMETER     | DESCRIPTION                            | CONDITIONS                           | MIN          | TYP | MAX          | UNIT      |
|---------------|--|--------------------------------------|--------------|-----|--------------|-----------|
| PSRR          | Power Supply Rejection Ratio           | DC, $V_S = \pm 4.75V$ to $\pm 5.25V$ | 55           | 75  |              | dB        |
| -IPSR         | - Input Current Power Supply Rejection | DC, $V_S = \pm 4.75V$ to $\pm 5.25V$ | -2           |     | 2            | $\mu A/V$ |
| <b>ENABLE</b> |  |                                      |              |     |              |           |
| $t_{EN}$      | Enable Time                            |                                      |              | 40  |              | ns        |
| $t_{DIS}$     | Disable Time                           |                                      |              | 600 |              | ns        |
| $I_{IHCE}$    | CE Pin Input High Current              | $\overline{CE} = V_{S+}$             |              | 0.8 | 6            | $\mu A$   |
| $I_{ILCE}$    | CE Pin Input Low Current               | $\overline{CE} = V_{S-}$             |              | 0   | -0.1         | $\mu A$   |
| $V_{IHCE}$    | CE Input High Voltage for Power-down   |                                      | $V_{S+} - 1$ |     |              | V         |
| $V_{ILCE}$    | CE Input Low Voltage for Power-down    |                                      |              |     | $V_{S+} - 3$ | V         |

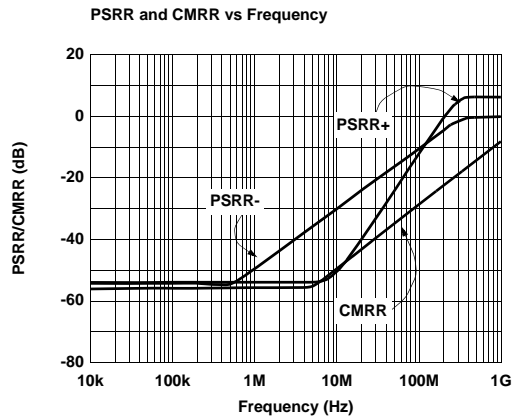
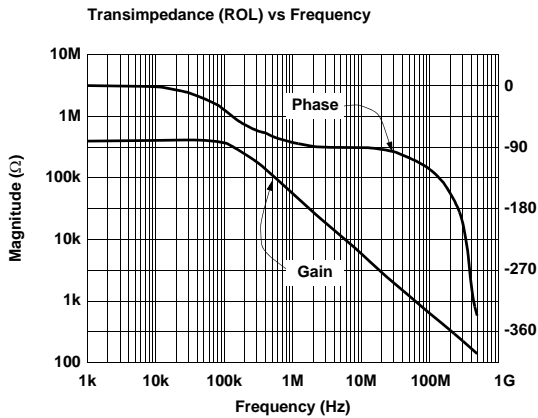
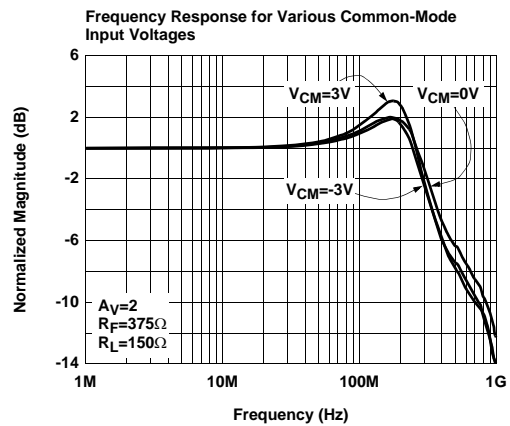
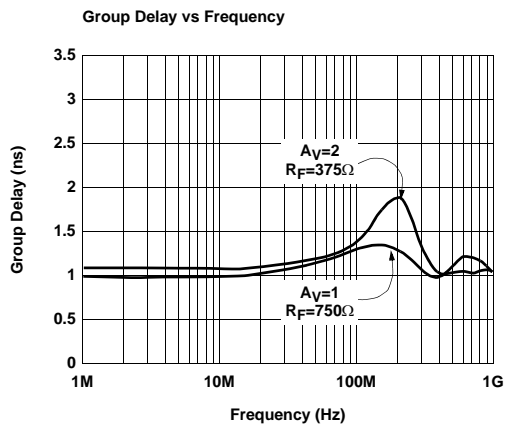
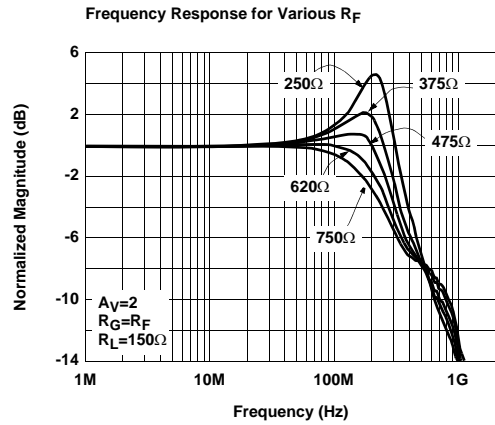
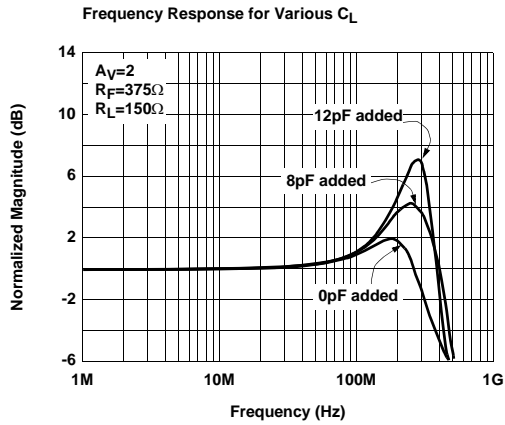
**NOTE:**

- Standard NTSC test, AC signal amplitude = 286mV<sub>p-p</sub>, f = 3.58MHz

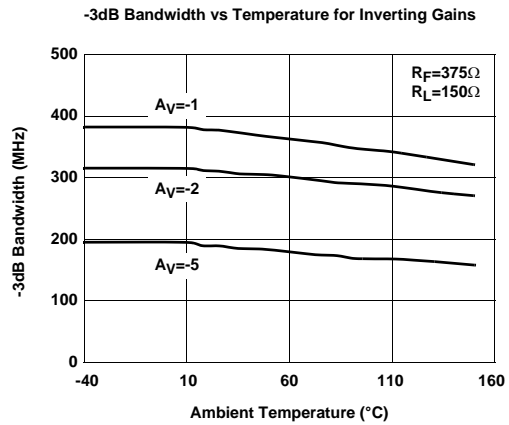
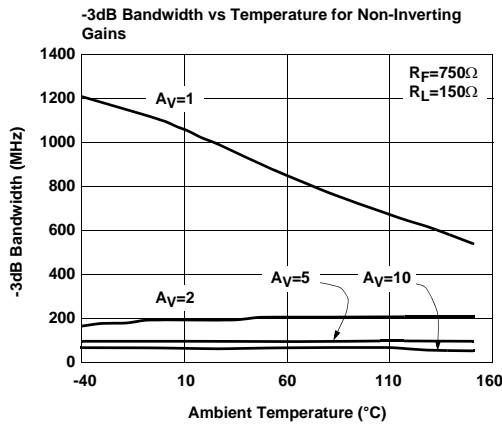
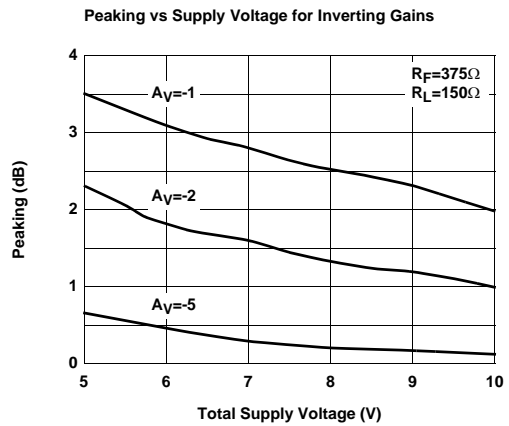
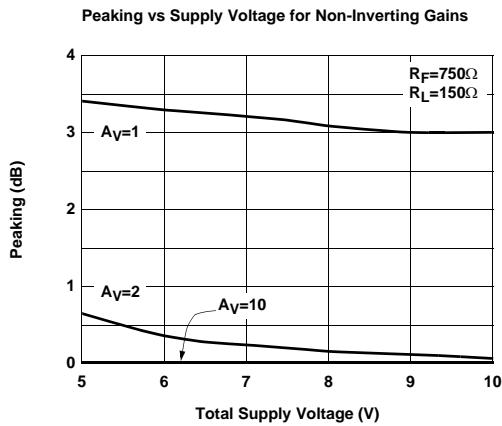
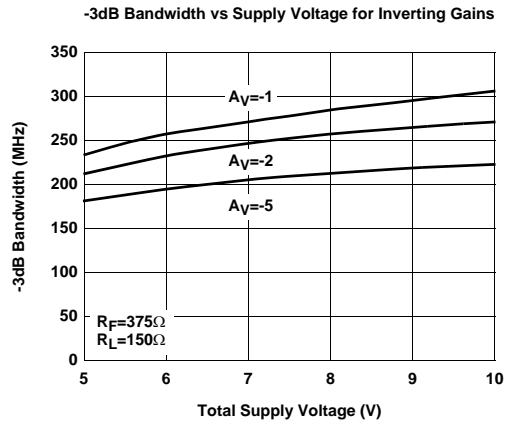
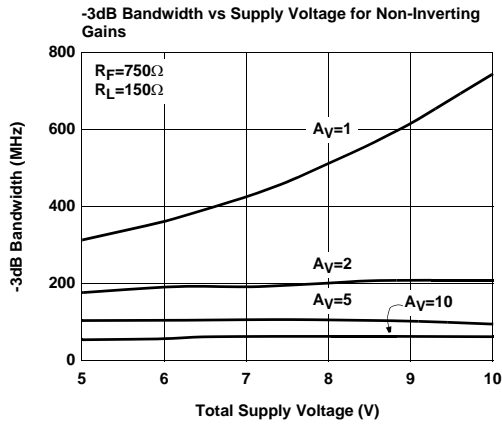
Typical Performance Curves



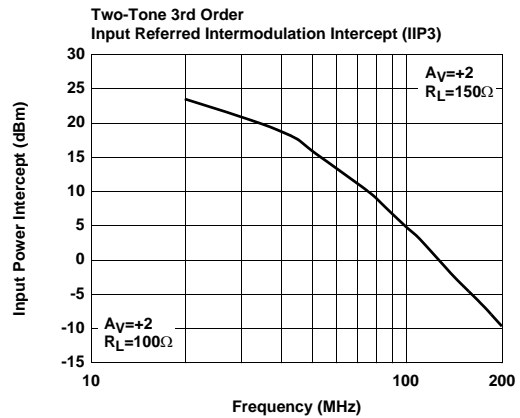
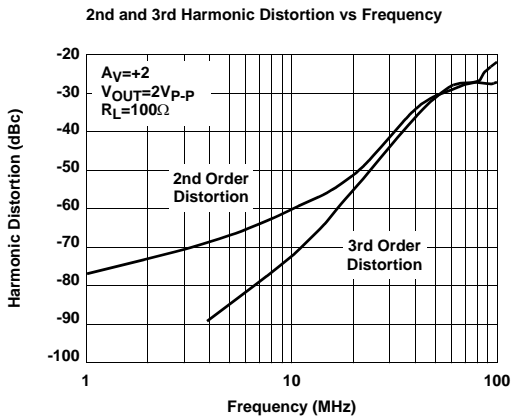
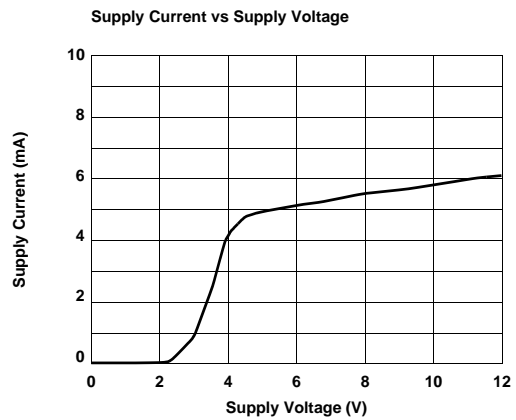
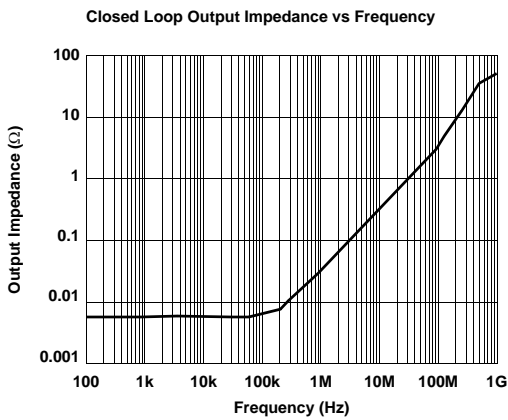
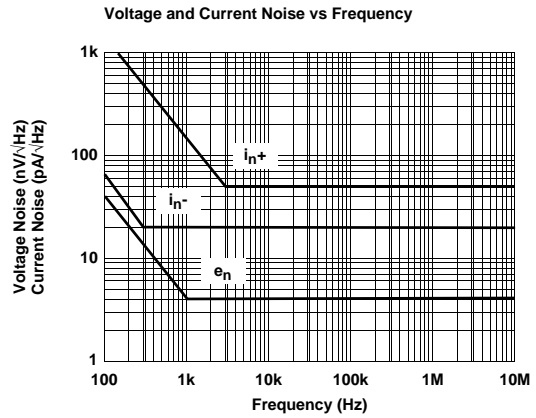
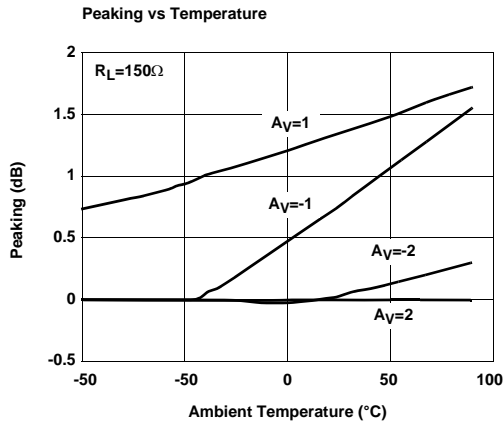
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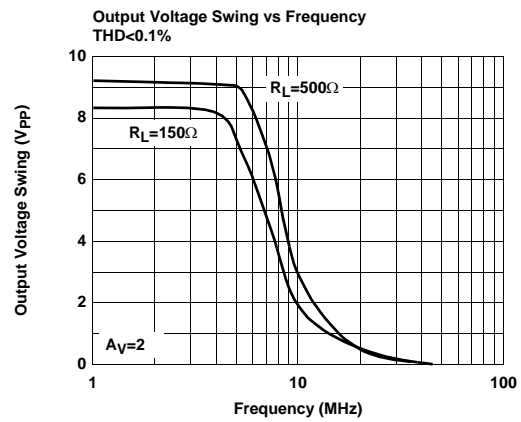
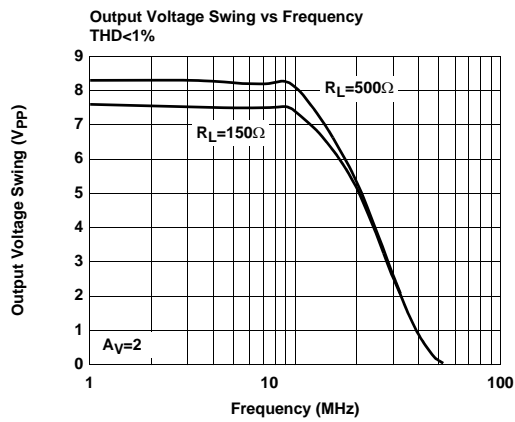
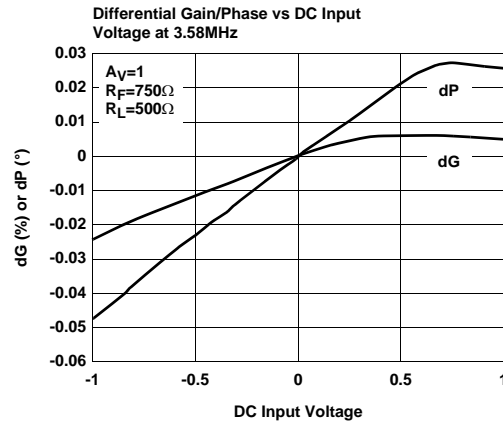
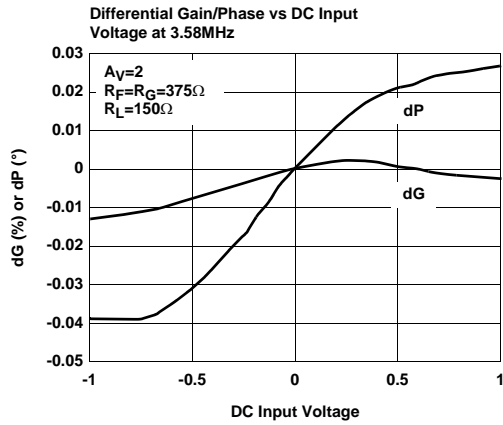
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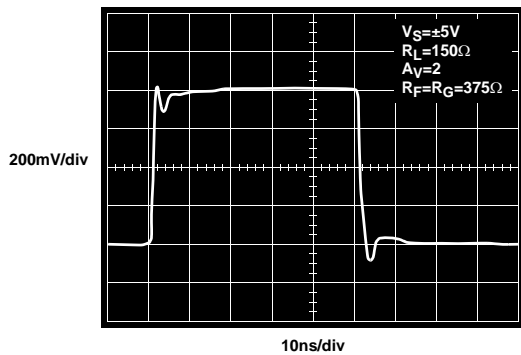
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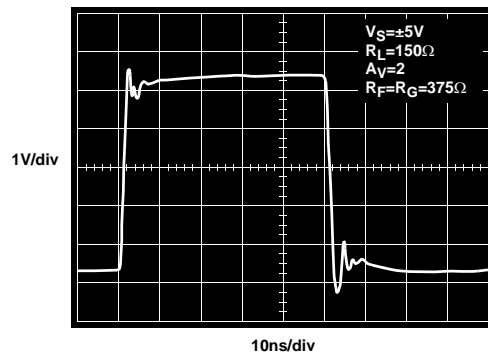
Typical Performance Curves (Continued)



Small Signal Step Response

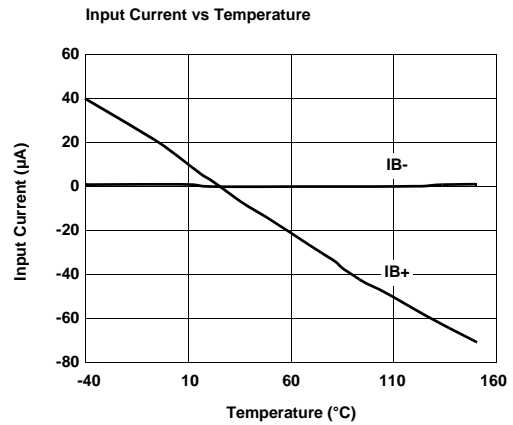
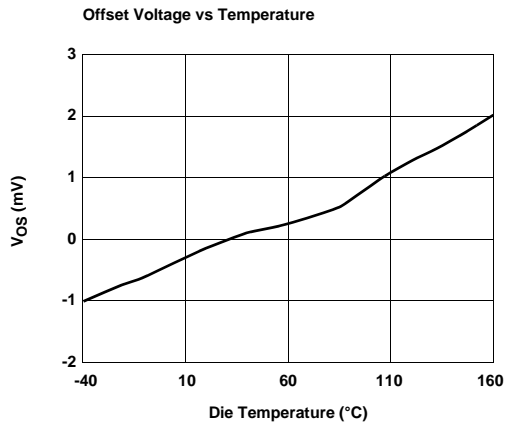
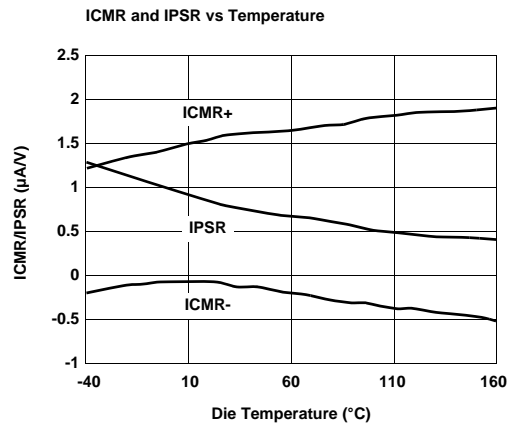
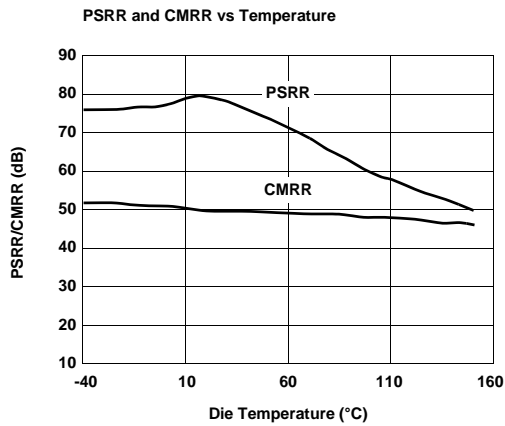
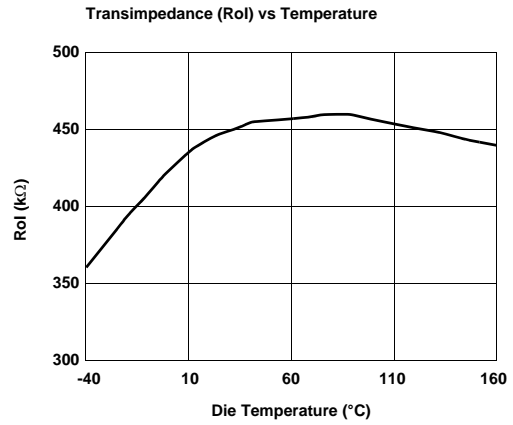
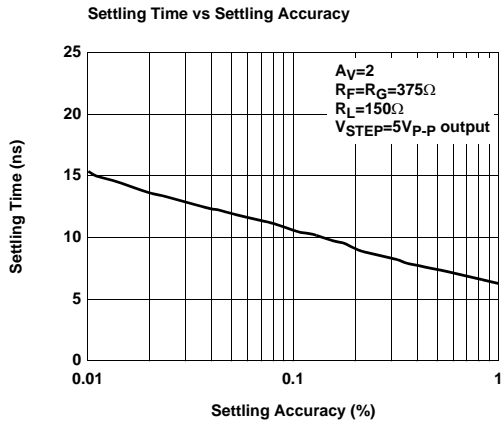


Large Signal Step Response

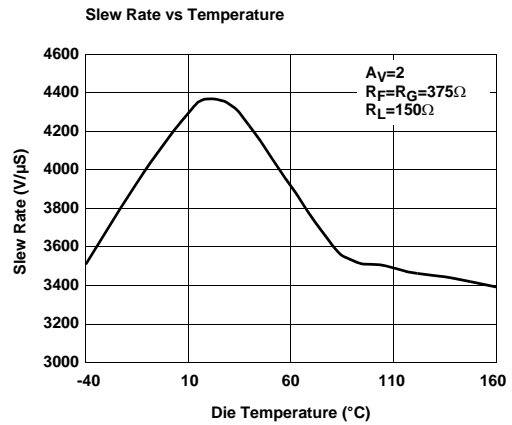
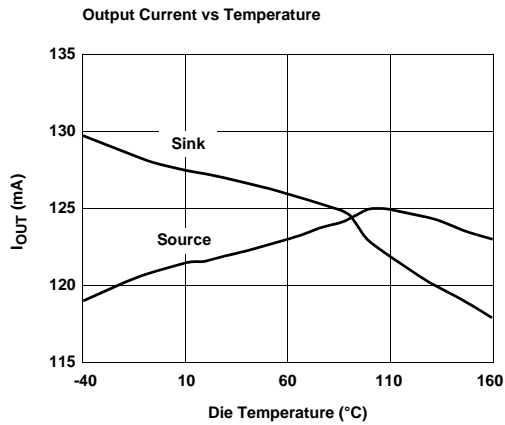
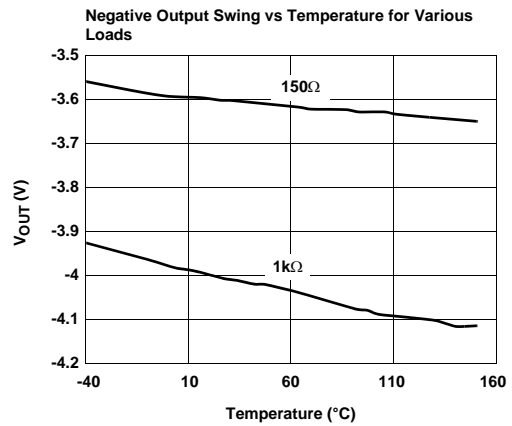
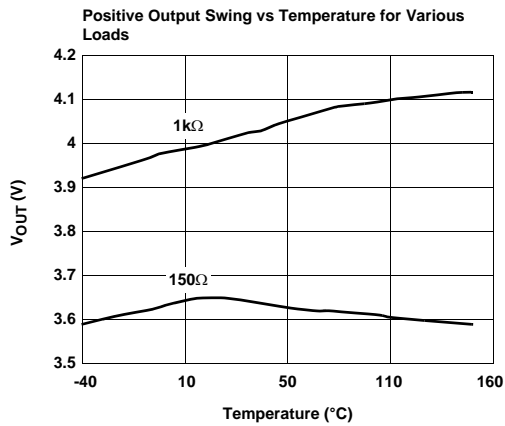
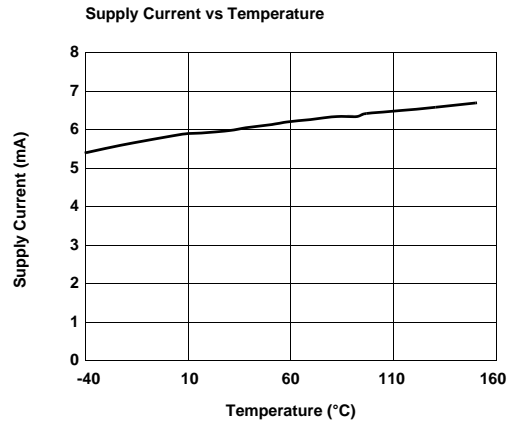
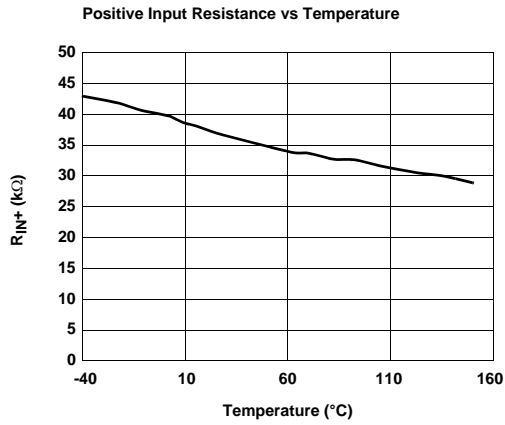




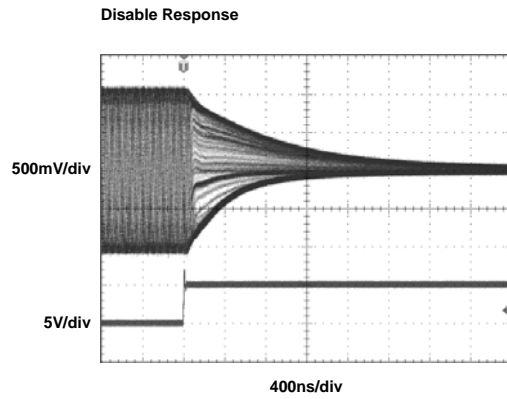
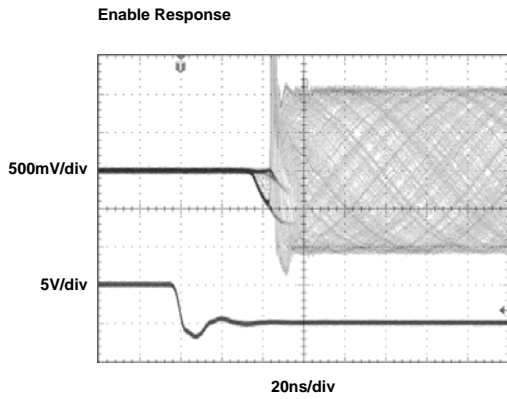
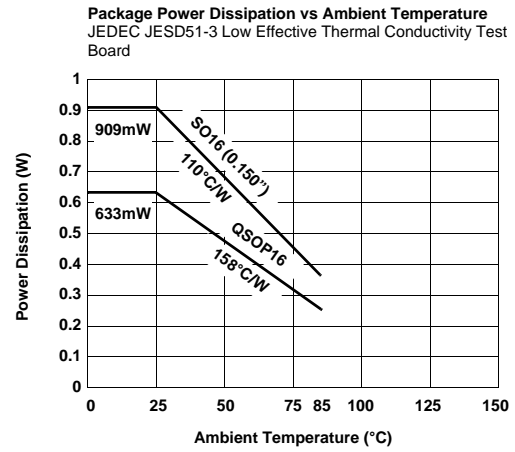
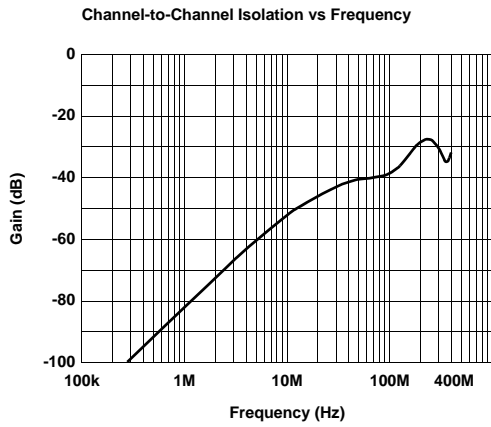
Typical Performance Curves (Continued)



Typical Performance Curves (Continued)



Typical Performance Curves (Continued)



**Pin Descriptions**

| 16 Ld SOIC<br>(0.150") | 16 Ld QSOP | Pin Name                | Function                       | Equivalent Circuit |
|------------------------|------------|-------------------------|--------------------------------|--------------------|
| 1                      | 1          | INA+                    | Non-inverting input, channel A | <p>Circuit 1</p>   |
| 2                      | 2          | $\overline{\text{CEA}}$ | Chip enable, channel A         | <p>Circuit 2</p>   |
| 3                      | 3          | VS-                     | Negative supply                |                    |
| 4                      | 4          | $\overline{\text{CEB}}$ | Chip enable, channel B         | (See circuit 2)    |
| 5                      | 5          | INB+                    | Non-inverting input, channel B | (See circuit 1)    |
| 6, 11                  | 6, 11      | NC                      | Not connected                  |                    |
| 7                      | 7          | $\overline{\text{CEC}}$ | Chip enable, channel C         | (See circuit 2)    |
| 8                      | 8          | INC+                    | Non-inverting input, channel C | (See circuit 1)    |
| 9                      | 9          | INC-                    | Inverting input, channel C     | (See circuit 1)    |
| 10                     | 10         | OUTC                    | Output, channel C              | <p>Circuit 3</p>   |
| 12                     | 12         | INB-                    | Inverting input, channel B     | (See circuit 1)    |
| 13                     | 13         | OUTB                    | Output, channel B              | (See circuit 3)    |
| 14                     | 14         | VS+                     | Positive supply                |                    |
| 15                     | 15         | OUTA                    | Output, channel A              | (See circuit 3)    |
| 16                     | 16         | INA-                    | Inverting input, channel A     | (See circuit 1)    |

**Applications Information**

**Product Description**

The EL5392A is a current-feedback operational amplifier that offers a wide -3dB bandwidth of 600MHz and a low supply current of 6mA per amplifier. The EL5392A works with supply voltages ranging from a single 5V to 10V and they are also capable of swinging to within 1V of either supply on the output. Because of their current-feedback topology, the EL5392A does not have the normal gain-bandwidth product associated with voltage-feedback operational amplifiers. Instead, its -3dB bandwidth to remain

relatively constant as closed-loop gain is increased. This combination of high bandwidth and low power, together with aggressive pricing make the EL5392A the ideal choice for many low-power/high-bandwidth applications such as portable, handheld, or battery-powered equipment.

For varying bandwidth needs, consider the EL5191 with 1GHz on a 9mA supply current or the EL5193 with 300MHz on a 4mA supply current. Versions include single, dual, and triple amp packages with 5 Ld SOT-23, 16 Ld QSOP, and 8 Ld or 16 Ld SOIC (0.150") outlines.

### **Power Supply Bypassing and Printed Circuit Board Layout**

As with any high frequency device, good printed circuit board layout is necessary for optimum performance. Low impedance ground plane construction is essential. Surface mount components are recommended, but if leaded components are used, lead lengths should be as short as possible. The power supply pins must be well bypassed to reduce the risk of oscillation. The combination of a 4.7 $\mu$ F tantalum capacitor in parallel with a 0.01 $\mu$ F capacitor has been shown to work well when placed at each supply pin.

For good AC performance, parasitic capacitance should be kept to a minimum, especially at the inverting input. (See the Capacitance at the Inverting Input section) Even when ground plane construction is used, it should be removed from the area near the inverting input to minimize any stray capacitance at that node. Carbon or Metal-Film resistors are acceptable with the Metal-Film resistors giving slightly less peaking and bandwidth because of additional series inductance. Use of sockets, particularly for the SOIC (0.150") package, should be avoided if possible. Sockets add parasitic inductance and capacitance which will result in additional peaking and overshoot.

### **Disable/Power-Down**

The EL5392A amplifier can be disabled placing its output in a high impedance state. When disabled, the amplifier supply current is reduced to < 450 $\mu$ A. The EL5392A is disabled when its  $\overline{\text{CE}}$  pin is pulled up to within 1V of the positive supply. Similarly, the amplifier is enabled by floating or pulling its  $\overline{\text{CE}}$  pin to at least 3V below the positive supply. For  $\pm 5$ V supply, this means that an EL5392A amplifier will be enabled when  $\overline{\text{CE}}$  is 2V or less, and disabled when  $\overline{\text{CE}}$  is above 4V. Although the logic levels are not standard TTL, this choice of logic voltages allows the EL5392A to be enabled by tying  $\overline{\text{CE}}$  to ground, even in 5V single supply applications. The  $\overline{\text{CE}}$  pin can be driven from CMOS outputs.

### **Capacitance at the Inverting Input**

Any manufacturer's high-speed voltage- or current-feedback amplifier can be affected by stray capacitance at the inverting input. For inverting gains, this parasitic capacitance has little effect because the inverting input is a virtual ground, but for non-inverting gains, this capacitance (in conjunction with the feedback and gain resistors) creates a pole in the feedback path of the amplifier. This pole, if low enough in frequency, has the same destabilizing effect as a zero in the forward open-loop response. The use of large-value feedback and gain resistors exacerbates the problem by further lowering the pole frequency (increasing the possibility of oscillation.)

The EL5392A has been optimized with a 375 $\Omega$  feedback resistor. With the high bandwidth of these amplifiers, these resistor values might cause stability problems when combined with parasitic capacitance, thus ground plane is

not recommended around the inverting input pin of the amplifier.

### **Feedback Resistor Values**

The EL5392A has been designed and specified at a gain of +2 with  $R_F$  approximately 375 $\Omega$ . This value of feedback resistor gives 300MHz of -3dB bandwidth at  $A_V=2$  with 2dB of peaking. With  $A_V=-2$ , an  $R_F$  of 375 $\Omega$  gives 275MHz of bandwidth with 1dB of peaking. Since the EL5392A is a current-feedback amplifier, it is also possible to change the value of  $R_F$  to get more bandwidth. As seen in the curve of Frequency Response for Various  $R_F$  and  $R_G$ , bandwidth and peaking can be easily modified by varying the value of the feedback resistor.

Because the EL5392A is a current-feedback amplifier, its gain-bandwidth product is not a constant for different closed-loop gains. This feature actually allows the EL5392A to maintain about the same -3dB bandwidth. As gain is increased, bandwidth decreases slightly while stability increases. Since the loop stability is improving with higher closed-loop gains, it becomes possible to reduce the value of  $R_F$  below the specified 375 $\Omega$  and still retain stability, resulting in only a slight loss of bandwidth with increased closed-loop gain.

### **Supply Voltage Range and Single-Supply Operation**

The EL5392A has been designed to operate with supply voltages having a span of greater than 5V and less than 10V. In practical terms, this means that the EL5392A will operate on dual supplies ranging from  $\pm 2.5$ V to  $\pm 5$ V. With single-supply, the EL5392A will operate from 5V to 10V.

As supply voltages continue to decrease, it becomes necessary to provide input and output voltage ranges that can get as close as possible to the supply voltages. The EL5392A has an input range which extends to within 2V of either supply. So, for example, on  $\pm 5$ V supplies, the EL5392A has an input range which spans  $\pm 3$ V. The output range of the EL5392A is also quite large, extending to within 1V of the supply rail. On a  $\pm 5$ V supply, the output is therefore capable of swinging from -4V to +4V. Single-supply output range is larger because of the increased negative swing due to the external pull-down resistor to ground.

### **Video Performance**

For good video performance, an amplifier is required to maintain the same output impedance and the same frequency response as DC levels are changed at the output. This is especially difficult when driving a standard video load of 150 $\Omega$ , because of the change in output current with DC level. Previously, good differential gain could only be achieved by running high idle currents through the output transistors (to reduce variations in output impedance.) These currents were typically comparable to the entire 6mA supply current of each EL5392A amplifier. Special circuitry has been incorporated in the EL5392A to reduce the

variation of output impedance with current output. This results in dG and dP specifications of 0.015% and 0.04°, while driving 150Ω at a gain of 2.

Video performance has also been measured with a 500Ω load at a gain of +1. Under these conditions, the EL5392A has dG and dP specifications of 0.03% and 0.05°, respectively.

### **Output Drive Capability**

In spite of its low 6mA of supply current, the EL5392A is capable of providing a minimum of ±95mA of output current. With a minimum of ±95mA of output drive, the EL5392A is capable of driving 50Ω loads to both rails, making it an excellent choice for driving isolation transformers in telecommunications applications.

### **Driving Cables and Capacitive Loads**

When used as a cable driver, double termination is always recommended for reflection-free performance. For those applications, the back-termination series resistor will decouple the EL5392A from the cable and allow extensive capacitive drive. However, other applications may have high capacitive loads without a back-termination resistor. In these applications, a small series resistor (usually between 5Ω and 50Ω) can be placed in series with the output to eliminate most peaking. The gain resistor ( $R_G$ ) can then be chosen to make up for any gain loss which may be created by this additional resistor at the output. In many cases it is also possible to simply increase the value of the feedback resistor ( $R_F$ ) to reduce the peaking.

### **Current Limiting**

The EL5392A has no internal current-limiting circuitry. If the output is shorted, it is possible to exceed the Absolute Maximum Rating for output current or power dissipation, potentially resulting in the destruction of the device.

### **Power Dissipation**

With the high output drive capability of the EL5392A, it is possible to exceed the 125°C Absolute Maximum junction temperature under certain very high load current conditions. Generally speaking when  $R_L$  falls below about 25Ω, it is important to calculate the maximum junction temperature ( $T_{JMAX}$ ) for the application to determine if power supply voltages, load conditions, or package type need to be

modified for the EL5392A to remain in the safe operating area. These parameters are calculated as follows:

$$T_{JMAX} = T_{MAX} + (\theta_{JA} \times n \times PD_{MAX})$$

where:

$T_{MAX}$  = Maximum ambient temperature

$\theta_{JA}$  = Thermal resistance of the package

n = Number of amplifiers in the package

$PD_{MAX}$  = Maximum power dissipation of each amplifier in the package

$PD_{MAX}$  for each amplifier can be calculated as follows:

$$PD_{MAX} = (2 \times V_S \times I_{SMAX}) + \left[ (V_S - V_{OUTMAX}) \times \frac{V_{OUTMAX}}{R_L} \right]$$

where:

$V_S$  = Supply voltage

$I_{SMAX}$  = Maximum supply current of 1A

$V_{OUTMAX}$  = Maximum output voltage (required)

$R_L$  = Load resistance

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