30 nA



LM148/LM149 Series Quad 741 Op Amp

LM148/LM248/LM348 Quad 741 Op Amps LM149/LM349 Wide Band Decompensated ($A_{V \text{ (MIN)}} = 5$)

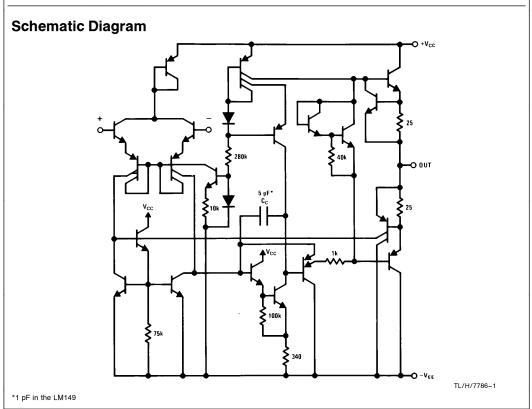
General Description

The LM148 series is a true quad 741. It consists of four independent, high gain, internally compensated, low power operational amplifiers which have been designed to provide functional characteristics identical to those of the familiar 741 operational amplifier. In addition the total supply current for all four amplifiers is comparable to the supply current of a single 741 type op amp. Other features include input offset currents and input bias current which are much less than those of a standard 741. Also, excellent isolation between amplifiers has been achieved by independently biasing each amplifier and using layout techniques which minimize thermal coupling. The LM149 series has the same features as the LM148 plus a gain bandwidth product of 4 MHz at a gain of 5 or greater.

The LM148 can be used anywhere multiple 741 or 1558 type amplifiers are being used and in applications where amplifier matching or high packing density is required.

Features

- 741 op amp operating characteristics
- Low supply current drain 0.6 mA/Amplifier
- Class AB output stage—no crossover distortion
- Pin compatible with the LM124
- Low input offset voltage 1 mV ■ Low input offset current 4 nA
- Low input bias currentGain bandwidth product
- LM148 (unity gain) 1.0 MHz LM149 (A $_{V} \ge 5$) 4 MHz
- LM149 (A $_{
 m V} \ge$ 5) 4 MHz • High degree of isolation between amplifiers 120 dB
- Overload protection for inputs and outputs



© 1995 National Semiconductor Corporation TL/H/778

RRD-B30M115/Printed in U. S. A.

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. (Note 4)

	LM148/LM149	LM248	LM348/LM349		
Supply Voltage	±22V	±18V	\pm 18V		
Differential Input Voltage	±44V	$\pm36V$	$\pm 36V$		
Output Short Circuit Duration (Note 1)	Continuous	Continuous	Continuous		
Power Dissipation (P _d at 25°C) and					
Thermal Resistance (θ_{jA}), (Note 2)					
Molded DIP (N) P _d	_	_	750 mW		
$ heta_{jA}$	_	_	100°C/W		
Cavity DIP (J) P _d	1100 mW	800 mW	700 mW		
$ heta_{\sf JA}$	110°C/W	110°C/W	110°C/W		
Maximum Junction Temperature (T _{jMAX})	150°C	110°C	100°C		
Operating Temperature Range	$-55^{\circ}C \leq T_{\mbox{\scriptsize A}} \leq +125^{\circ}C$	$-25^{\circ}\text{C} \leq \text{T}_{\text{A}} \leq +85^{\circ}\text{C}$	$0^{\circ}C \leq T_{A} \leq +70^{\circ}C$		
Storage Temperature Range	$-65^{\circ}\text{C to } + 150^{\circ}\text{C}$	-65°C to $+150$ °C	-65° C to $+150^{\circ}$ C		
Lead Temperature (Soldering, 10 sec.) Ceramic	300°C	300°C	300°C		
Lead Temperature (Soldering, 10 sec.) Plastic			260°C		
Soldering Information					
Dual-In-Line Package					
Soldering (10 seconds)	260°C	260°C	260°C		
Small Outline Package					
Vapor Phase (60 seconds)	215°C	215°C	215°C		
Infrared (15 seconds)	220°C	220°C	220°C		
See AN-450 "Surface Mounting Methods and The devices.	eir Effect on Product Reliabilit	y" for other methods of solo	dering surface mount		

ESD tolerance (Note 5) 500V 500V

Electrical Characteristics (Note 3)

Parameter	Conditions	LM148/LM149			LM248			LM348/LM349			Units
raiailletei		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Oilits
Input Offset Voltage	$T_A = 25^{\circ}C, R_S \le 10 \text{ k}\Omega$		1.0	5.0		1.0	6.0		1.0	6.0	mV
Input Offset Current	$T_A = 25^{\circ}C$		4	25		4	50		4	50	nA
Input Bias Current	$T_A = 25^{\circ}C$		30	100		30	200		30	200	nA
Input Resistance	$T_A = 25^{\circ}C$	0.8	2.5		0.8	2.5		0.8	2.5		$M\Omega$
Supply Current All Amplifiers	$T_A = 25^{\circ}C, V_S = \pm 15V$		2.4	3.6		2.4	4.5		2.4	4.5	mA
Large Signal Voltage Gain	$T_A = 25^{\circ}\text{C}, V_S = \pm 15\text{V}$ $V_{OUT} = \pm 10\text{V}, R_L \ge 2 \text{ k}\Omega$	50	160		25	160		25	160		V/mV
Amplifier to Amplifier Coupling	T _A = 25°C, f = 1 Hz to 20 kHz (Input Referred) See Crosstalk Test Circuit		-120			-120			-120		dB
Small Signal Bandwidth	LM148 Series		1.0			1.0			1.0		MHz
	LM149 Series		4.0			4.0			4.0		MHz
Phase Margin	LM148 Series ($A_V = 1$) $T_A = 25$ °C		60			60			60		degrees
	LM149 Series (A _V = 5)		60			60			60		degrees
Slew Rate	LM148 Series (A _V = 1) $T_A = 25$ °C		0.5			0.5			0.5		V/μs
	LM149 Series (A _V = 5)		2.0			2.0			2.0		V/μs
Output Short Circuit Current	$T_A = 25^{\circ}C$		25			25			25		mA
Input Offset Voltage	$R_S \le 10 \text{ k}\Omega$			6.0			7.5			7.5	mV
Input Offset Current				75			125			100	nA
Input Bias Current				325			500			400	nA

Electrical Characteristics (Note 3) (Continued)

Parameter	Conditions	LM148/LM149			LM248			LM348/LM349			Units
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Large Signal Voltage Gain	$V_S = \pm 15V, V_{OUT} = \pm 10V,$ $R_L > 2 k\Omega$	25			15			15			V/mV
Output Voltage Swing	$V_S = \pm 15V, R_L = 10 \text{ k}\Omega$ $R_L = 2 \text{ k}\Omega$	±12 ±10	±13 ±12		±12 ±10	±13 ±12		±12 ±10	±13 ±12		> >
Input Voltage Range	$V_S = \pm 15V$	±12			±12			±12			V
Common-Mode Rejection Ratio	$R_S \le 10 \text{ k}\Omega$	70	90		70	90		70	90		dB
Supply Voltage Rejection	$R_S \le 10 \text{ k}\Omega, \pm 5V \le V_S \le \pm 15V$	77	96		77	96		77	96		dB

Note 1: Any of the amplifier outputs can be shorted to ground indefinitely; however, more than one should not be simultaneously shorted as the maximum junction temperature will be exceeded.

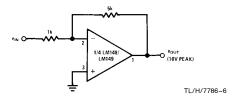
Note 2: The maximum power dissipation for these devices must be derated at elevated temperatures and is dicated by T_{jMAX} , θ_{jA} , and the ambient temperature, T_A . The maximum available power dissipation at any temperature is $P_d = (T_{jMAX} - T_A)/\theta_{jA}$ or the 25°C P_{dMAX} , whichever is less.

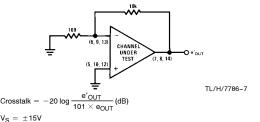
Note 3: These specifications apply for $V_S=\pm 15V$ and over the absolute maximum operating temperature range $(T_L \le T_A \le T_H)$ unless otherwise noted.

Note 4: Refer to RETS 148X for LM148 military specifications and refer to RETS 149X for LM149 military specifications.

Note 5: Human body model, 1.5 $k\Omega$ in series with 100 pF.

Cross Talk Test Circuit





Application Hints

The LM148 series are quad low power 741 op amps. In the proliferation of quad op amps, these are the first to offer the convenience of familiar, easy to use operating characteristics of the 741 op amp. In those applications where 741 op amps have been employed, the LM148 series op amps can be employed directly with no change in circuit performance.

The LM149 series has the same characteristics as the LM148 except it has been decompensated to provide a wider bandwidth. As a result the part requires a minimum gain of 5.

The package pin-outs are such that the inverting input of each amplifier is adjacent to its output. In addition, the amplifier outputs are located in the corners of the package which simplifies PC board layout and minimizes package related capacitive coupling between amplifiers.

The input characteristics of these amplifiers allow differential input voltages which can exceed the supply voltages. In addition, if either of the input voltages is within the operating common-mode range, the phase of the output remains correct. If the negative limit of the operating common-mode range is exceeded at both inputs, the output voltage will be positive. For input voltages which greatly exceed the maximum supply voltages, either differentially or common-mode, resistors should be placed in series with the inputs to limit the current.

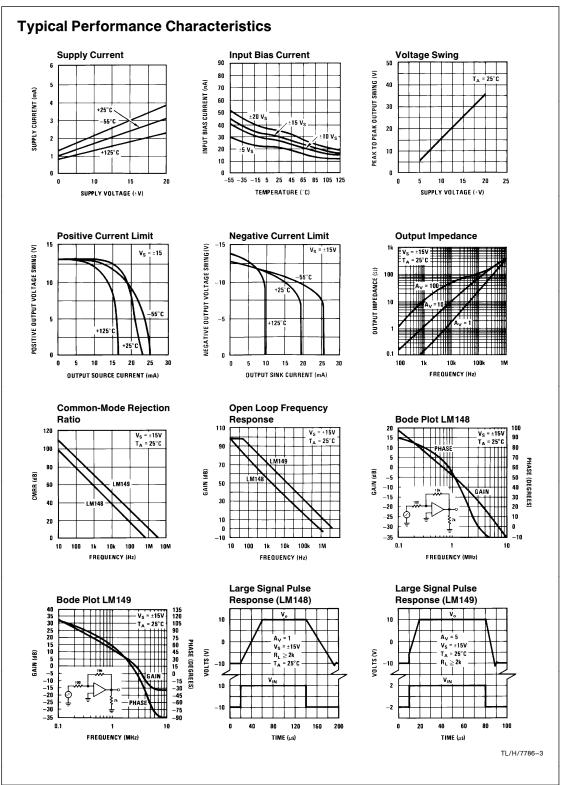
Like the LM741, these amplifiers can easily drive a 100 pF capacitive load throughout the entire dynamic output voltage and current range. However, if very large capacitive loads must be driven by a non-inverting unity gain amplifier,

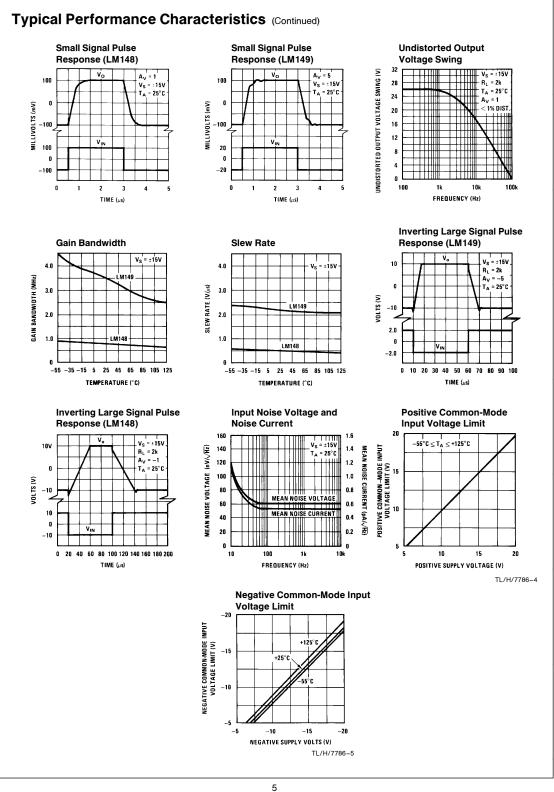
a resistor should be placed between the output (and feed-back connection) and the capacitance to reduce the phase shift resulting from the capacitive loading.

The output current of each amplifier in the package is limited. Short circuits from an output to either ground or the power supplies will not destroy the unit. However, if multiple output shorts occur simultaneously, the time duration should be short to prevent the unit from being destroyed as a result of excessive power dissipation in the IC chip.

As with most amplifiers, care should be taken lead dress, component placement and supply decoupling in order to ensure stability. For example, resistors from the output to an input should be placed with the body close to the input to minimize "pickup" and maximize the frequency of the feedback pole which capacitance from the input to ground creates.

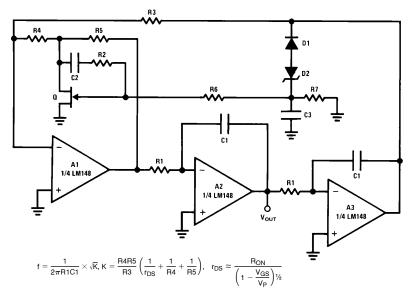
A feedback pole is created when the feedback around any amplifier is resistive. The parallel resistance and capacitance from the input of the device (usually the inverting input) to AC ground set the frequency of the pole. In many instances the frequency of this pole is much greater than the expected 3 dB frequency of the closed loop gain and consequently there is negligible effect on stability margin. However, if the feedback pole is less than approximately six times the expected 3 dB frequency a lead capacitor should be placed from the output to the input of the op amp. The value of the added capacitor should be such that the RC time constant of this capacitor and the resistance it parallels is greater than or equal to the original feedback pole time constant.





Typical Applications—LM148

One Decade Low Distortion Sinewave Generator



 $f_{\mbox{\scriptsize MAX}}\,=\,5$ kHz, THD $\leq\,0.03\%$

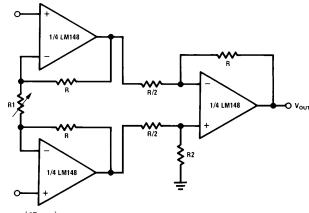
R1 = 100k pot. C1 = 0.0047 $\mu\text{F},$ C2 = 0.01 $\mu\text{F},$ C3 = 0.1 $\mu\text{F},$ R2 = R6 = R7 = 1M,

R3 = 5.1k, R4 = 12 Ω , R5 = 240 Ω , Q = NS5102, D1 = 1N914, D2 = 3.6V avalanche

diode (ex. LM103), $V_S = \pm 15 V$

A simpler version with some distortion degradation at high frequencies can be made by using A1 as a simple inverting amplifier, and by putting back to back zeners in the feedback loop of A3.

Low Cost Instrumentation Amplifier



 $V_{OUT} = 2 \bigg(\frac{2R}{R1} + 1 \bigg) \text{ , } V_{\overline{S}} - 3V \leq V_{IN \text{ CM}} \leq {V_{\overline{S}}}^+ - 3V \text{,}$

TL/H/7786-9

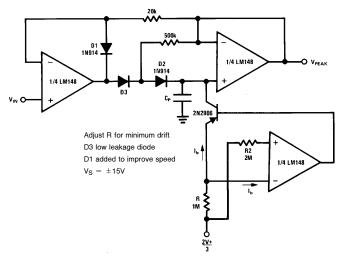
TL/H/7786-8

 $V_S = \pm 15V$

R = R2, trim R2 to boost CMRR

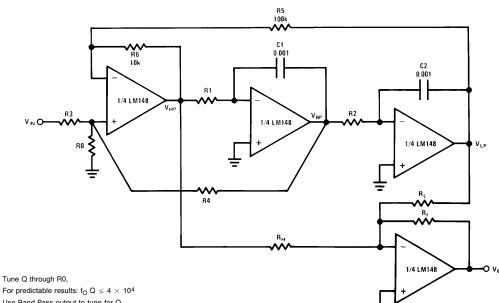
Typical Applications—LM148 (Continued)

Low Drift Peak Detector with Bias Current Compensation



TL/H/7786-10

Universal State-Variable Filter



Tune Q through R0,

Use Band Pass output to tune for Q

$$\frac{V_{(s)}}{V_{IN(s)}} = \frac{N_{(s)}}{D_{(s)}}, \; D(s) = S^2 + \frac{S\omega_0}{Q} + \, \omega_0^2$$

TL/H/7786-11

$$N_{HP(s)} = \, S^2 \, H_{OHP}, \, \, N_{BP(s)} = \frac{-s\omega_O \, H_{OBP}}{Q} \quad N_{LP} = \, \omega_o^2 \, H_{OLP}$$

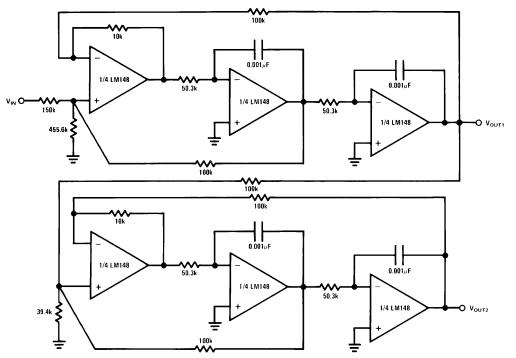
$$f_0 = \frac{1}{2\pi} \sqrt{\frac{R6}{R5}} \sqrt{\frac{1}{t1t2}} \,, t_i = R_i C_i \,, \; Q = \left(\frac{1 \, + \, R4 |R3 \, + \, R4 |R0}{1 \, + \, R6 |R5}\right) \left(\frac{R6 \, t_1}{R5 \, t_2}\right)^{5}$$

$$\begin{split} \frac{V_{(S)}}{V_{|N(S)}} &= \frac{N_{(S)}}{D_{(S)}}, \ D(S) = S^2 + \frac{S\omega_0}{Q} + \omega_0^2 \\ N_{HP(S)} &= S^2 H_{OHP}, \ N_{BP(S)} = \frac{-s\omega_0 \, H_{OBP}}{Q} \quad N_{LP} = \omega_0^2 \, H_{OLP}, \\ f_0 &= \frac{1}{2\pi} \sqrt{\frac{R6}{R5}} \sqrt{\frac{1}{1112}}, \ t_i = R_i C_i, \ Q = \left(\frac{1 + R4|R3 + R4|R0}{1 + R6|R5}\right) \left(\frac{R6}{R5} \frac{t_1}{t_2}\right)^{1/2} \\ f_{NOTCH} &= \frac{1}{2\pi} \left(\frac{R_H}{R_1 t_1 t_2}\right)^{1/2}, \ H_{OHP} = \frac{1 + R6|R5}{1 + R3|R0 + R3|R4}, H_{OBP} = \frac{1 + R4|R3 + R4|R0}{1 + R3|R0 + R3|R4} \\ H_{OLP} &= \frac{1 + R5|R6}{1 + R3|R0 + R3|R4} \end{split}$$

$$H_{OLP} = \frac{1 + R5|R6}{1 + R3|R0 + R3|R4}$$

Typical Applications—LM148 (Continued)

A 1 kHz 4 Pole Butterworth



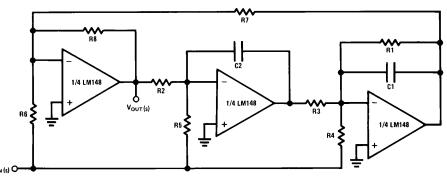
TL/H/7786-12

Use general equations, and tune each section separately

 $Q_{1stSECTION} = 0.541, Q_{2ndSECTION} = 1.306$

The response should have 0 dB peaking

A 3 Amplifier Bi-Quad Notch Filter



TL/H/7786-13

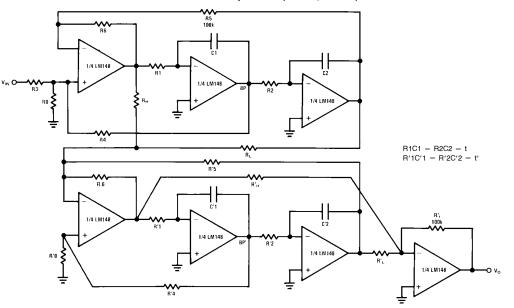
$$Q = \sqrt{\frac{RB}{R7}} \times \frac{R1C1}{\sqrt{R3C2R2C1}}, \ \ f_0 = \frac{1}{2\pi}\sqrt{\frac{RB}{R7}} \times \frac{1}{\sqrt{R2R3C1C2}}, \ \ f_{NOTCH} = \frac{1}{2\pi}\sqrt{\frac{R6}{R3R5R7C1C2}}$$

Necessary condition for notch: $\frac{1}{R6} = \frac{R1}{R4R7}$

Ex: $f_{NOTCH}=3$ kHz, Q=5, R1=270k, R2=R3=20k, R4=27k, R5=20k, R6=R8=10k, R7=100k, C1=C2=0.001 μF Better noise performance than the state-space approach.

Typical Applications—LM148 (Continued)

A 4th Order 1 kHz Elliptic Filter (4 Poles, 4 Zeros)



TL/H/7786-14

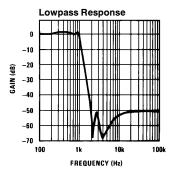
$$\begin{aligned} &f_C = 1 \text{ kHz, } f_S = 2 \text{ kHz, } f_p = 0.543, \ f_Z = 2.14, \ Q = 0.841, \ f'_P = 0.987, \ f'_Z = 4.92, \ Q' = 4.403, \text{normalized to ripple BW} \\ &f_P = \frac{1}{2\pi} \sqrt{\frac{R6}{R5}} \times \frac{1}{t}, \ f_Z = \frac{1}{2\pi} \sqrt{\frac{R_H}{R_L}} \times \frac{1}{t}, \ Q = \left(\frac{1 + R4|R3 + R4|R0}{1 + R6|R5}\right) \times \sqrt{\frac{R6}{R5}}, \ Q' = \sqrt{\frac{R'6}{R5}} \frac{1 + R'4|R'0}{1 + R'6|R'5 + R'6|R_P} \end{aligned}$$

$$R_P = \frac{R_H R_L}{R_H + R_L}$$

Use the BP outputs to tune Q, Q', tune the 2 sections separately

 $R1 \, = \, R2 \, = \, 92.6k, \ R3 \, = \, R4 \, = \, R5 \, = \, 100k, \ R6 \, = \, 10k, \ R0 \, = \, 107.8k, \ R_L \, = \, 100k, \ R_H \, = \, 155.1k,$

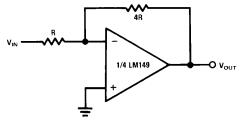
 $R'1 = R'2 = 50.9k, \; R'4 = R'5 = 100k, \; R'6 = 10k, \; R'0 = 5.78k, \; R'_L = 100k, \; R'_H = 248.12k, \; R'f = 100k. \; All \; capacitors are 0.001 \; \mu F. \; All \; Capacitors are 0.001 \; All \; Capacitors are 0$



TL/H/7786-15

Typical Applications—LM149

Minimum Gain to Insure LM149 Stability



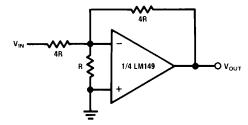
TL/H/7786-16

$$A_{CL(S)} = \frac{V_{OUT}}{V_{IN}} = \frac{-4}{\left(1 + \frac{5}{A_{CL(S)}}\right)} \simeq -4$$

$$V_0 \bigg|_{V_{INI} = 0} \approx \pm 5 V_{OS}$$

Small Signal BW = G BW/5

The LM149 as a Unity Gain Inverter



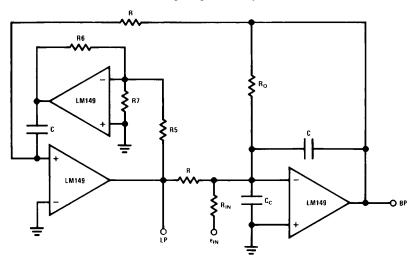
TL/H/7786-17

$$A_{CL(s)} = \frac{V_{OUT}}{V_{IN}} = \left(\frac{-1}{1 + \frac{6}{A_{OL(s)}}}\right) \approx -1$$

$$V_{O}$$
 $V_{IN} = 0$
 $\approx \pm 5 V_{OS}$

Small Signal BW = G BW/5

Non-inverting-Integrator Bandpass Filter



TL/H/7786-18

For stability purposes: R7 = R6/4, 10R6 = R5,
$$C_C = 10C$$

 $f_O = \frac{1}{2\pi}\sqrt{\frac{R5}{R6}} \times \frac{1}{RC}$, $Q = \frac{R_Q}{R}\sqrt{\frac{R5}{R6}}$, $Ho_{BP} = \frac{R_Q}{R_{IN}}$

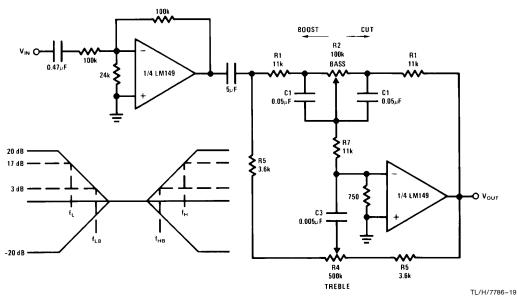
 $f_{O(MAX)}$, $Q_{MAX} = 20$ kHz, 10

Better Q sensitivity with respect to open loop gain variations than the state variable filter.

R7, C_C added for compensation



Active Tone Control with Full Output Swing (No Slew Limiting at 20 kHz)



$$\begin{split} &\text{V}_{\text{S}}=~\pm15\text{V},~\text{V}_{\text{OUT}(\text{MAX})}=~9.1~\text{V}_{\text{RMS}},\\ &\text{f}_{\text{MAX}}=~20~\text{kHz},~\text{THD}~\leq~1\%\\ &\text{Duplicate the above circuit for stereo} \end{split}$$

$$f_L = \frac{1}{2\pi R2C1}, \ f_{LB} = \frac{1}{2\pi R1C1}$$

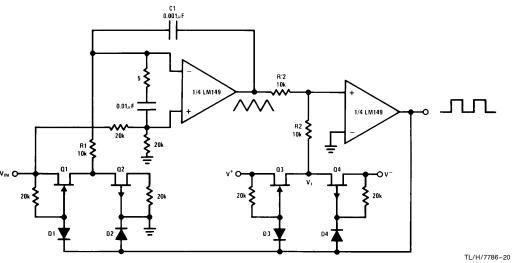
$${\rm f_H} = \frac{1}{2\pi {\rm R5C3}} \,, \,\, {\rm f_{HB}} = \frac{1}{2\pi {\rm (R1\,+\,2R7)\,C3}} \,$$

Max Bass Gain ≃ (R1 + R2)/R1

Max Treble Gain \simeq (R1 + 2R7)/R5

as shown: fL \simeq 32 Hz, fLB \simeq 320 Hz fH \simeq 11 kHz, fHB \simeq 1.1 Hz

Triangular Squarewave Generator

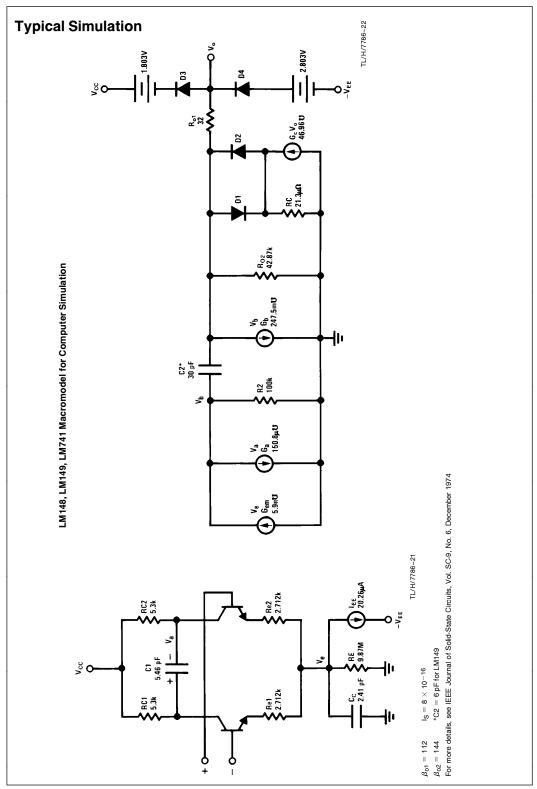


$$f = \frac{K \times V_{IN}}{8V^{+}C1R1}, \; K = R2/R'2 \, , \, \frac{2V_{I}}{K} \leq 25V, \; V^{+} = V^{-}, \; V_{S} = \, \pm 15V$$

Use LM125 for $\pm\,15V$ supply

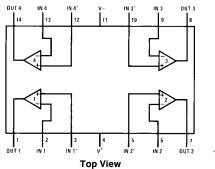
The circuit can be used as a low frequency V/F for process control.

Q1, Q3: KE4393, Q2, Q4: P1087E, D1-D4 = 1N914



Connection Diagram

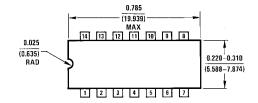
Dual-In-Line Package

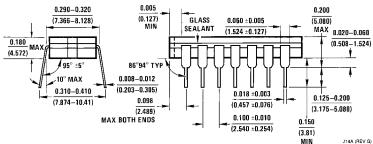


TL/H/7786-2

Order Number LM148J, LM148J/883, LM149J, LM149J/883, LM248J, LM348J, LM348M, LM348N or LM349N See NS Package Number J14A, M14A or N14A LM148J is available per JM38510/11001

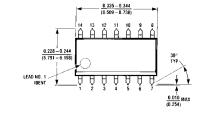
Physical Dimensions inches (millimeters)

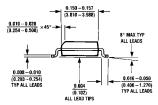


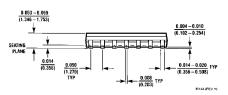


Ceramic Dual-In-Line Package (J)
Order Number LM148J, LM148J/883, LM149J, LM149J/883, LM248J or LM348J NS Package Number J14A

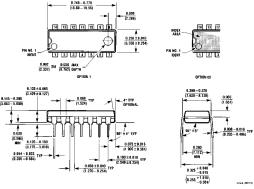
Physical Dimensions inches (millimeters) (Continued)







S.O. Package (M) Order Number LM348M NS Package Number M14A



Molded Dual-In-Line Package (N) Order Number LM348N or LM349N NS Package Number N14A

LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor

National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018

National Semiconductor Europe

Fax: (+49) 0-180-530 85 86 Fax: (+49) U-18U-35U oo oo Email: onjwege etevm2.nsc.com Deutsch Tel: (+49) 0-180-530 85 85 English Tei: (+49) 0-180-532 78 32 Français Tel: (+49) 0-180-532 93 58 Italiano Tel: (+49) 0-180-534 16 80 National Semiconductor Hong Kong Ltd.
13th Floor, Straight Block,
Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960

National Semiconductor Japan Ltd.
Tel: 81-043-299-2309
Fax: 81-043-299-2408

National does not assume any responsibility for use of any circuitry described, no circuit patent licenses are implied and National reserves the right at any time without notice to change said circuitry and specifications