

# LM107/LM207/LM307 Operational Amplifiers

### **General Description**

The LM107 series are complete, general purpose operational amplifiers, with the necessary frequency compensation built into the chip. Advanced processing techniques make the input currents a factor of ten lower than industry standards like the 709. Yet, they are a direct, plug-in replacement for the 709, LM101A and 741.

The LM107 series offers the features of the LM101A, which makes its application nearly foolproof. In addition, the device provides better accuracy and lower noise in high impedance circuitry. The low input currents also make it particularly well suited for long interval integrators or timers, sample and hold circuits and low frequency waveform genera-

tors. Further, replacing circuits where matched transistor pairs buffer the inputs of conventional IC op amps, it can give lower offset voltage and drift at a lower cost.

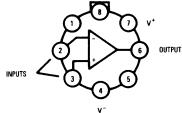
The LM107 is guaranteed over a  $-55^{\circ}$ C to  $+125^{\circ}$ C temperature range, the LM207 from  $-25^{\circ}$ C to  $+85^{\circ}$ C and the LM307 from 0°C to  $+70^{\circ}$ C.

#### **Features**

- Offset voltage 3 mV maximum over temperature
- Input current 100 nA maximum over temperature
- Offset current 20 nA maximum over temperature
- Guaranteed drift characteristics

## **Connection Diagrams**





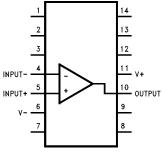
TI /H/7757-2

Note: Pin 4 connected to case

#### **Top View**

Order Number LM107H/883\* See NS Package Number H08C

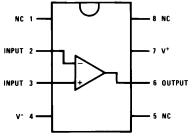
#### **Dual-in-Line Package**



TL/H/77.
Order Number LM107J-14/883\*

See NS Package Number J14A

### Dual-in-Line Package



TL/H/7757-3

Top View Order Number LM107J/883\* or LM207J See NS Package Number J08A

> Order Number LM307N See NS Package Number N08A

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<sup>\*</sup>Available per SMD# 5962-8958901.

# **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications. (Note 4)

,					
	LM107/LM207	LM307			
Supply Voltage	$\pm22V$	$\pm18V$		т	т
Power Dissipation (Note 1)	500 mW	500 mW		T <sub>MIN</sub>	$T_{MAX}$
Differential Input Voltage	$\pm30V$	$\pm 30V$	LM107	−55°C	+ 125°C
Input Voltage (Note 2)	$\pm15V$	$\pm15V$	LM207	−25°C	+85°C
Output Short Circuit Duration	Continuous	Continuous	LM307	0°C	+70°C
Operating Temperature Range (T <sub>A</sub> )			ESD rating to be determined.		
(1.14.07)	FF00 +- + 40F00	000 +- + 7000			

-65°C to +150°C -65°C to +150°C Lead Temperature (Soldering, 10 sec) 260°C 260°C

# **Electrical Characteristics** (Note 3)

Parameter	Conditions	LM107/LM207			LM307			Units
		Min	Тур	Max	Min	Тур	Max	Onits
Input Offset Voltage	$T_A = 25^{\circ}C, R_S \le 50 \text{ k}\Omega$		0.7	2.0		2.0	7.5	mV
Input Offset Current	$T_A = 25^{\circ}C$		1.5	10		3.0	50	nA
Input Bias Current	$T_A = 25^{\circ}C$		30	75		70	250	nA
Input Resistance	$T_A = 25^{\circ}C$	1.5	4.0		0.5	2.0		MΩ
Supply Current	$T_A = 25^{\circ}C$ $V_S = \pm 20V$ $V_S = \pm 15V$		1.8	3.0		1.8	3.0	mA mA
Large Signal Voltage Gain	$T_{A}=25^{\circ}\text{C},V_{S}=\pm15\text{V} \ V_{OUT}=\pm10\text{V},R_{L}\geq2k\Omega$	50	160		25	160		V/mV
Input Offset Voltage	$R_S \le 50 \text{ k}\Omega$			3.0			10	mV
Average Temperature Coefficient of Input Offset Voltage			3.0	15		6.0	30	μV/°C
Input Offset Current				20			70	nA
Average Temperature Coefficient of Input Offset Current	$25^{\circ}C \leq T_{A} \leq T_{MAX}$ $T_{MIN} \leq T_{A} \leq 25^{\circ}C$		0.01 0.02	0.1 0.2		0.01 0.02	0.3 0.6	nA/°C nA/°C
Input Bias Current				100			300	nA
Supply Current	$T_A = +125^{\circ}C, V_S = \pm 20V$		1.2	2.5				mA

## Electrical Characteristics (Note 3) (Continued)

Parameter	Conditions	LM107/LM207			LM307			Units
		Min	Тур	Max	Min	Тур	Max	Onits
Large Signal Voltage Gain	$V_S = \pm 15V, V_{OUT} = \pm 10V$ $R_L \ge 2 k\Omega$	25			15			V/mV
Output Voltage Swing	$\begin{aligned} &V_S =  \pm  15V \\ &R_L =  10  k\Omega \\ &R_L =  2  k\Omega \end{aligned}$	±12 ±10	±14 ±13		± 12 ± 10	±14 ±13		V V
Input Voltage Range	$V_S = \pm 20V$ $V_S = \pm 15V$	±15	+ 15 - 13		±12	+ 15 - 13		V V
Common Mode Rejection Ratio	$R_S \le 50 \text{ k}\Omega$	80	96		70	90		dB
Supply Voltage Rejection Ratio	$R_S \le 50 \text{ k}\Omega$	80	96		70	96		dB

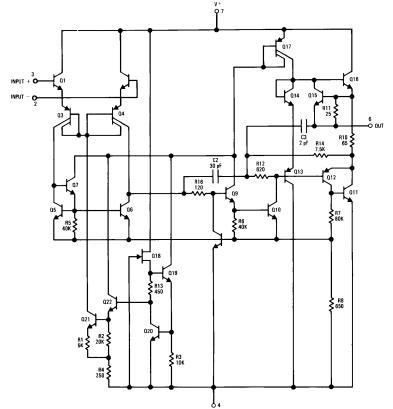
Note 1: The maximum junction temperature of the LM107 is 150°C, and the LM207/LM307 is 100°C. For operating at elevated temperatures, devices in the H08 package must be derated based on a thermal resistance of 165°C/W, junction to ambient, or 30°C/W, junction to case. The thermal resistance of the dual-in-line package is 100°C/W, junction to ambient.

Note 2: For supply voltages less than  $\pm 15V$ , the absolute maximum input voltage is equal to the supply voltage.

Note 3: These specifications apply for  $\pm 5 \text{V} \le \text{V}_S \le +20 \text{V}$  and  $-55^{\circ}\text{C} \le \text{T}_A \le +125^{\circ}\text{C}$  for the LM107 or  $-25^{\circ}\text{C} \le \text{T}_A +85^{\circ}\text{C}$  for the LM207, and  $0^{\circ}\text{C} \le \text{T}_A \le +70^{\circ}\text{C}$  and  $\pm 5 \text{V} \le \text{V}_S \le \pm 15 \text{V}$  for the LM307 unless otherwise specified.

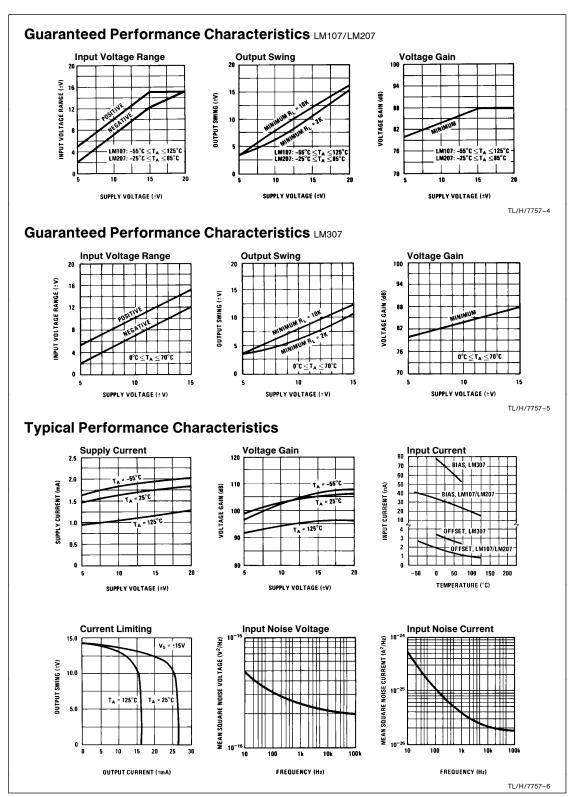
Note 4: Refer to RETS107X for LM107H and LM107J military specifications.

## Schematic Diagram\*

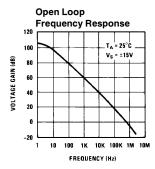


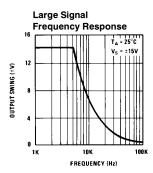
\*Pin connections shown are for metal can.

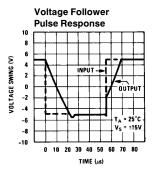
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# **Typical Performance Characteristics** (Continued)

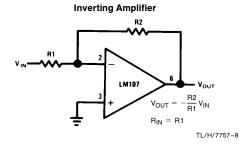


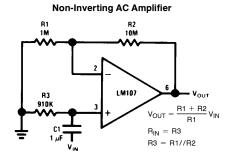




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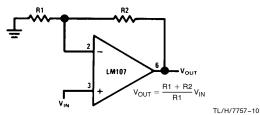
# Typical Applications\*\*



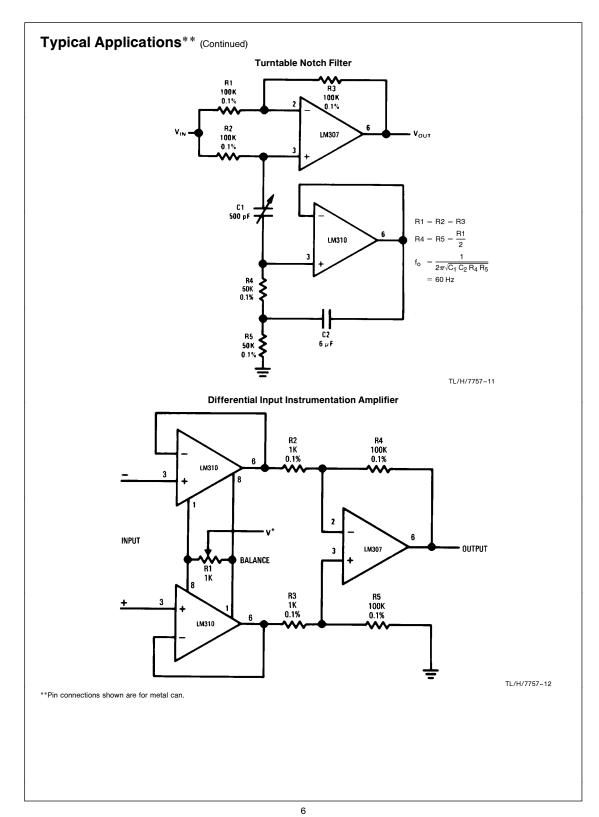


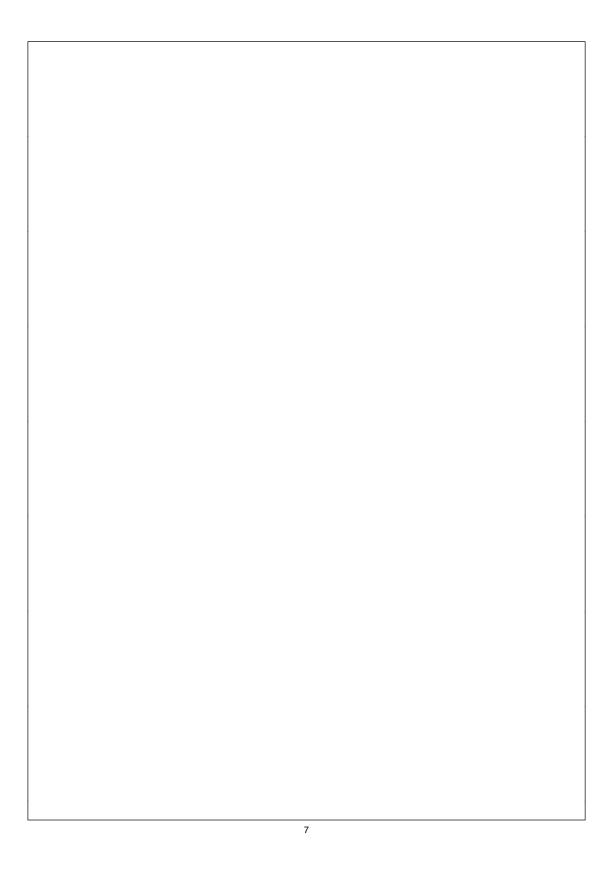
#### Non-Inverting Amplifier

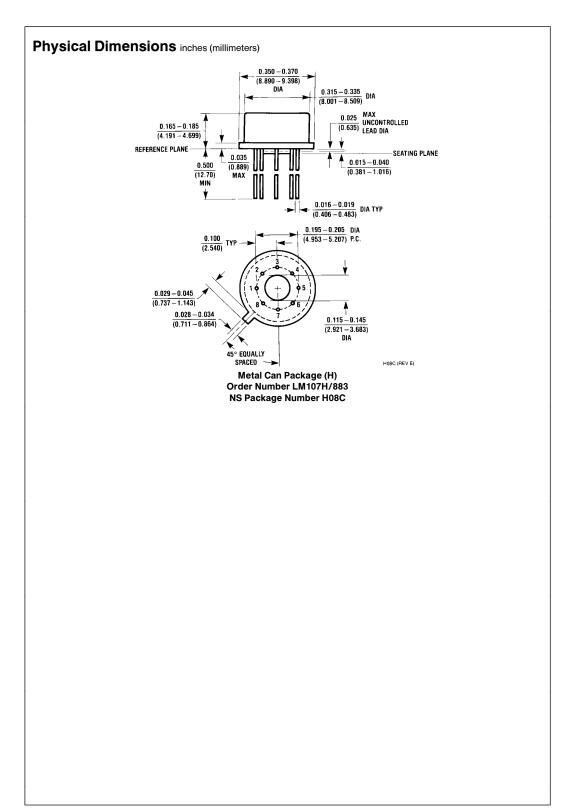
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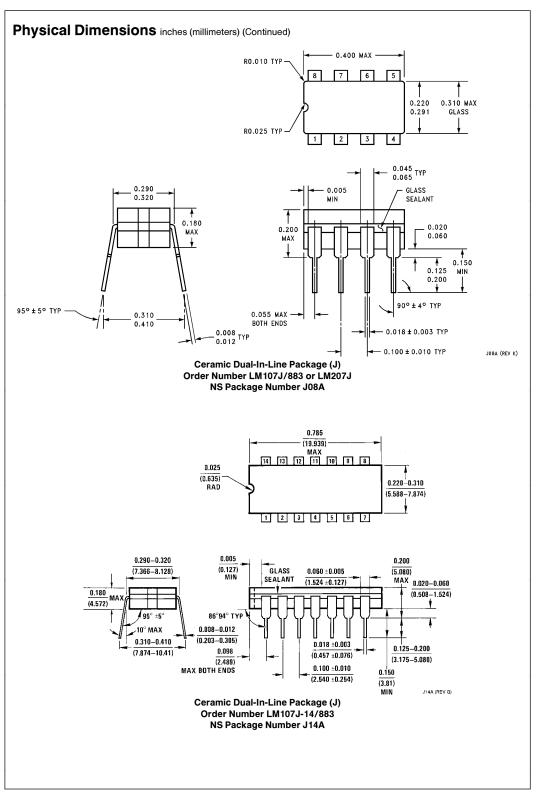


<sup>\*\*</sup>Pin connections shown are for metal can.

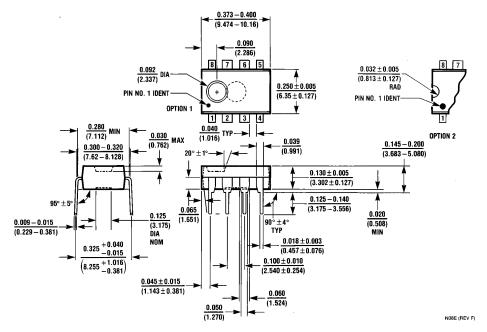








### Physical Dimensions inches (millimeters) (Continued)



Molded Dual-In-Line Package (N) Order Number LM307N **NS Package Number N08E** 

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