

## FEATURES

- 60 $\mu$ V Maximum Offset Voltage
- 300pA Maximum Input Bias Current
- 135 $\mu$ A Supply Current per Amplifier
- Rail-to-Rail Output Swing
- 120dB Minimum Voltage Gain,  $V_S = \pm 15\text{V}$
- 0.8 $\mu\text{V}/^\circ\text{C}$  Maximum  $V_{OS}$  Drift
- 14nV/ $\sqrt{\text{Hz}}$  Input Noise Voltage
- 2.7V to  $\pm 18\text{V}$  Supply Voltage Operation
- Operating Temperature Range:  $-40^\circ\text{C}$  to  $85^\circ\text{C}$
- Space Saving 3mm  $\times$  3mm DFN Package

## APPLICATIONS

- Thermocouple Amplifiers
- Precision Photo Diode Amplifiers
- Instrumentation Amplifiers
- Battery-Powered Precision Systems
- Low Voltage Precision Systems

## DESCRIPTION

The LT<sup>®</sup>6011/LT6012 op amps combine low noise and high precision input performance with low power consumption and rail-to-rail output swing.

Input offset voltage is trimmed to less than 60 $\mu\text{V}$ . The low drift and excellent long-term stability guarantee a high accuracy over temperature and time. The 300pA maximum input bias current and 120dB minimum voltage gain further maintain this precision over operating conditions.

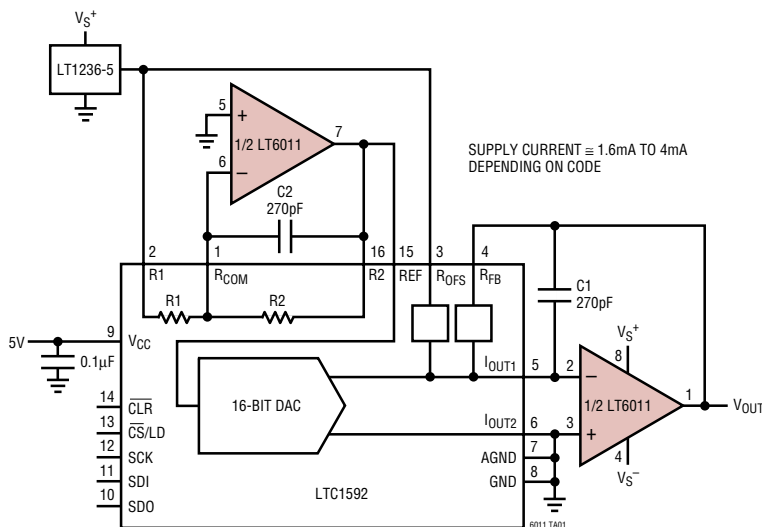
The LT6011/LT6012 work on any power supply voltage from 2.7V to 36V and draw only 135 $\mu\text{A}$  of supply current on a 5V supply. The output swings to within 40mV of either supply rail, making the amplifier a good choice for low voltage single supply applications.

The LT6011/LT6012 are specified at 5V and  $\pm 15\text{V}$  supplies and from  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ . The LT6011 (dual) is available in SO-8, MS8 and space saving 3mm  $\times$  3mm DFN packages. The LT6012 (quad) is available in SO-14 and 16-pin SSOP packages.

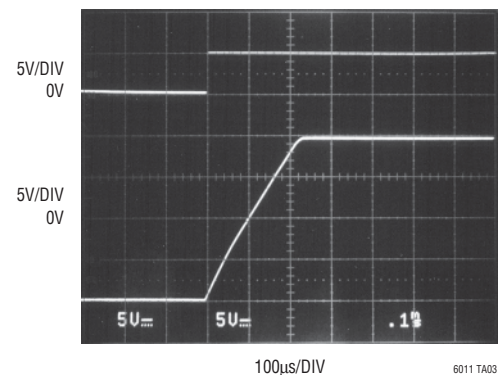
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## TYPICAL APPLICATION

### Low Power Programmable Output Range 16-Bit SoftSpan™ DAC



### 20V Output Step Response

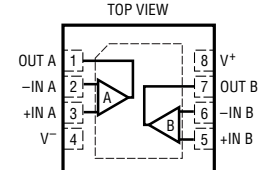
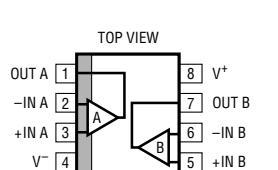
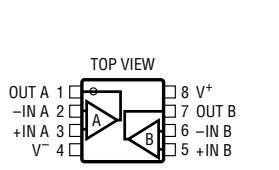
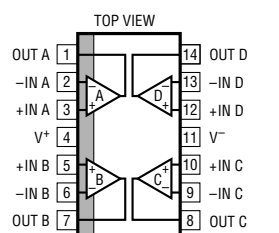
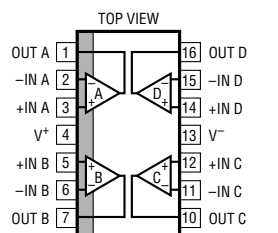


# LT6011/LT6012

## ABSOLUTE MAXIMUM RATINGS (Note 1)

Total Supply Voltage ( $V^+$ to $V^-$ ) .....	40V	Maximum Junction Temperature	
Differential Input Voltage (Note 2) .....	10V	DD Package .....	125°C
Input Voltage .....	$V^+$ to $V^-$	All Other Packages .....	150°C
Input Current (Note 2) .....	$\pm 10\text{mA}$	Storage Temperature Range	
Output Short-Circuit Duration (Note 3) .....	Indefinite	DD Package .....	-65°C to 125°C
Operating Temperature Range (Note 4) ..	-40°C to 85°C	All Other Packages .....	-65°C to 150°C
Specified Temperature Range (Note 5) ...	-40°C to 85°C	Lead Temperature (Soldering, 10 sec) .....	300°C

## PACKAGE/ORDER INFORMATION

 <p>DD PACKAGE 8-LEAD (3mm x 3mm) PLASTIC DFN <math>T_{JMAX} = 125^\circ\text{C}</math>, <math>\theta_{JA} = 43^\circ\text{C/W}</math> UNDERSIDE METAL CONNECTED TO <math>V^-</math> (PCB CONNECTION OPTIONAL)</p>		 <p>S8 PACKAGE 8-LEAD PLASTIC SO <math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 190^\circ\text{C/W}</math></p>		 <p>MS8 PACKAGE 8-LEAD PLASTIC MSOP <math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 220^\circ\text{C/W}</math></p>	
ORDER PART NUMBER	DD PART MARKING*	ORDER PART NUMBER	S8 PART MARKING	ORDER PART NUMBER	MS8 PART MARKING*
LT6011CDD	LACD	LT6011CS8	6011	LT6011CMS8	LTCGC
LT6011IDD	LACD	LT6011IS8	6011I	LT6011IMS8	LTCGC
LT6011ACDD	LACD	LT6011ACS8	6011A		
LT6011AIDD	LACD	LT6011AIS8	6011AI		
 <p>S PACKAGE 14-LEAD PLASTIC SO <math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 110^\circ\text{C/W}</math></p>			 <p>GN PACKAGE 16-LEAD PLASTIC SSOP <math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 135^\circ\text{C/W}</math></p>		
ORDER PART NUMBER			ORDER PART NUMBER	GN PART MARKING	
LT6012CS	LT6012ACS	LT6012CGN	LT6012ACGN	6012	6012A
LT6012IS	LT6012AIS	LT6012IGN	LT6012AIGN	6012I	6012AI
<b>Order Options</b> Tape and Reel: Add #TR Lead Free: Add #PBF Lead Free Tape and Reel: Add #TRPBF Lead Free Part Marking: <a href="http://www.linear.com/leadfree/">http://www.linear.com/leadfree/</a>					

Consult LTC Marketing for parts specified with wider operating temperature ranges.

\*Temperature grades are identified by a label on the shipping container.

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**ELECTRICAL CHARACTERISTICS** The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  $V_S = 5\text{V}$ ,  $0\text{V}$ ;  $V_{CM} = 2.5\text{V}$ ;  $R_L$  to  $0\text{V}$ ; unless otherwise specified. (Note 5)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$V_{OS}$	Input Offset Voltage (Note 8)	LT6011AS8, LT6012AS $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		20	60 85 110	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011ADD, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		25	85 135 170	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011S8, LT6012S $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		25	75 100 125	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011DD, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		30	125 175 210	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift (Note 6)	LT6011AS8, LT6011S8, LT6012AS, LT6012S LT6011ADD, LT6011DD, LT6012AGN, LT6012GN, LT6011MS8 ●		0.2	0.8	$\mu\text{V}/^\circ\text{C}$
				0.2	1.2	$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Input Offset Current (Note 8)	LT6011AS8, LT6011ADD, LT6012AS, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		20	300 450 600	$\text{pA}$ $\text{pA}$ $\text{pA}$
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		150	900 1200 1500	$\text{pA}$ $\text{pA}$ $\text{pA}$
$I_B$	Input Bias Current (Note 8)	LT6011AS8, LT6011ADD, LT6012AS, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		20	$\pm 300$ $\pm 450$ $\pm 600$	$\text{pA}$ $\text{pA}$ $\text{pA}$
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ ● $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ●		150	$\pm 900$ $\pm 1200$ $\pm 1500$	$\text{pA}$ $\text{pA}$ $\text{pA}$
	Input Noise Voltage	0.1Hz to 10Hz		400		$\text{nV}_{P-P}$
$e_n$	Input Noise Voltage Density	$f = 1\text{kHz}$		14		$\text{nV}/\sqrt{\text{Hz}}$
$i_n$	Input Noise Current Density	$f = 1\text{kHz}$ , Unbalanced Source Resistance		0.1		$\text{pA}/\sqrt{\text{Hz}}$
$R_{IN}$	Input Resistance	Common Mode, $V_{CM} = 1\text{V}$ to $3.8\text{V}$	10	120		$\text{G}\Omega$
		Differential		20		$\text{M}\Omega$
$C_{IN}$	Input Capacitance			4		$\text{pF}$
$V_{CM}$	Input Voltage Range (Positive)	Guaranteed by CMRR ●	3.8	4		$\text{V}$
	Input Voltage Range (Negative)	Guaranteed by CMRR ●		0.7	1	$\text{V}$
CMRR	Common Mode Rejection Ratio	$V_{CM} = 1\text{V}$ to $3.8\text{V}$ ●	107	135		$\text{dB}$
	Minimum Supply Voltage	Guaranteed by PSRR ●		2.4	2.7	$\text{V}$
PSRR	Power Supply Rejection Ratio	$V_S = 2.7\text{V}$ to $36\text{V}$ , $V_{CM} = 1/2V_S$ ●	112	135		$\text{dB}$
$A_{VOL}$	Large-Signal Voltage Gain	$R_L = 10\text{k}$ , $V_{OUT} = 1\text{V}$ to $4\text{V}$ ●	300	2000		$\text{V}/\text{mV}$
		$R_L = 2\text{k}$ , $V_{OUT} = 1\text{V}$ to $4\text{V}$ ●	250	2000		$\text{V}/\text{mV}$
	Channel Separation	$V_{OUT} = 1\text{V}$ to $4\text{V}$ ●	110	140		$\text{dB}$

## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  $V_S = 5\text{V}$ ,  $0\text{V}$ ;  $V_{\text{CM}} = 2.5\text{V}$ ;  $R_L$  to  $0\text{V}$ ; unless otherwise specified. (Note 5)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$V_{\text{OUT}}$	Maximum Output Swing (Positive, Referred to $V^+$ )	No Load, 50mV Overdrive	●	35	55 65	mV mV
		$I_{\text{SOURCE}} = 1\text{mA}$ , 50mV Overdrive	●	120	170 220	mV mV
	Maximum Output Swing (Negative, Referred to $0\text{V}$ )	No Load, 50mV Overdrive	●	40	55 65	mV mV
		$I_{\text{SINK}} = 1\text{mA}$ , 50mV Overdrive	●	150	225 275	mV mV
$I_{\text{SC}}$	Output Short-Circuit Current (Note 3)	$V_{\text{OUT}} = 0\text{V}$ , 1V Overdrive, Source	●	10 4	14	mA mA
		$V_{\text{OUT}} = 5\text{V}$ , -1V Overdrive, Sink	●	10 4	21	mA mA
SR	Slew Rate	$A_V = -10$ , $R_F = 50\text{k}$ , $R_G = 5\text{k}$ $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ● ●	0.06 0.05 0.04	0.09	V/ $\mu\text{s}$ V/ $\mu\text{s}$ V/ $\mu\text{s}$
GBW	Gain Bandwidth Product	$f = 10\text{kHz}$	●	250 225	330	kHz kHz
$t_s$	Settling Time	$A_V = -1$ , 0.01%, $V_{\text{OUT}} = 1.5\text{V}$ to $3.5\text{V}$		45		$\mu\text{s}$
$t_r$ , $t_f$	Rise Time, Fall Time	$A_V = 1$ , 10% to 90%, 0.1V Step		1		$\mu\text{s}$
$\Delta V_{\text{OS}}$	Offset Voltage Match (Note 7)	LT6011AS8, LT6012AS $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	50	120 170 220	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011ADD, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	50	170 270 340	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011S8, LT6012S $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	50	150 200 250	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011DD, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	60	250 350 420	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●		1800 2400 3000	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
$\Delta I_B$	Input Bias Current Match (Note 7)	LT6011AS8, LT6011ADD, LT6012AS, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	50	600 900 1200	pA pA pA
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●		1800 2400 3000	pA pA pA
$\Delta\text{CMRR}$	Common Mode Rejection Ratio Match (Note 7)		●	101	135	dB
$\Delta\text{PSRR}$	Power Supply Rejection Ratio Match (Note 7)		●	106	135	dB
$I_S$	Supply Current	per Amplifier $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ● ●	135	150 190 210	$\mu\text{A}$ $\mu\text{A}$ $\mu\text{A}$

**ELECTRICAL CHARACTERISTICS** The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  $V_S = \pm 15\text{V}$ ,  $V_{CM} = 0\text{V}$ ,  $R_L$  to  $0\text{V}$ , unless otherwise specified. (Note 5)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$V_{OS}$	Input Offset Voltage (Note 8)	LT6011AS8, LT6012AS $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	30	135	$\mu\text{V}$
			●		160	$\mu\text{V}$
			●		185	$\mu\text{V}$
		LT6011ADD, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	35	160	$\mu\text{V}$
			●		210	$\mu\text{V}$
			●		225	$\mu\text{V}$
	LT6011S8, LT6012S $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	35	150	$\mu\text{V}$	
			●		175	$\mu\text{V}$
			●		200	$\mu\text{V}$
	LT6011DD, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	40	200	$\mu\text{V}$	
			●		250	$\mu\text{V}$
			●		275	$\mu\text{V}$
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift (Note 6)	LT6011AS8, LT6011S8, LT6012AS, LT6012S	●	0.2	0.8	$\mu\text{V}/^\circ\text{C}$
		LT6011ADD, LT6011DD, LT6012AGN, LT6012GN, LT6011MS8	●	0.2	1.3	$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Input Offset Current (Note 8)	LT6011AS8, LT6011ADD, LT6012AS, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	20	300	$\text{pA}$
			●		450	$\text{pA}$
			●		600	$\text{pA}$
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	150	900	$\text{pA}$
			●		1200	$\text{pA}$
			●		1500	$\text{pA}$
$I_B$	Input Bias Current (Note 8)	LT6011AS8, LT6011ADD, LT6012AS, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	20	$\pm 300$	$\text{pA}$
			●		$\pm 450$	$\text{pA}$
			●		$\pm 600$	$\text{pA}$
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	150	$\pm 900$	$\text{pA}$
			●		$\pm 1200$	$\text{pA}$
			●		$\pm 1500$	$\text{pA}$
	Input Noise Voltage	0.1Hz to 10Hz		400		$\text{nV}_{\text{p-p}}$
$e_n$	Input Noise Voltage Density	$f = 1\text{kHz}$		13		$\text{nV}/\sqrt{\text{Hz}}$
$i_n$	Input Noise Current Density	$f = 1\text{kHz}$ , Unbalanced Source Resistance		0.1		$\text{pA}/\sqrt{\text{Hz}}$
$R_{IN}$	Input Resistance	Common Mode, $V_{CM} = \pm 13.5\text{V}$		50	400	$\text{G}\Omega$
		Differential			20	$\text{M}\Omega$
$C_{IN}$	Input Capacitance			4		$\text{pF}$
$V_{CM}$	Input Voltage Range	Guaranteed by CMRR	●	$\pm 13.5$	$\pm 14$	$\text{V}$
CMRR	Common Mode Rejection Ratio	$V_{CM} = -13.5\text{V}$ to $13.5\text{V}$	●	115	135	$\text{dB}$
			●	112	135	$\text{dB}$
	Minimum Supply Voltage	Guaranteed by PSRR	●	$\pm 1.2$	$\pm 1.35$	$\text{V}$
PSRR	Power Supply Rejection Ratio	$V_S = \pm 1.35\text{V}$ to $\pm 18\text{V}$	●	112	135	$\text{dB}$
$A_{VOL}$	Large-Signal Voltage Gain	$R_L = 10\text{k}$ , $V_{OUT} = -13.5\text{V}$ to $13.5\text{V}$	●	1000	2000	$\text{V}/\text{mV}$
			●	600		$\text{V}/\text{mV}$
		$R_L = 5\text{k}$ , $V_{OUT} = -13.5\text{V}$ to $13.5\text{V}$	●	500	1500	$\text{V}/\text{mV}$
			●	300		$\text{V}/\text{mV}$
	Channel Separation	$V_{OUT} = -13.5\text{V}$ to $13.5\text{V}$	●	120	140	$\text{dB}$
$V_{OUT}$	Maximum Output Swing (Positive, Referred to $V^+$ )	No Load, 50mV Overdrive	●	45	80	$\text{mV}$
			●		100	$\text{mV}$
		$I_{SOURCE} = 1\text{mA}$ , 50mV Overdrive	●	140	195	$\text{mV}$
			●		240	$\text{mV}$
Maximum Output Swing (Negative, Referred to $V^-$ )	No Load, 50mV Overdrive	●	45	80	$\text{mV}$	
		●		100	$\text{mV}$	
	$I_{SINK} = 1\text{mA}$ , 50mV Overdrive	●	150	250	$\text{mV}$	
		●		300	$\text{mV}$	

## ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  $V_S = \pm 15\text{V}$ ,  $V_{CM} = 0\text{V}$ ,  $R_L$  to  $0\text{V}$ , unless otherwise specified. (Note 5)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$I_{SC}$	Output Short-Circuit Current (Note 3)	$V_{OUT} = 0\text{V}$ , 1V Overdrive (Source)	● 10	15		mA
			5			mA
		$V_{OUT} = 0\text{V}$ , -1V Overdrive (Sink)	● 10	20		mA
			5			mA
SR	Slew Rate	$A_V = -10$ , $R_F = 50\text{k}$ , $R_G = 5\text{k}$ $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● 0.08 ● 0.07 ● 0.05	0.11		V/ $\mu\text{s}$ V/ $\mu\text{s}$ V/ $\mu\text{s}$
GBW	Gain Bandwidth Product	$f = 10\text{kHz}$	● 275 ● 250	350		kHz kHz
$t_s$	Settling Time	$A_V = -1$ , 0.01%, $V_{OUT} = 0\text{V}$ to $10\text{V}$		85		$\mu\text{s}$
$t_r$ , $t_f$	Rise Time, Fall Time	$A_V = 1$ , 10% to 90%, 0.1V Step		1		$\mu\text{s}$
$\Delta V_{OS}$	Offset Voltage Match (Note 7)	LT6011AS8, LT6012AS $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	50	270 320 370	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011ADD, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	50	320 420 450	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011S8, LT6012S $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	70	300 350 400	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
		LT6011DD, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	80	400 500 550	$\mu\text{V}$ $\mu\text{V}$ $\mu\text{V}$
$\Delta I_B$	Input Bias Current Match (Note 7)	LT6011AS8, LT6011ADD, LT6012AS, LT6012AGN $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●	50	600 900 1200	pA pA pA
		LT6011S8, LT6011DD, LT6012S, LT6012GN, LT6011MS8 $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	● ●		1800 2400 3000	pA pA pA
$\Delta\text{CMRR}$	Common Mode Rejection Ratio Match (Note 7)		● 109	135		dB
$\Delta\text{PSRR}$	Power Supply Rejection Ratio Match (Note 7)		● 106	135		dB
$I_S$	Supply Current	per Amplifier $T_A = 0^\circ\text{C}$ to $70^\circ\text{C}$ $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$	●	260	330 380 400	$\mu\text{A}$ $\mu\text{A}$ $\mu\text{A}$
			●			

**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The inputs are protected by back-to-back diodes and internal series resistors. If the differential input voltage exceeds 10V, the input current must be limited to less than 10mA.

**Note 3:** A heat sink may be required to keep the junction temperature below absolute maximum ratings.

**Note 4:** Both the LT6011C/LT6012C and LT6011I/LT6012I are guaranteed functional over the operating temperature range of  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .

**Note 5:** The LT6011C/LT6012C are guaranteed to meet the specified performance from  $0^\circ\text{C}$  to  $70^\circ\text{C}$  and is designed, characterized and expected to meet specified performance from  $-40^\circ\text{C}$  to  $85^\circ\text{C}$  but is not tested or QA sampled at these temperatures. The LT6011I/LT6012I are guaranteed to meet specified performance from  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .

**Note 6:** This parameter is not 100% tested.

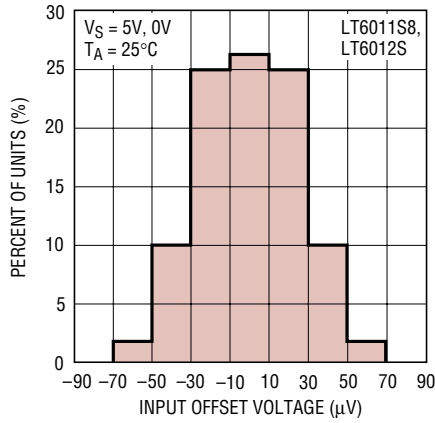
**Note 7:** Matching parameters are the difference between any two amplifiers.  $\Delta\text{CMRR}$  and  $\Delta\text{PSRR}$  are defined as follows: (1) CMRR and PSRR are measured in  $\mu\text{V}/\text{V}$  for the individual amplifiers. (2) The difference between matching amplifiers is calculated in  $\mu\text{V}/\text{V}$ . (3) The result is converted to dB.

**Note 8:** The specifications for  $V_{OS}$ ,  $I_B$ , and  $I_{OS}$  depend on the grade and on the package. The following table clarifies the notations.

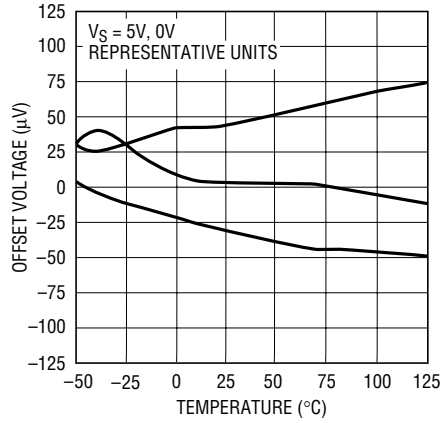
	STANDARD GRADE	A GRADE
S8 Package	LT6011S8	LT6011AS8
DFN Package	LT6011DD	LT6011ADD
S14 Package	LT6012S	LT6012AS
GN16 Package	LT6012GN	LT6012AGN
MS8 Package	LT6011MS8	N/A

# TYPICAL PERFORMANCE CHARACTERISTICS

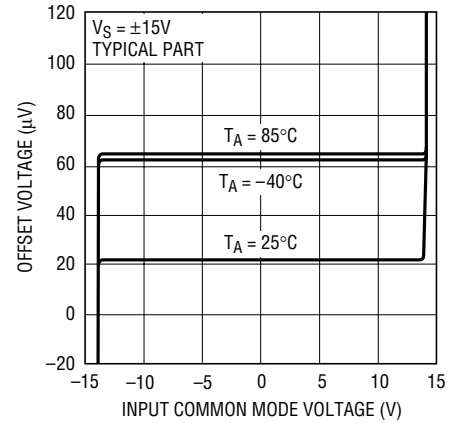
**Distribution of Input Offset Voltage**



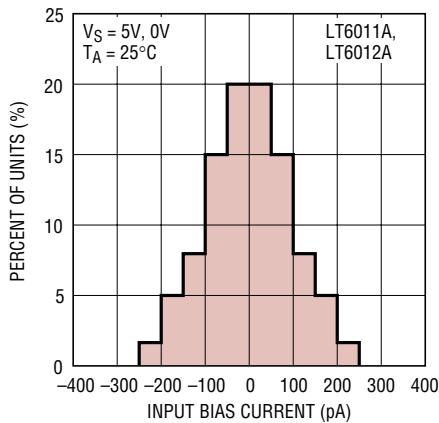
**Input Offset Voltage vs Temperature**



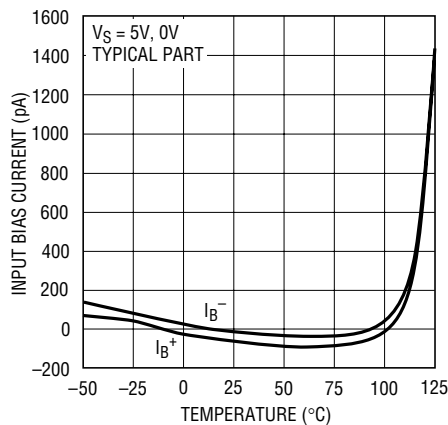
**Offset Voltage vs Input Common Mode Voltage**



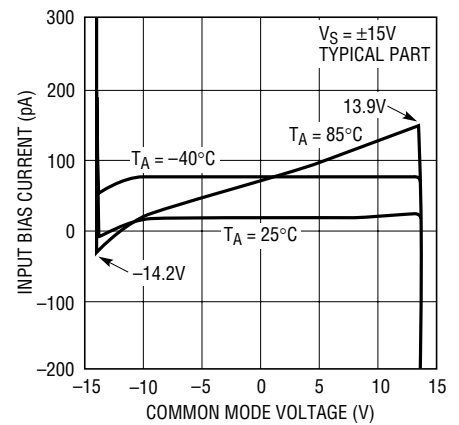
**Distribution of Input Bias Current**



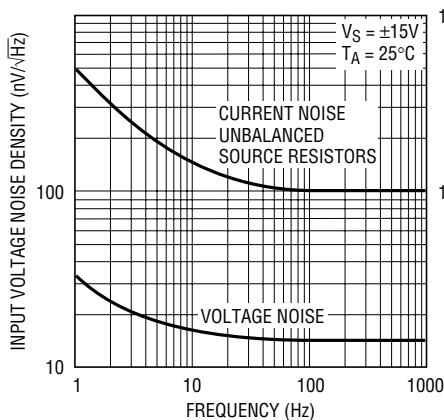
**Input Bias Current vs Temperature**



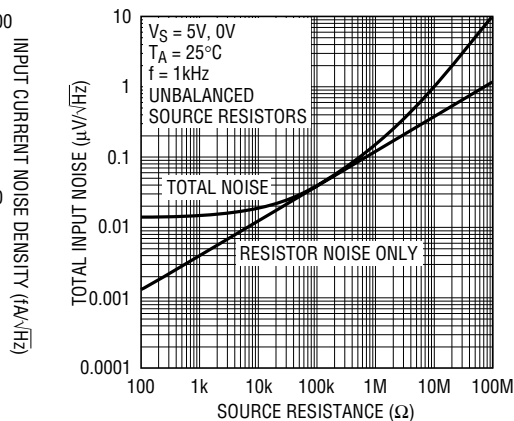
**Input Bias Current vs Input Common Mode Voltage**



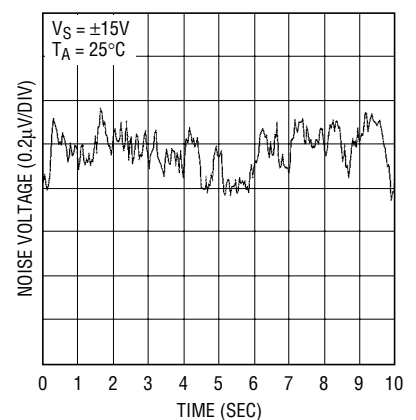
**$e_n, i_n$  vs Frequency**



**Total Input Noise vs Source Resistance**

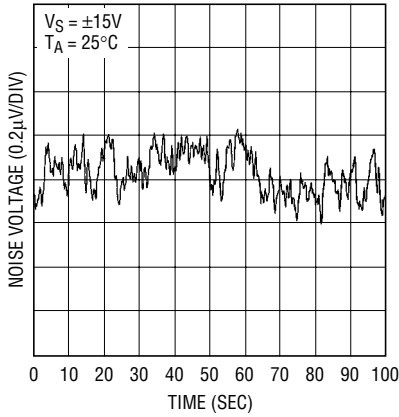


**0.1Hz to 10Hz Noise**



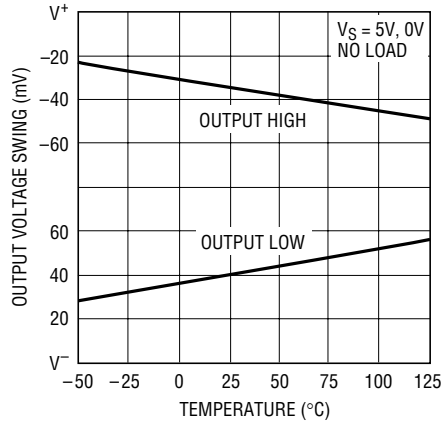
TYPICAL PERFORMANCE CHARACTERISTICS

0.01Hz to 1Hz Noise



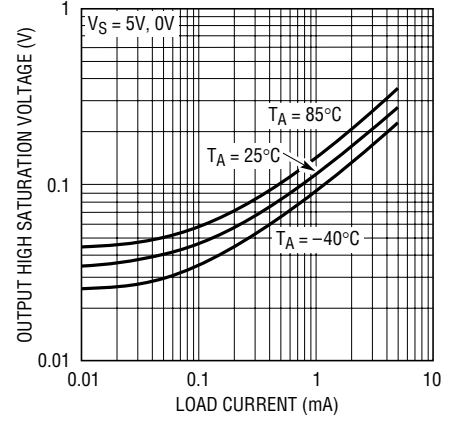
6011 G10

Output Voltage Swing vs Temperature



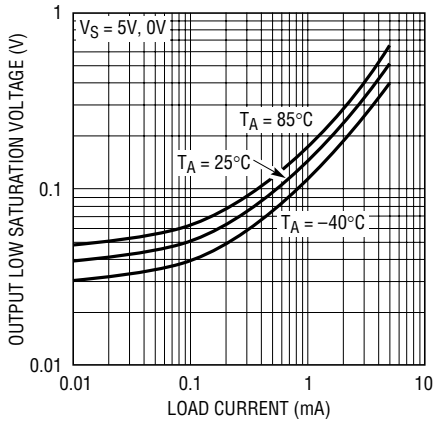
6011 G11

Output Saturation Voltage vs Load Current (Output High)



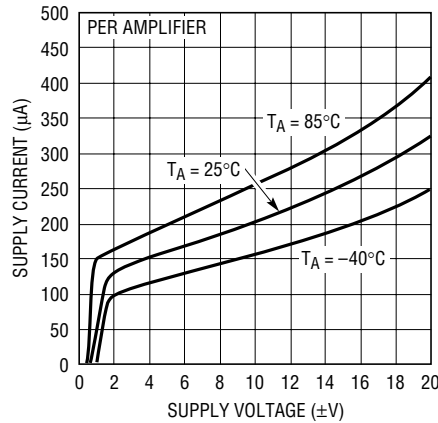
6011 G12

Output Saturation Voltage vs Load Current (Output Low)



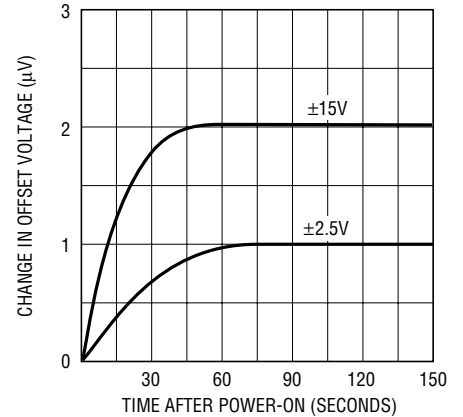
6011 G13

Supply Current vs Supply Voltage



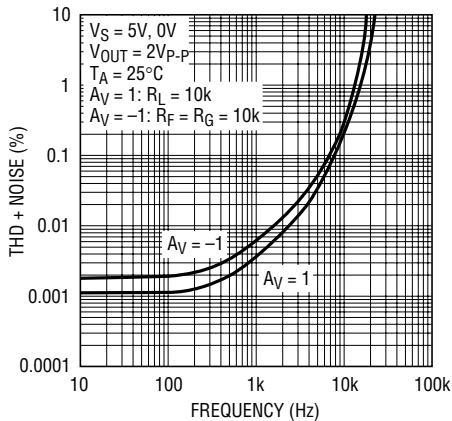
6011 G14

Warm-Up Drift



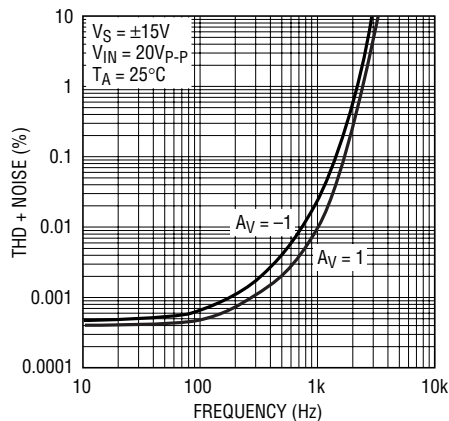
6011 G15

THD + Noise vs Frequency



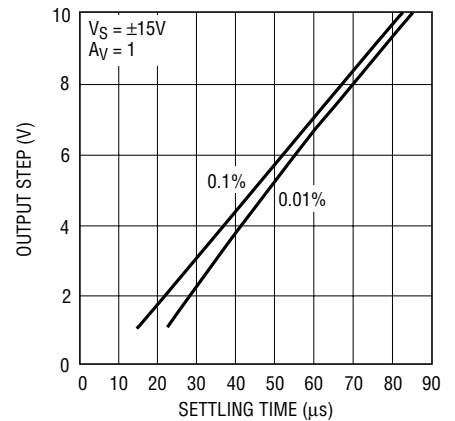
6011 G16

THD + Noise vs Frequency



6011 G17

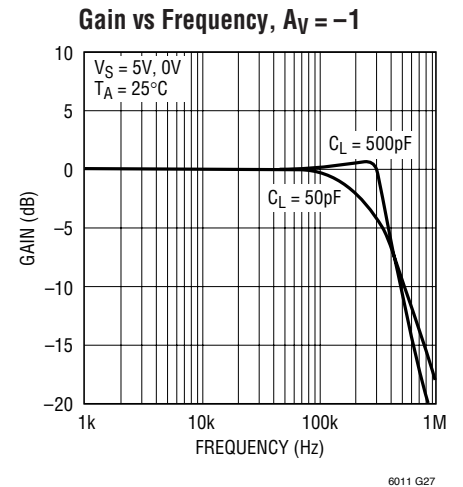
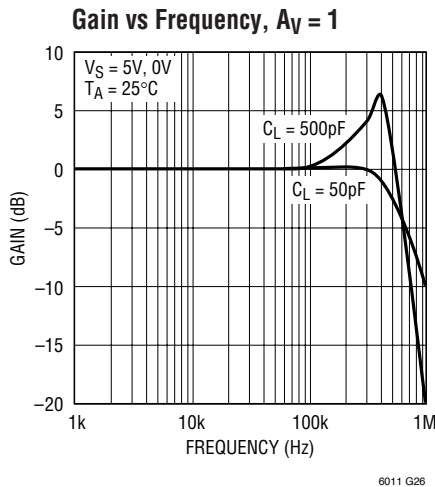
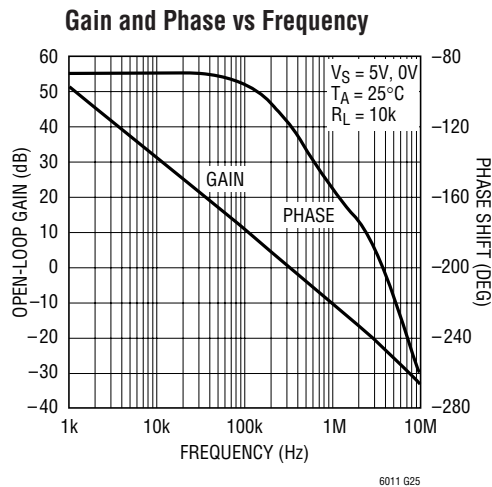
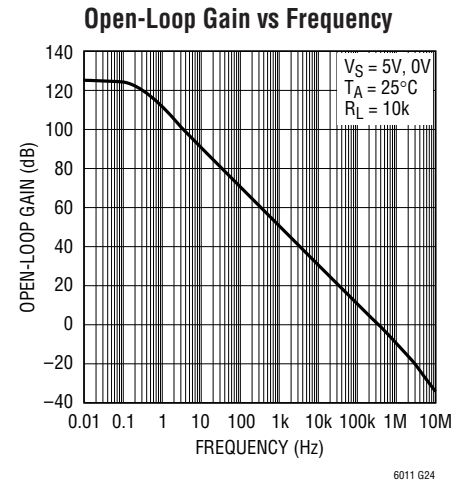
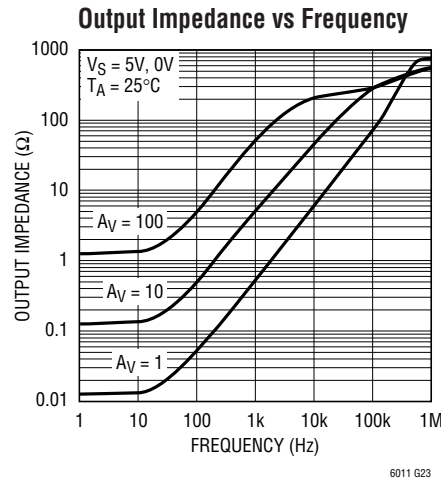
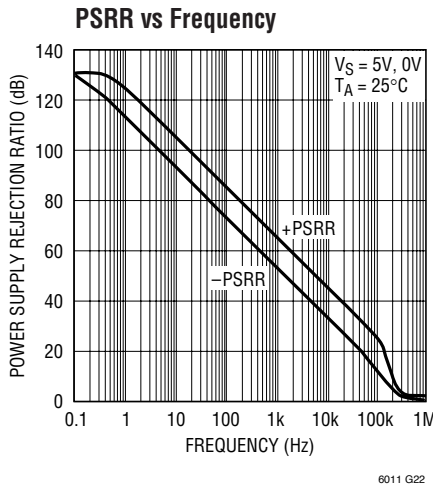
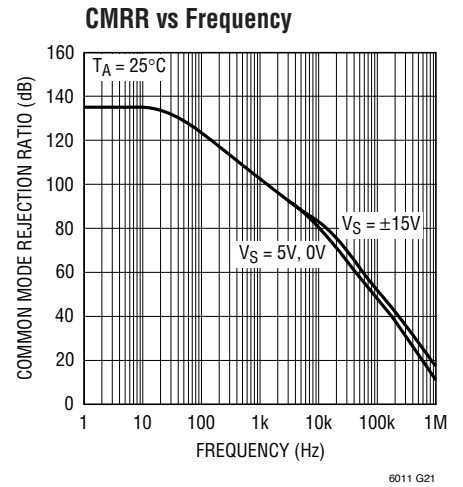
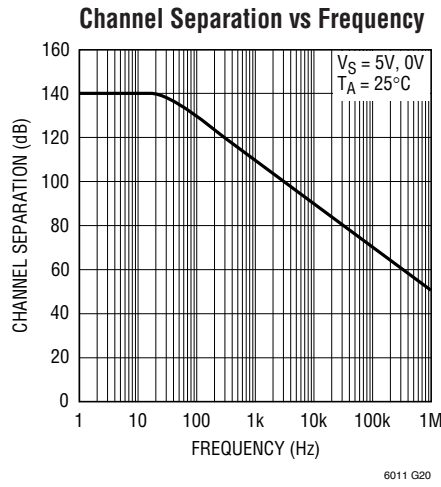
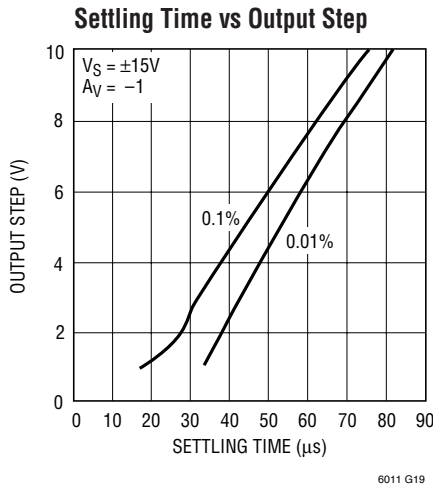
Settling Time vs Output Step



6011 G18

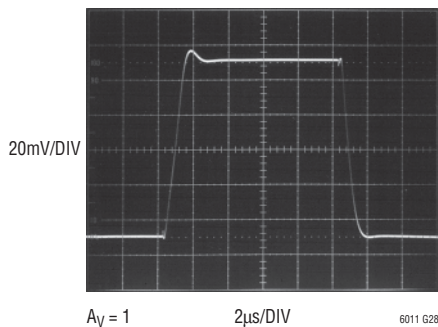


# TYPICAL PERFORMANCE CHARACTERISTICS

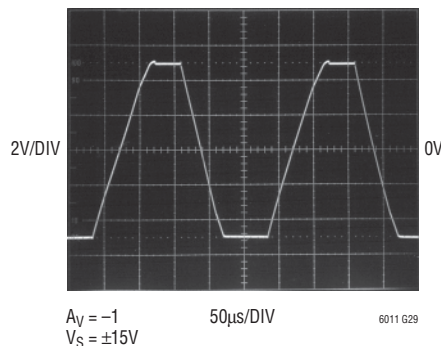


## TYPICAL PERFORMANCE CHARACTERISTICS

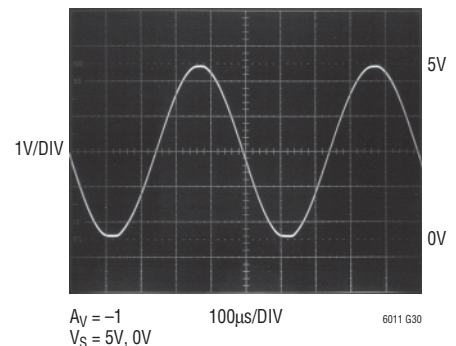
Small-Signal Transient Response



Large-Signal Transient Response



Rail-to-Rail Output Swing



## APPLICATIONS INFORMATION

### Preserving Input Precision

Preserving the input accuracy of the LT6011/LT6012 requires that the applications circuit and PC board layout do not introduce errors comparable to or greater than the 25µV typical offset of the amplifiers. Temperature differentials across the input connections can generate thermocouple voltages of 10's of microvolts so the connections to the input leads should be short, close together and away from heat dissipating components. Air currents across the board can also generate temperature differentials.

The extremely low input bias currents (20pA typical) allow high accuracy to be maintained with high impedance sources and feedback resistors. The LT6011/LT6012 low input bias currents are obtained by a cancellation circuit on-chip. This causes the resulting  $I_{B^+}$  and  $I_{B^-}$  to be uncorrelated, as implied by the  $I_{OS}$  specification being comparable to  $I_B$ . Do not try to balance the input resistances in each input lead; instead keep the resistance at either input as low as possible for maximum accuracy.

Leakage currents on the PC board can be higher than the input bias current. For example, 10GΩ of leakage between a 15V supply lead and an input lead will generate 1.5nA! Surround the input leads with a guard ring driven to the same potential as the input common mode to avoid excessive leakage in high impedance applications.

### Input Protection

The LT6011/LT6012 feature on-chip back-to-back diodes between the input devices, along with 500Ω resistors in

series with either input. This internal protection limits the input current to approximately 10mA (the maximum allowed) for a 10V differential input voltage. Use additional external series resistors to limit the input current to 10mA in applications where differential inputs of more than 10V are expected. For example, a 1k resistor in series with each input provides protection against 30V differential voltage.

### Input Common Mode Range

The LT6011/LT6012 output is able to swing close to each power supply rail (rail-to-rail out), but the input stage is limited to operating between  $V^- + 1V$  and  $V^+ - 1.2V$ . Exceeding this common mode range will cause the gain to drop to zero, however, no phase reversal will occur.

### Total Input Noise

The LT6011/LT6012 amplifier contributes negligible noise to the system when driven by sensors (sources) with impedance between 20kΩ and 1MΩ. Throughout this range, total input noise is dominated by the  $4kTR_S$  noise of the source. If the source impedance is less than 20kΩ, the input voltage noise of the amplifier starts to contribute with a minimum noise of  $14nV/\sqrt{Hz}$  for very low source impedance. If the source impedance is more than 1MΩ, the input current noise of the amplifier, multiplied by this high impedance, starts to contribute and eventually dominate. Total input noise spectral density can be calculated as:

$$V_{n(TOTAL)} = \sqrt{e_n^2 + 4kTR_S + (i_n R_S)^2}$$

## APPLICATIONS INFORMATION

where  $e_n = 14\text{nV}/\sqrt{\text{Hz}}$ ,  $i_n = 0.1\text{pA}/\sqrt{\text{Hz}}$  and  $R_S$  is the total impedance at the input, including the source impedance.

### Capacitive Loads

The LT6011/LT6012 can drive capacitive loads up to 500pF in unity gain. The capacitive load driving capability increases as the amplifier is used in higher gain configurations. A small series resistance between the output and the load further increases the amount of capacitance that the amplifier can drive.

### Rail-to-Rail Operation

The LT6011/LT6012 outputs can swing to within millivolts of either supply rail, but the inputs can not. However, for most op amp configurations, the inputs need to swing less than the outputs. Figure 1 shows the basic op amp configurations, lists what happens to the op amp inputs and specifies whether or not the op amp must have rail-to-rail inputs. Select a rail-to-rail input op amp only when really necessary, because the input precision specifications are usually inferior.

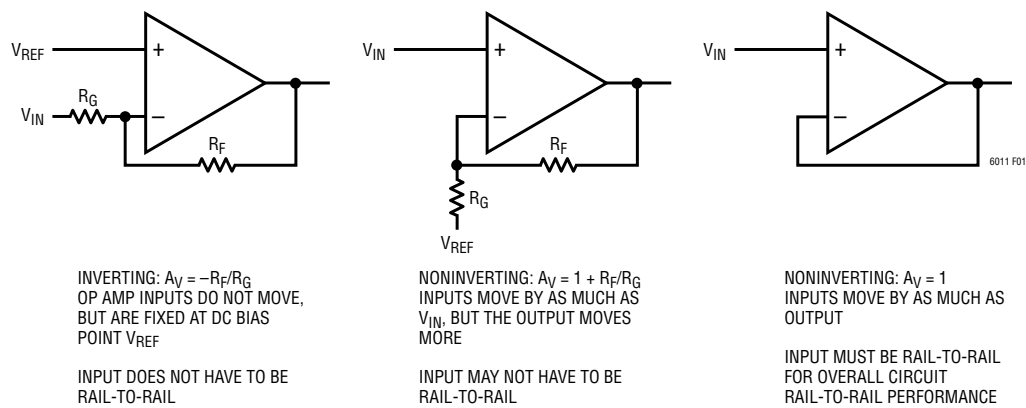
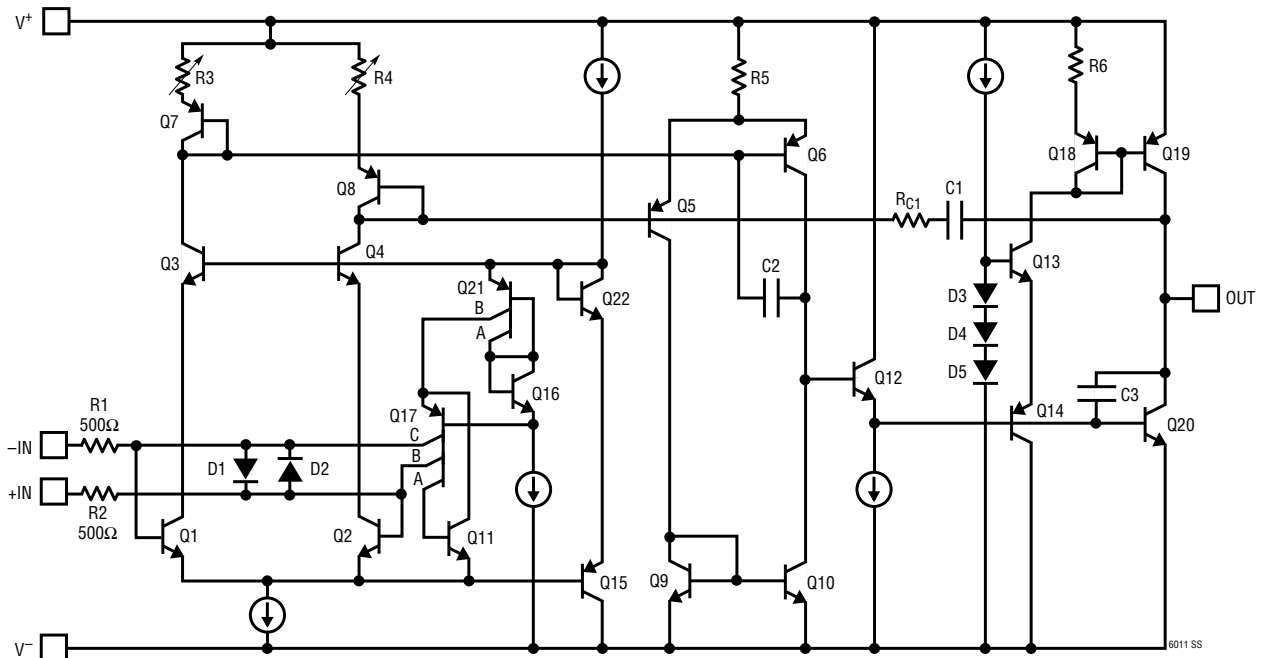


Figure 1. Some Op Amp Configurations Do Not Require Rail-to-Rail Inputs to Achieve Rail-to-Rail Outputs

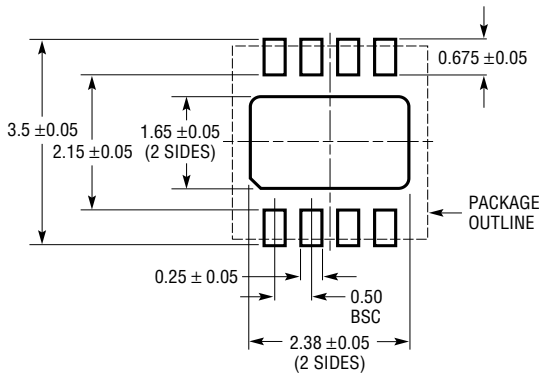
## SIMPLIFIED SCHEMATIC (One Amplifier)



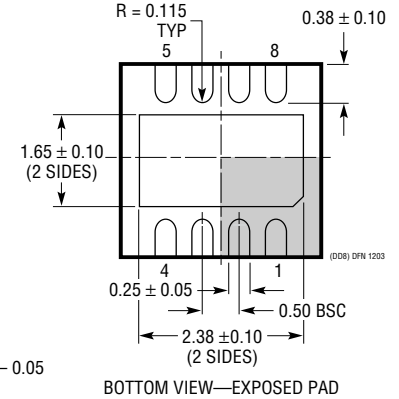
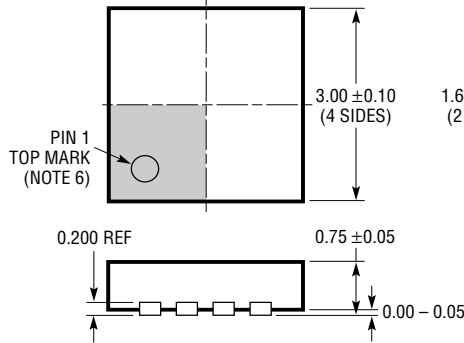
60112fb

**PACKAGE DESCRIPTION**

**DD Package**  
**8-Lead Plastic DFN (3mm × 3mm)**  
 (Reference LTC DWG # 05-08-1698)

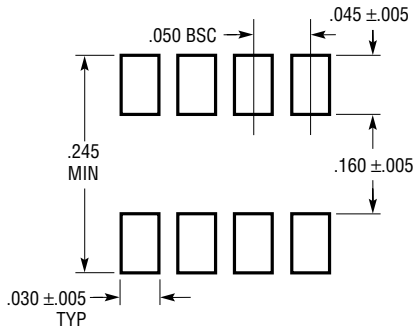


RECOMMENDED SOLDER PAD PITCH AND DIMENSIONS

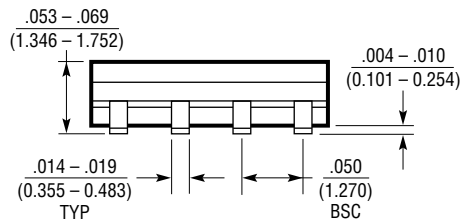
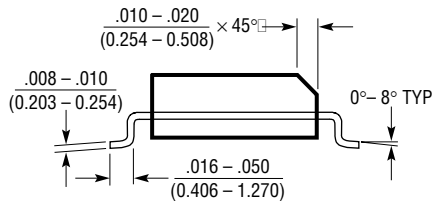
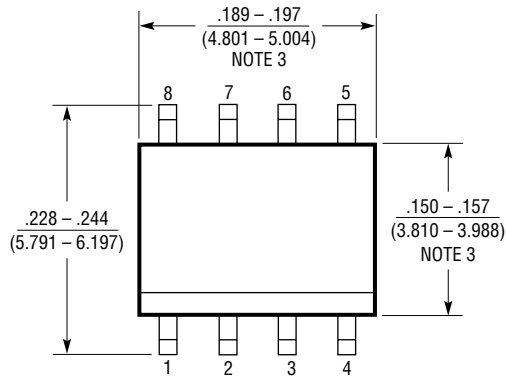


- NOTE:
1. DRAWING TO BE MADE A JEDEC PACKAGE OUTLINE M0-229 VARIATION OF (WEED-1)
  2. DRAWING NOT TO SCALE
  3. ALL DIMENSIONS ARE IN MILLIMETERS
  4. DIMENSIONS OF EXPOSED PAD ON BOTTOM OF PACKAGE DO NOT INCLUDE MOLD FLASH. MOLD FLASH, IF PRESENT, SHALL NOT EXCEED 0.15mm ON ANY SIDE
  5. EXPOSED PAD SHALL BE SOLDER PLATED
  6. SHADED AREA IS ONLY A REFERENCE FOR PIN 1 LOCATION ON TOP AND BOTTOM OF PACKAGE

**S8 Package**  
**8-Lead Plastic Small Outline (Narrow .150 Inch)**  
 (Reference LTC DWG # 05-08-1610)



RECOMMENDED SOLDER PAD LAYOUT



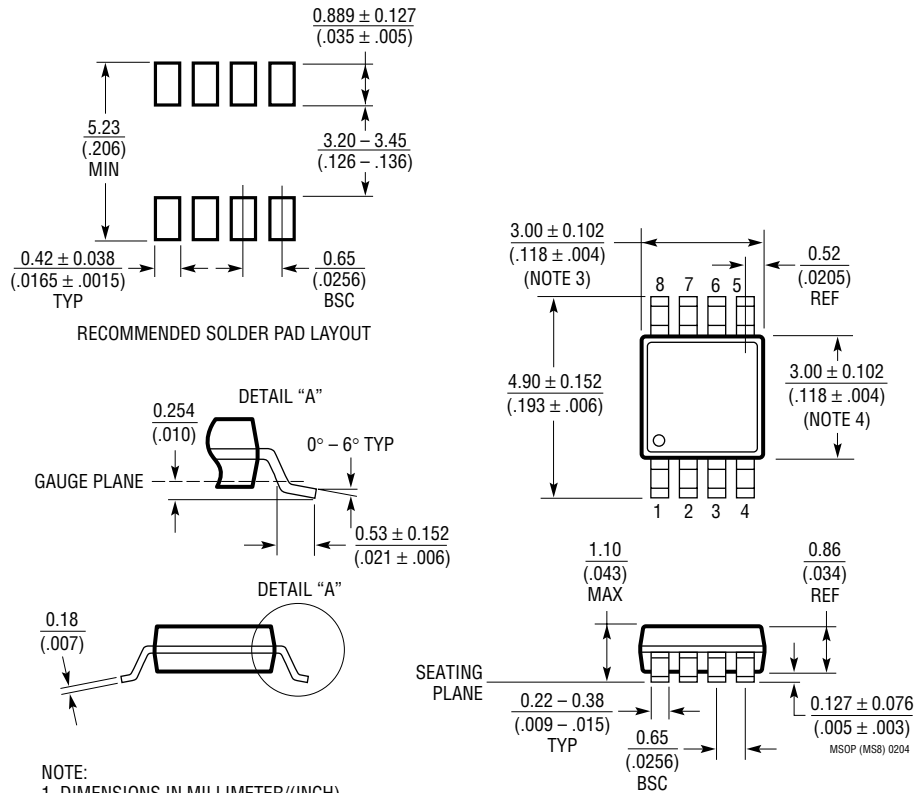
- NOTE:
1. DIMENSIONS IN  $\frac{\text{INCHES}}{\text{MILLIMETERS}}$
  2. DRAWING NOT TO SCALE
  3. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .006" (0.15mm)

S08 0303

60112fb

**PACKAGE DESCRIPTION**

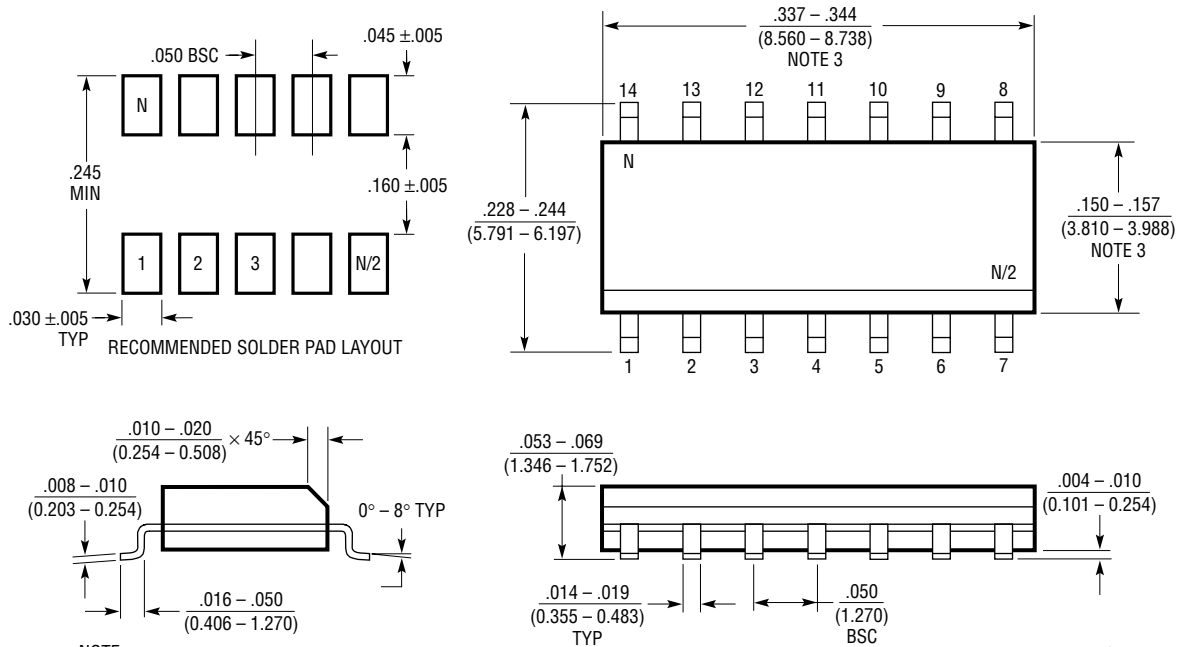
**MS8 Package**  
**8-Lead Plastic MSOP**  
 (Reference LTC DWG # 05-08-1660)



- NOTE:
1. DIMENSIONS IN MILLIMETER/(INCH)
  2. DRAWING NOT TO SCALE
  3. DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.  
MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
  4. DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.  
INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.152mm (.006") PER SIDE
  5. LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.102mm (.004") MAX

**PACKAGE DESCRIPTION**

**S14 Package**  
**14-Lead Plastic Small Outline (Narrow .150 Inch)**  
 (Reference LTC DWG # 05-08-1610)

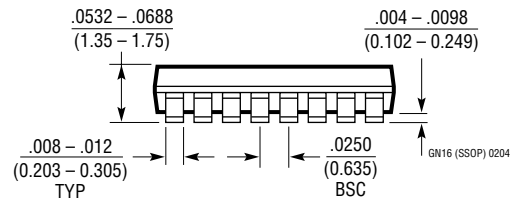
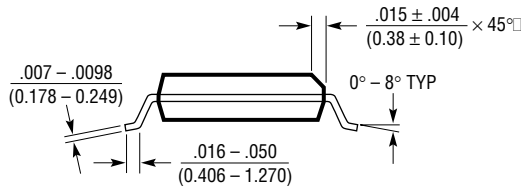
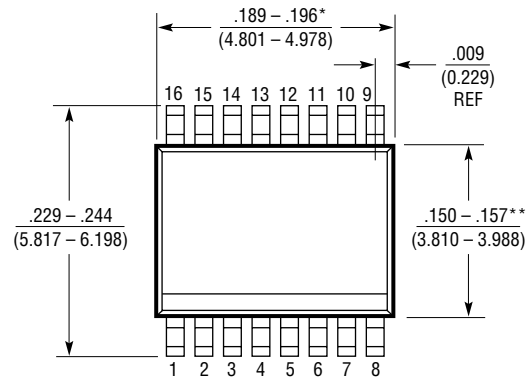
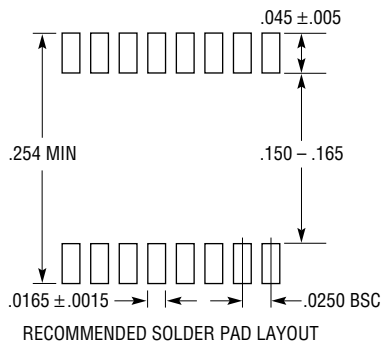


- NOTE:
1. DIMENSIONS IN  $\frac{\text{INCHES}}{\text{MILLIMETERS}}$
  2. DRAWING NOT TO SCALE
  3. THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED  $.006''$  (0.15mm)

S14 0502

# PACKAGE DESCRIPTION

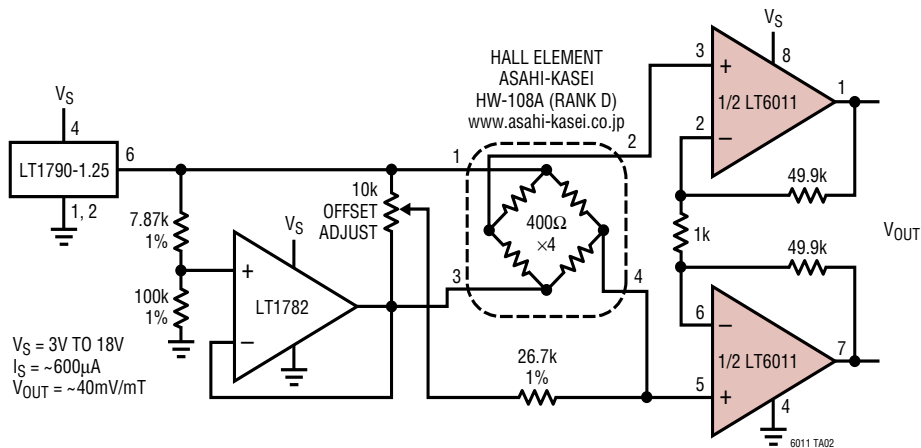
## GN Package 16-Lead Plastic SSOP (Narrow .150 Inch) (Reference LTC DWG # 05-08-1641)



- NOTE:
1. CONTROLLING DIMENSION: INCHES
  2. DIMENSIONS ARE IN  $\frac{\text{INCHES}}{\text{(MILLIMETERS)}}$
  3. DRAWING NOT TO SCALE
- \* DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE
- \*\* DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

## TYPICAL APPLICATION

### Low Power Hall Sensor Amplifier



## RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1112/LT1114	Dual/Quad Low Power, Picoamp Input Precision Op Amp	250pA Input Bias Current
LT1880	Rail-to-Rail Output, Picoamp Input Precision Op Amp	SOT-23
LT1881/LT1882	Dual/Quad Rail-to-Rail Output, Picoamp Input Precision Op Amp	$C_{LOAD}$ Up to 1000pF
LT1884/LT1885	Dual/Quad Rail-to-Rail Output, Picoamp Input Precision Op Amp	9.5nV/ $\sqrt{Hz}$ Input Noise
LT1991/LT1996	Precision, 100µA Gain-Selectable Amplifier	LT6011-Like Op Amp with 0.04% Matched Resistors
LT6010	Single 135µA, 14nV/ $\sqrt{Hz}$ Rail-to-Rail Output Precision Op Amp	35µV Maximum $V_{OS}$ ; 100pA Maximum $I_B$ ; Shutdown
LT6013/LT6014	Single/Dual 145µA, 9.5nV/ $\sqrt{Hz}$ , Rail-to-Rail Output Precision Op Amp	$A_V \geq 5$ Stable; 1.4MHz GBW