# Freescale Semiconductor Data Sheet Addendum

MC9S08DZ128AD Rev. 0, 10/2010

# MC9S08DZ128 Data Sheet Errata

by: Microcontroller Division

This errata document describes corrections to the *MC9S08DZ128 Data Sheet*, order number MC9S08DZ128. For convenience, the addenda items are grouped by revision. Please check our website at www.freescale.com for the latest updates.

The current version available of the MC9S08DZ128 Data Sheet is Revision 1.

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### 1 Errata for Revision 1

Table 1. MC9S08DZ128 Rev 1 Errata

Pin assignm				escription		
information i				5 in table 2-1	required updat	ing. The correct
Pir	ո Numl	ber	<-	- Lowest Pi	riority> Hig	hest
100	64	48			Alt 1	Alt 2
9	7	4				$V_{DD}$
10	8	5				V <sub>SS</sub>
11	9	6	PTG0		EXTAL	
12	10	7	PTG1		XTAL	
13	11	8				RESET
14	_	_	PTJ2	PIJ2	TPM3CH2	
15	_	_	PTJ3	PIJ3	ТРМ3СН3	
	100 9 10 11 12 13 14	100 64 9 7 10 8 11 9 12 10 13 11 14 —	9 7 4 10 8 5 11 9 6 12 10 7 13 11 8 14 — —	100 64 48 Pin/lr 9 7 4 10 8 5 11 9 6 PTG0 12 10 7 PTG1 13 11 8 14 — PTJ2	100         64         48         Port Pin/Interrupt           9         7         4           10         8         5           11         9         6         PTG0           12         10         7         PTG1           13         11         8           14         —         PTJ2         PIJ2	100         64         48         Port Pin/Interrupt         Alt 1           9         7         4 </td

## 2 Revision History

Table 2 provides a revision history for this document.

**Table 2. Revision History Table** 

Rev. Number	Substantive Changes	Date of Release
0	Initial release.	10/2010

2 Freescale Semiconductor

**Revision History** 

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