



## Corrections of Hardware Manual

# MB90340

## ds90340-ds07-13730-3e-corr-x1-01

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### Addendum, MB90340 Datasheet (DS07-13730-3E)

This is the Addendum for the Datasheet DS07-13730-3E of the MB90340 microcontroller series. It describes all known discrepancies of the MB90340 microcontroller series datasheet.

Ref. Number (Text Link)	Date dd.mm.yy	Version No.	Chapter/Page	Description/Correction
<a href="#">DS90340001</a>	18.07.05	1.00	Page 15	Typo
<a href="#">DS90340002</a>	03.07.06	1.01	Page 53	Typo in Remarks, pin listing

Page 15, Typo in pin description

OLD:

PIN No.		PIN Name	Circuit type	Function
LQFP100*2	QFP100*1			
...	...	...	...	...
8	10	P35	G	General purpose I/O. The register can be set to select whether to use a pull-up resistor. This function is enabled either in single-chip mode or with the hold function disabled.
		$\overline{\text{HAK}}$		Hold acknowledge output pin. This function is enabled when both the external bus and the hold function are enabled.
		OUT5		Waveform output pin for output compare OCU6
9	11	P36	G	General purpose I/O. The register can be set to select whether to use a pull-up resistor. This function is enabled either in single-chip mode or with the external ready function disabled.
		RDY		Ready input pin. This function is enabled when both the external bus and the external ready function are enabled.
		OUT6		Waveform output pin for output compare OCU5
...	...	...	...	...

Correction:

PIN No.		PIN Name	Circuit type	Function
LQFP100*2	QFP100*1			
...	...	...	...	...
8	10	P35	G	General purpose I/O. The register can be set to select whether to use a pull-up resistor. This function is enabled either in single-chip mode or with the hold function disabled.
		$\overline{\text{HAK}}$		Hold acknowledge output pin. This function is enabled when both the external bus and the hold function are enabled.
		OUT5		Waveform output pin for output compare <b>OCU5</b>
9	11	P36	G	General purpose I/O. The register can be set to select whether to use a pull-up resistor. This function is enabled either in single-chip mode or with the external ready function disabled.
		RDY		Ready input pin. This function is enabled when both the external bus and the external ready function are enabled.
		OUT6		Waveform output pin for output compare <b>OCU6</b>
...	...	...	...	...

Page 53, Typo in Remarks, pin listing

Chapter: ELECTRICAL CHARACTERISTICS, 3. DC characteristics

Input H voltage (At Vcc = 5V +/- 10%), row V<sub>IHS</sub>, Column Remarks:

OLD:

Port inputs if CMOS hysteresis input levels are selected (except P12, P44, P45, P46, P47, P50, P82, P83)

Correction:

Port inputs if CMOS hysteresis input levels are selected (except P12, P44, P45, P46, P47, P50, P82, **P85**)

Input L voltage (At Vcc = 5V +/- 10%), row V<sub>ILS</sub>, Column Remarks:

OLD:

Port inputs if CMOS hysteresis input levels are selected (except P12, P44, P45, P46, P47, P50, P82, P83)

Correction:

Port inputs if CMOS hysteresis input levels are selected (except P12, P44, P45, P46, P47, P50, P82, **P85**)

(JRo, KDi)