
AVR524: Migration from ATmega64 to ATmega64A



8-bit **AVR**[®]
Microcontrollers

Application Note

1 Introduction

In order to optimize the manufacturing process and to further reduce current consumption, an optimized version of ATmega64 has been introduced.

The ATmega64A is a functionally identical, drop-in replacement for the ATmega64. All devices are subject to the same qualification process and same set of production tests, but as the manufacturing process is not the same some electrical characteristics differ.

ATmega64 and ATmega64A have separate datasheets. This application note outlines the differences between the two devices and the datasheets. There is also a detailed change log to assist the user at the end of the ATmega64A datasheet. Remember to always use the latest revision of the device datasheet.

Minor differences in typical characteristics are not discussed in this document as long as the low and high limits remain the same. For detailed information about the typical characteristics, see sections “Electrical Characteristics” and “Typical Characteristics” of the device datasheets.

Note: This application note serves as a guide to ease migration. For complete device details, always refer to the most recent version of the ATmega64A datasheet.

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2 Changes in Characteristics

This section outlines major differences in characteristics that may have an effect on the application in which the device is used. For detailed information, refer to the most recent version of the device datasheets.

2.1 Current Consumption

Active and Idle mode current consumption of the device has been reduced significantly. The tables below present typical current consumption figures at room temperature. All values are taken from device datasheets, unless otherwise noted.

Table 2-1. Typical Current Consumption of Device at Room Temperature

| Mode | Condition | ATmega64 | ATmega64A | Change |
|--------|-----------------------------|----------|-----------|--------|
| Active | $V_{CC}=3V, f=4\text{ MHz}$ | 4.1 mA | 2.5 mA | - 39% |
| | $V_{CC}=5V, f=8\text{ MHz}$ | 15,5 mA | 9 mA | - 42% |
| Idle | $V_{CC}=3V, f=4\text{ MHz}$ | 2 mA | 0.7 mA | - 65% |
| | $V_{CC}=5V, f=8\text{ MHz}$ | 8 mA | 2.8 mA | - 65% |
| Reset | $V_{CC}=3V, f=4\text{ MHz}$ | 3 mA | 2.5 mA | - 17% |
| | $V_{CC}=5V, f=8\text{ MHz}$ | 13 mA | 7.8 mA | - 40% |

2.2 VOL Levels

In Table 2-2 are listed differences in output low and high voltage levels.

Table 2-2. Changes to VOL and VOH Levels

| Symbol | Parameter | Condition | ATmega64 | | ATmega64A | | Units |
|----------|-------------------------------------|---------------------------------|----------|-----|-----------|-----|-------|
| | | | Min | Max | Min | Max | |
| V_{OL} | Output Low Voltage (Ports A - G) | $I_{OL}=20\text{mA}, V_{CC}=5V$ | - | 0.7 | - | 0.9 | V |
| | | $I_{OL}=10\text{mA}, V_{CC}=3V$ | - | 0.5 | - | 0.6 | V |

3 Datasheet Changes

For a summary of changes, see the datasheet revision history at the end of the ATmega64A datasheet.



Headquarters

Atmel Corporation
2325 Orchard Parkway
San Jose, CA 95131
USA
Tel: 1(408) 441-0311
Fax: 1(408) 487-2600

International

Atmel Asia
Unit 1-5 & 16, 19/F
BEA Tower, Millennium City 5
418 Kwun Tong Road
Kwun Tong, Kowloon
Hong Kong
Tel: (852) 2245-6100
Fax: (852) 2722-1369

Atmel Europe
Le Krebs
8, Rue Jean-Pierre Timbaud
BP 309
78054 Saint-Quentin-en-
Yvelines Cedex
France
Tel: (33) 1-30-60-70-00
Fax: (33) 1-30-60-71-11

Atmel Japan
9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
Tel: (81) 3-3523-3551
Fax: (81) 3-3523-7581

Product Contact

Web Site
<http://www.atmel.com/>

Technical Support
avr@atmel.com

Sales Contact
www.atmel.com/contacts

Literature Request
www.atmel.com/literature

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