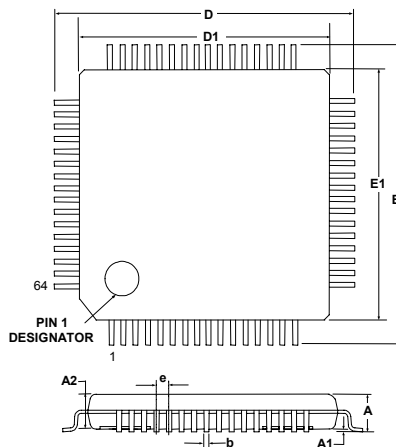


Selected Electrical Specifications

($T_A = -40$ to $+85$ °C, $V_{DD} = 2.7$ V unless otherwise specified)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
GLOBAL CHARACTERISTICS					
Supply Voltage		2.7		3.6	V
Supply Current (CPU active)	Clock = 50 MHz Clock = 1 MHz Clock = 32 kHz		25 0.5 16		mA mA μA
Supply Current (shutdown)	Oscillator off; V_{DD} Monitor Enabled Oscillator off; V_{DD} Monitor Disabled		10 <0.1		μA μA
Clock Frequency Range		DC		50	MHz
INTERNAL CLOCKS					
Oscillator Frequency		24.0	24.5	25.0	MHz
PLL Frequency		48	49	50	MHz
A/D CONVERTER					
Resolution			12		bits
Integral Nonlinearity				±1	LSB
Differential Nonlinearity	Guaranteed Monotonic			±1	LSB
Signal-to-Noise Plus Distortion		66	69		dB
Throughput Rate				100	ksps
D/A CONVERTERS					
Resolution			12		bits
Differential Nonlinearity	Guaranteed Monotonic			±1	LSB
Output Settling Time			10		μS
COMPARATORS					
Supply Current	(each Comparator)		1.5		μA
Response Time	(CP+) – (CP-) = 100 mV		4.0		μS

Package Information



	MIN (mm)	NOM (mm)	MAX (mm)
A	-	-	1.20
A1	0.05	-	0.15
A2	0.95	-	1.05
b	0.17	0.22	0.27
D	-	12.00	-
D1	-	10.00	-
e	-	0.50	-
E	-	12.00	-
E1	-	10.00	-

C8051F120DK Development Kit

