



PIC18F2450/4450

PIC18F2450/4450 Rev. A3 Silicon Errata

The PIC18F2450/4450 parts you have received conform functionally to the Device Data Sheet (DS39760B), except for the anomalies described below. Any Data Sheet Clarification issues related to the PIC18F2450/4450 devices will be reported in a separate Data Sheet errata. Please check the Microchip web site for any existing issues.

The following silicon errata apply only to PIC18F2450/4450 devices with these Device/Revision IDs:

Part Number	Device ID	Revision ID
PIC18F2450	0001 0100 001	0 0010
PIC18F4450	0001 0100 000	0 0010

The Device IDs (DEVID1 and DEVID2) are located at addresses 3FFFFEh:3FFFFh in the device's configuration space. They are shown in hexadecimal in the format "DEVID2 DEVID1".

All of the issues listed here will be addressed in future revisions of the PIC18F2450/4450 silicon.

1. Module: EUSART

In Synchronous Master mode, while transmitting the Most Significant data bit, the data line (DT) may change state before the bit finishes transmitting. If the receiver samples the data line later than 0.5 bit times + 1.5 T_{cy} (of the master) after the starting edge of the MSb, the bit may be read incorrectly.

Work around

None.

Date Codes that pertain to this issue:

All engineering and production devices.

2. Module: 10-Bit Analog-to-Digital (A/D) Converter Module

When the A/D clock source is selected as 2 T_{osc} or RC (when ADCS2:ADCS0 = 000 or x11), in extremely rare cases, the EIL (Integral Linearity Error) and EDL (Differential Linearity Error) may exceed the data sheet specification at codes 511 and 512 only.

Work around

Select a different A/D clock source (4 T_{osc}, 8 T_{osc}, 16 T_{osc}, 32 T_{osc} or 64 T_{osc}) and avoid selecting the 2 T_{osc} or RC modes.

Date Codes that pertain to this issue:

All engineering and production devices.

PIC18F2450/4450

REVISION HISTORY

Rev A Document (3/2007)

Initial version of this document. Silicon issues 1 (EUSART) and 2 (A/D – Offset).

Rev B Document (7/2007)

Removed silicon issue 2 (A/D – Offset). Added new silicon issue 2 (10-Bit A/D Converter Module).

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06/25/07