



# MICROCHIP PIC18F85J11 FAMILY

## PIC18F85J11 Family Rev. A3 Silicon Errata

The PIC18F85J11 Family Rev. A3 parts you have received conform functionally to the Device Data Sheet (DS39774B), except for the anomalies described below. Any Data Sheet Clarification issues related to the PIC18F85J11 Family will be reported in a separate Data Sheet errata. Please check the Microchip web site for any existing issues.

**The following silicon errata apply only to PIC18F85J11 Family devices with these Device/Revision IDs:**

Part Number	Device ID	Revision ID
PIC18F63J11	0011 1001 000	0 0011
PIC18F64J11	0011 1001 001	0 0011
PIC18F65J11	0011 1001 011	0 0011
PIC18F83J11	0011 1001 100	0 0011
PIC18F84J11	0011 1001 101	0 0011
PIC18F85J11	0011 1001 111	0 0011

The Device IDs (DEVID1 and DEVID2) are located at addresses 3FFFEh:3FFFFh in the device's configuration space. They are shown in hexadecimal in the format "DEVID2 DEVID1".

All of the issues listed here will be addressed in future revisions of the PIC18F85J11 Family silicon.

### 1. Module: Resets

When a Brown-out Reset (BOR) occurs and the  $\overline{\text{BOR}}$  bit is reset, the Power-on Reset ( $\overline{\text{POR}}$ ) bit also may be reset. The resulting state matches that of the RCON register following a Power-on Reset event.

Consequently, an application may not be able to detect whether a BOR or POR event has occurred.

#### Work around

None.

#### Date Codes that pertain to this issue:

All engineering and production devices.

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## REVISION HISTORY

Rev A Document (4/2007)

Initial release of this document. Silicon issue 1 (Resets).

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**Note the following details of the code protection feature on Microchip devices:**

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
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