Notice for TAIYO YUDEN products

Please read this notice before using the TAIYO YUDEN products.

/!\ REMINDERS

■ Product information in this catalog is as of October 2008. All of the contents specified herein are subject to change without notice due to technical improvements, etc. Therefore, please check for the latest information carefully before practical application or usage of the Products.

Please note that Taiyo Yuden Co., Ltd. shall not be responsible for any defects in products or equipment incorporating such products, which are caused under the conditions other than those specified in this catalog or individual specification.

- Please contact Taiyo Yuden Co., Ltd. for further details of product specifications as the individual specification is available.
- Please conduct validation and verification of products in actual condition of mounting and operating environment before commercial shipment of the equipment.
- All electronic components or functional modules listed in this catalog are developed, designed and intended for use in general electronics equipment. (for AV, office automation, household, office supply, information service, telecommunications, (such as mobile phone or PC) etc.). Before incorporating the components or devices into any equipment in the field such as transportation, (automotive control, train control, ship control), transportation signal, disaster prevention, medical, public information network (telephone exchange, base station) etc. which may have direct influence to harm or injure a human body, please contact Taiyo Yuden Co., Ltd. for more detail in advance.

Do not incorporate the products into any equipment in fields such as aerospace, aviation, nuclear control, submarine system, military, etc. where higher safety and reliability are especially required.

In addition, even electronic components or functional modules that are used for the general electronic equipment, if the equipment or the electric circuit require high safety or reliability function or performances, a sufficient reliability evaluation check for safety shall be performed before commercial shipment and moreover, due consideration to install a protective circuit is strongly recommended at customer's design stage.

- The contents of this catalog are applicable to the products which are purchased from our sales offices or distributors (so called "TAIYO YUDEN's official sales channel"). It is only applicable to the products purchased from any of TAIYO YUDEN's official sales channel.
- Please note that Taiyo Yuden Co., Ltd. shall have no responsibility for any controversies or disputes that may occur in connection with a third party's intellectual property rights and other related rights arising from your usage of products in this catalog. Taiyo Yuden Co., Ltd. grants no license for such rights.
- Caution for export

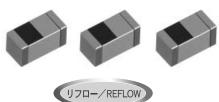
Certain items in this catalog may require specific procedures for export according to "Foreign Exchange and Foreign Trade Control Law" of Japan, "U.S. Export Administration Regulations," and other applicable regulations. Should you have any question or inquiry on this matter, please contact our sales staff.

Should you have any question or inquiry on this matter, please contact our sales staff.

高周波チップ積層インダクタ(High Qタイプ) **MULTILAYER CHIP INDUCTOR FOR HIGH FREQUENCY** HIGH Q TYPE

AQ SERIES

105 : −55~125°C * OPERATING TEMP. -55~85°C*



- *保証定格により変わります。
- *Operating temperature depends on rated current.

特長 FEATURES

- ・高周波領域でQが高く、自己共振周波数が高いため高周波回路に最適です
- ・実装性、耐熱性に優れ、巻線インダクタの置き換えに最適です・2nHから10nHではE24系列を実現しており、回路設計が容易です
- ・モノリシック構造のため、高信頼性です

- · High frequency inductors with high Q and high SRF suitable for high frequency circuit.
- · Excellent mountability and heat-resistance suitable for replacement of wire-wound inductors.
- · E24 series lineup in a range from 2nH to 10nH makes circuit design easy.
- · Monolithic structure provides high-reliability.

APPLICATIONS

- ・携帯電話、無線LAN
- ・高周波モジュール チューナー
- ·高周波回路全般

- · Mobile telephone, Wireless LAN
- · High frequency module
- Tuner
- · High-frequency circuits

形名表記法 ORDERING CODE

O

形式 高周波チップインダクタ

形状寸法 (L×W) [mm] 105 (0402) 1.0×0.6

公称インダクタンス〔nH〕 例 3N9 10N 10

※N=nHとしての小数点

インダクタンス許容差 Н ± 3% \pm 5% ±0.2nH

±0.3nH

包装 リールテーピング





Type Chip inductors for high AΩ frequency High Q type

External Dimensions (mm) 105 (0402) 1.0×0.6

10N

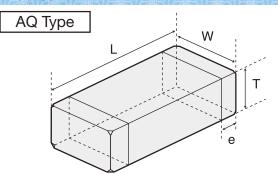
Nominal Inductance (nH) Example 3N9

> 10 *N=0.0 (nH type)

Inductance Tolerances ± 3% ± 5% ±0.2nH Н

Packaging Tape & Reel

外形寸法 EXTERNAL DIMENSIONS



Туре	L	W	Т	е
AQ105	1.0 ± 0.05	0.6 ± 0.1	0.5 ± 0.05	0.175 ± 0.075
(0402)	(0.039 ± 0.002)	(0.024 ± 0.004)	(0.020 ± 0.002)	(0.007 ± 0.003)

Unit: mm (inch)

概略バリエーション AVAILABLE INDUCTANCE RANGE

Range	Туре		AQ105	
		使用温度範囲	–55∼+125℃ Imax	−55~+85°C Imax
	[nH]		[mA]	[mA]
	1.0	1N0	710	930
	1.2	1N2□	710	930
	1.5	1N5	710	930
Ξ	1.8	1N8□	710	930
	2.2	2N2□	660	870
inductance	2.7	2N7□	630	820
cta	3.3	3N3□	540	710
ήp	3.9	3N9□	490	630
.⊑	4.7	4N7□	450	590
	5.6	5N6□	420	550
	6.8	6N8○	390	510
	8.2	8N2O	360	470
	10.0	10NO	330	440
	12.0	12N〇	300	390
	15.0	15NO	280	360

值 iles	Inductance	lmax	[mA]	Rdcmax[Ω]
₩ di		-55~+125℃	-55~+85°C	
₹ÿ	1.5nH	710	930	0.07
	10.0nH	330	440	0.31

※形名の□、○にはインダクタンス許容差記号が入ります。±0.3nH(□)、±5%(○)以下の許容差も対応可能ですので、お問い合わせ下さい。 \square , \bigcirc mark indicates the Inductance tolerance code. The product with tolerance less than ± 0.3 nH (\square), $\pm 5\%$ (\bigcirc) is also available. Please contact your local sales office.











アイテム一覧 PART NUMBERS

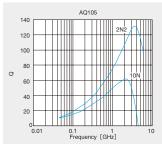
AQ105 -

	EHS	1. 4					/	.\			長周波数	直流	抵抗	定格		厚さ
形名	(Environmental	インダクタンス	Q	LQ測定周波数			(Typic			Self-re	sonant	DC.Res	sistance	Rated		Thickness
Ordering code	Hazardous	Inductance	min.	Measuring frequency	周波	釵 Fr	equer	icy [M	Hz]	frequ [M		[(2)	(m ma		[mm]
	Substances)	(nH)		[MHz]	300	800	900	1500	1800		Typ.	max.	_	−55~ +125°C		(inch)
AQ 105 1N0 🗌	RoHS	1.0±0.3nH	8	100	53	129	147	217	244	10000	>13000	0.07	0.014	710		
AQ 105 1N2 🗌	RoHS	1.2±0.3nH	8	100	45	97	110	156	177	10000	>13000	0.07	0.016	710	930	
AQ 105 1N5 🗌	RoHS	1.5±0.3nH	8	100	35	69	76	104	116	8000	>13000	0.07	0.030	710	930	
AQ 105 1N8 🗌	RoHS	1.8±0.3nH	8	100	32	61	66	92	100	6000	11000		0.035	710	930	
AQ 105 2N0 🗌	RoHS	$2.0 \pm 0.3 \text{nH}$	8	100	38	68	73	94	103	6000	10500	0.08	0.035	660	870	
AQ 105 2N2 🗌	RoHS	2.2±0.3nH	8	100	37	67	71	92	101	6000	10000		0.040	660	870	
AQ 105 2N4 🗌	RoHS	2.4±0.3nH	8	100	34	54	59	74	86	6000	9600	0.09	0.050	630	820	
AQ 105 2N7 🗌	RoHS	2.7 ± 0.3 nH	8	100	30	49	52	67	73	6000	9200	0.09	0.060	630	820	
AQ 105 3N0 🗌	RoHS	$3.0 \pm 0.3 \text{nH}$	8	100	31	51	54	70	76	6000	8700	0.11	0.070	570	740	
AQ 105 3N3 🗌	RoHS	$3.3 \pm 0.3 \text{nH}$	8	100	32	54	57	72	79	6000	8300		0.075	540	710	
AQ 105 3N6 🗌	RoHS	$3.6 \pm 0.3 \text{nH}$	8	100	33	53	56	71	77	5000	7800	0.14	0.080	500	650	
AQ 105 3N9 🗌	RoHS	$3.9 \pm 0.3 \text{nH}$	8	100	34	53	56	70	76	4000	7300		0.085	490	630	0.50±0.05
AQ 105 4N3 🗌	RoHS	$4.3 \pm 0.3 \text{nH}$	8	100	29	47	50	64	71	4000	6900	0.16	0.090	470	610	(0.020±0.002)
AQ 105 4N7 🗌	RoHS	4.7±0.3nH	8	100	30	48	51	65	72	4000	6400	0.17	0.095	450	590	
AQ 105 5N1 🗌	RoHS	$5.1 \pm 0.3 \text{nH}$	8	100	30	48	51	64	71	4000	6300	0.19	0.110	430	560	
AQ 105 5N6 🗌	RoHS	5.6±0.3nH	8	100	30	48	51	65	71	4000	6200	0.20	0.120	420	550	
AQ 105 6N2 🗌	RoHS	$6.2 \pm 0.3 \text{nH}$	8	100	31	49	52	66	72	3900	6100	0.22	0.130	400	520	
AQ 105 6N8 O	RoHS	6.8±5%	8	100	28	44	49	59	64	3900	6000	0.23	0.130	390	510	
AQ 105 7N5 🔾	RoHS	7.5±5%	8	100	28	45	50	60	65	3700	5500	0.25	0.135	370	490	
AQ 105 8N2 O	RoHS	8.2±5%	8	100	29	46	50	62	66	3600	5000	0.27	0.140	360	470	
AQ 105 9N1 O	RoHS	9.1±5%	8	100	29	45	49	59	62	3400	4800	0.29	0.150	350	450	
AQ 105 10N O	RoHS	10±5%	8	100	28	45	48	57	60	3200	4500	0.31	0.165	330	440	
AQ 105 12N O	RoHS	12±5%	8	100	26	40	45	51	52	2700	4300	0.39	0.165	300	390	
AQ 105 15N O	RoHS	15±5%	8	100	25	38	42	49	51	2300	4100	0.45	0.190	280	360	

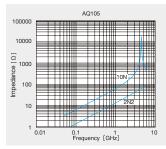
[※]形名の□、○にはインダクタンス許容差記号が入ります。±0.3nH (□)、±5% (○) 以下の許容差も対応可能ですので、お問い合わせ下さい。 □, ○mark indicates the Inductance tolerance code. The product with tolerance less than ±0.3nH (□), ±5% (○) is also available. Please contact your local sales office.

特性図 ELECTRICAL CHARACTERISTICS

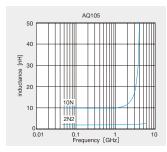
Q-周波数特性例 Q-Characteristics(Measured by HP8719C)



インピーダンス周波数特性例 Impedance-vs-Frequency characteristics (Measured by HP8719C)

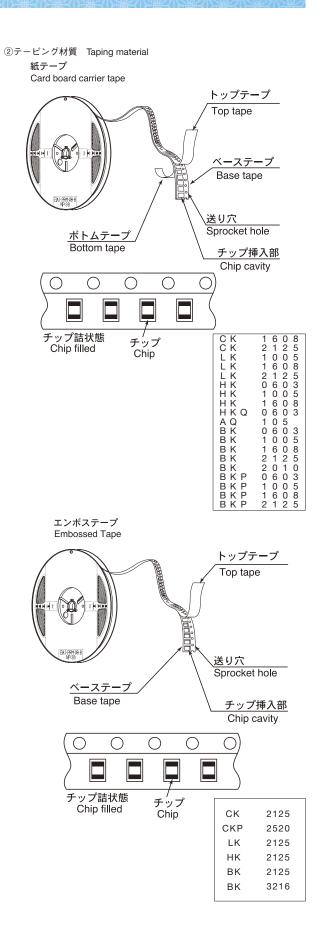


インダクタンス周波数特性例 Inductance-vs-Frequency characteristics (Measured by HP8719C)



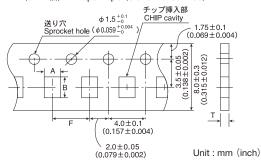
①最小受注単位数 Minimum Quantity ■テーピング梱包 Tape & Reel Packaging

製品厚み		文量 [pcs] rd Quantity
[mm] (inch)	紙テープ Paper Tape	エンボステープ Embossed Tape
0.8 (0.031)	4000	_
0.85 (0.033)	4000	_
(0.049)	_	2000
(0.035)	_	3000
(0.043)	_	2000
(0.020)	10000	_
(0.031)	4000	_
0.85 (0.033)	4000	_
1.25 (0.049)	_	2000
0.3 (0.012)	15000	_
0.5 (0.020)	10000	_
0.8 (0.031)	4000	_
0.85	_	4000
1.0 (0.039)	_	3000
0.3 (0.012)	15000	_
0.5 (0.020)	10000	_
0.3 (0.012)	15000	_
0.5 (0.020)	10000	_
0.8 (0.031)	4000	_
0.85 (0.033)	4000	_
1.25 (0.049)		2000
0.45 (0.018)	4000	_
0.8	_	4000
(0.031)		
(0.031) 0.3 (0.012)	15000	_
0.3	15000	-
0.3 (0.012) 0.5		- -
	Thickness [mm] (inch) 0.8 (0.031) 0.85 (0.033) 1.25 (0.049) 0.9 (0.035) 1.1 (0.043) 0.5 (0.020) 0.8 (0.031) 0.85 (0.033) 1.25 (0.049) 0.3 (0.012) 0.5 (0.020) 0.8 (0.031) 0.85 (0.033) 1.0 (0.039) 0.3 (0.012) 0.5 (0.020) 0.8 (0.031) 0.85 (0.033) 1.0 (0.039) 0.3 (0.012) 0.5 (0.020) 0.8 (0.031) 0.85 (0.033) 1.0 (0.039) 0.3 (0.012) 0.5 (0.020) 0.8 (0.031) 0.5 (0.020) 0.3 (0.012) 0.5 (0.020) 0.3 (0.012) 0.5 (0.020) 0.3 (0.012) 0.5 (0.020) 0.3 (0.012) 0.5 (0.020) 0.3 (0.012) 0.5 (0.020) 0.8 (0.031) 0.85 (0.033) 1.25 (0.049) 0.45 (0.018)	Thickness [mm] (inch)



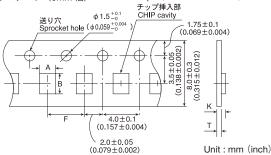
③テーピング寸法 Taping Dimensions

・紙テープ (8mm幅) Paper tape (0.315 inches wide)



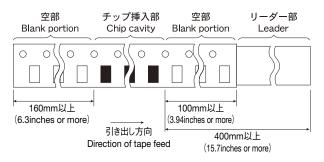
形 式 Type	製品厚み Thickness (mm)	チップ Chip・		挿入ピッチ Insertion Pitch	テープ厚み Tape Thickness
	(inch)	A	В	F	Т
CK1608 (0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1m a x (0.043max)
CK2125(0805)	0.85	1.5±0.2	2.3±0.2	4.0±0.1	1.1m a x
	(0.033)	(0.059±0.008)	(0.091±0.008)	(0.157±0.004)	(0.043max)
LK1005 (0402)	0.5	0.65±0.1	1.15±0.1	2.0±0.05	0.8ma x
	(0.020)	(0.026±0.004)	(0.045±0.004)	(0.079±0.002)	(0.031max)
LK1608 (0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1m a x (0.043max)
LK2125 (0805)	0.85	1.5±0.2	2.3±0.2	4.0±0.1	1.1m a x
	(0.033)	(0.059±0.008)	(0.091±0.008)	(0.157±0.004)	(0.043max)
HK0603 (0201)	0.3	0.40±0.06	0.70±0.06	2.0±0.05	0.45max
	(0.012)	(0.016±0.002)	(0.028±0.002)	(0.079±0.002)	(0.018max)
HK1005(0402)	0.5	0.65±0.1	1.15±0.1	2.0±0.05	0.8ma x
	(0.020)	(0.026±0.004)	(0.045±0.004)	(0.079±0.002)	(0.031max)
HK1608 (0603)	0.8	1.0±0.2	1.8±0.2	4.0±0.1	1.1m a x
	(0.031)	(0.039±0.008)	(0.071±0.008)	(0.157±0.004)	(0.043max)
HKQ0603S(0201)	0.3	0.40±0.06	0.70±0.06	2.0±0.05	0.45max
	(0.012)	(0.016±0.002)	(0.028±0.002)	(0.079±0.002)	(0.018max)
AQ105(0402)	0.5	0.75±0.1	1.15±0.1	2.0±0.05	0.8ma x
	(0.020)	(0.030±0.004)	(0.045±0.004)	(0.079±0.002)	(0.031max)
BK0603(0201)	0.3 (0.012)	0.40±0.06 (0.016±0.002)	0.70±0.06 (0.028±0.002)	2.0±0.05 (0.079±0.002)	0.45max (0.018max)
BK1005(0402)	0.5	0.65±0.1	1.15±0.1	2.0±0.05	0.8ma x
	(0.020)	(0.026±0.004)	(0.045±0.004)	(0.079±0.002)	(0.031max)
BK1608 (0603)	0.8	1.0±0.2	1.8±0.2	4.0±0.1	1.1m a x
	(0.031)	(0.039±0.008)	(0.071±0.008)	(0.157±0.004)	(0.043max)
BK2125 (0805)	0.85	1.5±0.2	2.3±0.2	4.0±0.1	1.1m a x
	(0.033)	(0.059±0.008)	(0.091±0.008)	(0.157±0.004)	(0.043max)
BK2010 (0804)	0.45	1.2±0.1	2.17±0.1	4.0±0.1	0.8ma x
	(0.018)	(0.047±0.004)	(0.085±0.004)	(0.157±0.004)	(0.031max)
BKP0603(0201)	0.3 (0.012)	0.40±0.06 (0.016±0.002)	0.70±0.06 (0.028±0.002)	2.0±0.05 (0.079±0.002)	0.45max (0.018max)
BKP1005(0402)	0.5 (0.020)	0.65±0.1 (0.026±0.004)	1.15±0.1 (0.045±0.004)	2.0±0.05 (0.079±0.002)	0.8ma x (0.031max)
BKP1608(0603)	0.8 (0.031)	1.0±0.2 (0.039±0.008)	1.8±0.2 (0.071±0.008)	4.0±0.1 (0.157±0.004)	1.1m a x (0.043max)
BKP2125 (0805)	0.85 (0.033)	1.5±0.2 (0.059±0.008)	2.3±0.2 (0.091±0.008)	4.0±0.1 (0.157±0.004)	1.1m a x (0.043max)

・エンボステープ (8mm 幅) Embossed Tape (0.312 inches wide)

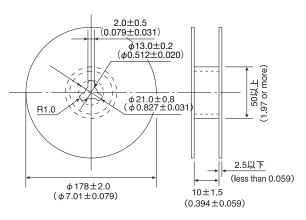


形 式 Type	製品厚み Thickness (mm)	チップ Chip (挿入部 cavity	挿入ピッチ Insertion Pitch	テーフ Ta Thick	ре
	(inch)	Α	В	F	K	Т
CK2125(0805)	1.25	1.5±0.2	2.3±0.2	4.0±0.1	2.0	0.3
GN2 123 (0603)	(0.049)	(0.059±0.008)	(0.091±0.008)	(0.157±0.004)	(0.079)	(0.012)
	0.9				1.4	
CKP2520(1008)	(0.035)	2.3±0.1	2.8±0.1	4.0±0.1	(0.055)	0.3
CKP2520 (1008)	1.1	(0.091±0.004)	(0.110±0.004)	(0.157±0.004)	1.7	(0.012)
	(0.043)				(0.067)	
LK2125 (0805)	1.25	1.5±0.2	2.3±0.2	4.0±0.1	2.0	0.3
LN2 120 (0800)	(0.049)	(0.059±0.008)	(0.091±0.008)	(0.157±0.004)	(0.079)	(0.012)
	0.85				1.5	
HK2125(0805)	(0.033)	1.5±0.2	2.3±0.2	4.0±0.1	(0.059)	0.3
HK2123(0803)	1.0	(0.059±0.008)	(0.091±0.008)	(0.157±0.004)	2.0	(0.012)
	(0.039)				(0.079)	
BK2125(0805)	1.25	1.5±0.2	2.3±0.2	4.0±0.1	2.0	0.3
DN2123(U8U5)	(0.049)	(0.059±0.008)	(0.091 ± 0.008)	(0.157±0.004)	(0.079)	(0.012)
DK3046(4006)	0.8	1.9±0.1	3.5±0.1	4.0±0.1	1.4	0.3
BK3216(1206)	(0.031)	(0.075±0.004)	(0.138±0.004)	(0.157±0.004)	(0.055)	(0.012)

④リーダー部・空部 LEADER AND BLANK PORTION

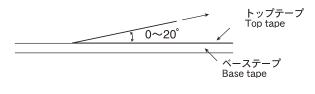


⑤リール寸法 Reel Size



⑥トップテープ強度 Top tape strength

トップテープの剥離力は、下図矢印方向にて0.1~0.7Nとなります。 The top tape requires a peel-off force of $0.1 \sim 0.7 N$ in the direction of the arrow as illustrated below.



⚠Please read the "Notice for TAIYO YUDEN products" before using this catalog.

											Specif	ied Valu	ıe										
Item					AR	RAY																	Test Methods and Remarks
	BK0603	BK1005	BK1608	BK2125	BK2010	BK3216	BKP0603	BKP1005	BKP1608	BKP2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608	HK2125	HKQ0603S	AQ105	
1. Operating Temperature Range		-	-55~-	- 125°C				-55~	+85°C				-40~	+85°C			-55~	+125℃	-40~	+85°C	-55~	+125°C	
Storage Temperature Range			-55~-	⊦125°C				-55~	+85°C				−40 ~	+85°C			-55~	+125℃	_40~	+85°C	-55~	+125℃	
3. Rated Current	100~	150~		200~	100mA	100~	1.0A	1.0A	1.0~	2.0~	50~	60~	1.1~	10~	1~	5~	60~	110~	150~		130~	280~	
	500mA DC	1000mA D C	1500mA D C	1200mA D C	DC	200mA DC	DC	DC	3.0A DC	4.0A D C	60mA DC	500mA DC	1.4 DC	25mA DC	50mA DC	300mA DC	470mA DC	300mA DC	300mA DC	300mA DC	600mA DC	710mA DC	
4. Impedance	10~ 600Ω ±25%	10~ 1000Ω ±25%	22~ 2500Ω ±25%	15~ 2500Ω ±25%	5~ 600Ω ±25%	68~ 1000Ω ±25%	22~ 33Ω ±25%	120Ω ±25%	33~ 390Ω ±25%	33~ 2200 ±25%													BK0603 Series: BKP0603 Series: Measuring frequency:100±1MHz Measuring equipment:HP4291A Measuring ijg:16193A BK1005 Series: BKP1005 Series: BKP1005 Series: Measuring frequency:100±1MHz Measuring equipment:HP4291A Measuring ijg:16192A, 16193A BK1608, 2125 Series: BKP1608, 2125 Series: BKP1608, 2125 Series: Measuring frequency:100±1MHz Measuring frequency:100±1MHz Measuring ijg:16092A or 16192A (HW)
5. Impedance											4.7~ 10.0μH : ±20%	0.1~ 10.0μH : ±20%		0.12~ 2.2μH : ±10%	0.047~ 33.0μH :±20%	0.047~ 33.0μH : ±20%		1.0~ 6.2nH :±0.3nH	1.0~ 5.6nH :±0.3nH	1.0~ 5.6nH :±0.3nH	0.6~ 6.2nH :±0.3nH	1.0~ 6.2nH :±0.3nH	BK2010, 3216 Series: Measuring frequency: 100±1MHz Measuring equipment: HP4291A, HP4195A Measuring jig: 16192A CK Series: Measuring frequency: 2 to 4MHz (CK1608) Measuring frequency: 2 to 25MHz (CK2125)
					_									Q 0.12~ 2.2μH :±30%	0.10~ 12.0µH :±10% 0 0.12~ 22.µH :±30%	0.10~ 12.0μH :±10% 0 0.12~ 2.2μH :±30%	6.8~ 100nH : ±5%	6.8~ 270nH :±5%	6.8~ 470nH :±5%	6.8∼ 470nH :±5%	6.8∼ 22nH :±5%	6.8∼ 15nH :±5%	Measuring frequency : 1MHz (CKP2520) LK Series : Measuring frequency : 10 to 25MHz (LK1005) Measuring frequency : 10 to 50MHz (LK1008) Measuring frequency : 1 to 50MHz (LK1008) Measuring equipment, Ijig : HP4194 + 160958 + 16092A (or its equivalent) HP4195 + 14951 + 16092A (or its equivalent) HP4294 + 16192A HP4291A-16193A (LK1005) HP4291A-16193A (LK1005) HP4285A-42841A-42842C+42851-61100 (CKP2520) Measuring current : ImA rms (0.047 to 4.7μH) O.1mA rms (5.6 to 33μH) HK, AQ Series : Measuring frequency : 100MHz (HK0603 + HK1005 - AQ105) Measuring frequency : 50/100MHz (HK1608 + HK2125) Measuring frequency : 50/100MHz (HK1608 - HK2125) Measuring requipment, Ijig : HP4291A + 16197A (HK0603 - AQ105)

^{*} Definition of rated current : In the CK and BK Series, the rated current is the value of current at which the temperature of the element is increased within 20°C .

In the BK Series P type and CK Series P type, the rated current is the value of current at which the temperature of the element is increased within 40°C.

In the LK,HK,HKQ,and AQ Series, the rated current is either the DC value at which the internal L value is decreased within 5% with the application of DC bias, or the value of current at which the temperature of the element is increased within $20\,^\circ\!\text{C}$.

											Specifi	ied Valu	ie										
Item	BK0603	BK1005	BK1608	BK2125	-	RAY BK3216	- BKP0603	BKP1005	BKP1608	BKP2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608	HK2125	HKQ0603S	AQ105	Test Methods and Remarks
6. Q					_						20 min.	15~20 min.		10~20 min.	10~35 min.	15~50 min.	4∼5 min.	8 min.	8~12 min.	10~18 min.	10~13 min.	8 min.	CK Series: Measuring frequency: 2 to 4MHz (CK1608) Measuring frequency: 2 to 25MHz (CK2125) LK Series: Measuring frequency: 10 to 25MHz (LK1005) Measuring frequency: 10 to 25MHz (LK1005) Measuring frequency: 10 to 50MHz (LK1608) Measuring frequency: 0.4 to 50MHz (LK2125) Measuring equipment, jig: HP4194 + 16085B + 16092A (or its equivalent)
7. DC Resistance	0.07~ 1.50Ω max.	0.05~ 0.80Ω max.	0.05~ 1.10Ω max.	0.05~ 0.75Ω max.	0.10~ 0.90Ω max.	0.15~ 0.80Ω max.	0.065~ 0.070Ω max.	0.140Ω max.	0.025~ 0.140Ω max.	0.020~ 0.050Ω max.		0.16~ 0.65Ω max.	0.08~ 0.15 max.	0.7~ 1.70Ω max.	0.2~ 2.2Ω max.	0.1~ 1.1Ω max.	0.11~ 3.74Ω max.	0.08~ 4.8Ω max.	0.05~ 2.6Ω max.	0.10~ 1.5Ω max.	0.06~ 1.29Ω max.	0.07~ 0.45Ω max.	Measuring equipment: VOAC-7412 (made by Iwasaki Tsushinki) VOAC-7512 (made by Iwasaki Tsushinki)
8. Self Resonance Frequency (SRF)					_	_					17~ 25MHz min.	24~ 235MHz min.		40~ 180MHz min.	9~ 260MHz min.	13~ 320MHz min.	900~ 10000MHz min.	400~ 10000MHz min.	300~ 10000MHz min.	200~ 4000MHz min.	1900~ 10000MHz min.	2300~ 10000MHz min.	LK Series: Measuring equipment: HP4195A Measuring jig: 41951+16092A (or its equivalent) HK, HKQ, AQ Series: Measuring equipment: HP8719C HP8753D (HK2125)
Temperature Characteristic					_	_							_	_			1	±10%	hange	:			HK, HKQ, AQ Series: Temperature range: -30 to +85°C Reference temperature: +20°C
10. Resistance to Flexure of Substrate	No me	echanic	al dam	age.																			Warp : 2mm Testing board : glass epoxy-resin substrate Thickness : 0.8mm Board R-230 Warp 45 45 45 (Unit. mm)

										Specifi	ed Valu	ıe										
Item	BK0603	BK1005	BK1608	BK2125	ARRAY BK2010 BK32		BKP1005	BKP1608	BKP2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608	HK2125	HKQ0603S	AQ105	Test Methods and Remarks
11. Solderability	At leas	st 75% o	of term	inal ele	ctrode is c	overed b	y new s	older.		At leas	t 75%	of termi	nal elec	ctrode i	s cove	red by r	new sol	der.				Solder temperature : 230±5°C
																						Duration: 4±1 sec.
12. Resistance to	Appea	rance	: No sig	gnifican	t abnorma	ity.				No meci	nanical d	amage.	No	No meci	nanical	No me	chanic	al dama	ige.			Solder temperature : 260±5℃
Soldering	Imped	ance ch	nange	: With	in ±30%					Remaini	ng termir	nal	mechanical	damag	е.	Remair	ning terr	ninal ele	ctrode	: 70% r	nin.	Duration: 10±0.5 sec.
										electrod	e: 70%	min.	damage.	Remain	ing							Preheating temperature: 150 to 180°C
													Remaining	termina	ıl	Induct	ance c	nange				Preheating time: 3 min.
										Inductar	nce chan	ge	terminal	electro	de :	Within	±5%					Flux: Immersion into methanol solution with
										R10~4R	7: Within	±10%	electrode	70% m	iin.							colophony for 3 to 5 sec.
										6R8~10	:Within	±15%	: 70% min.	Inducta	ince							Recovery: 2 to 3 hrs of recovery under
										CKP252	0:Within	±30%	Inductance	change								the standard condition after the test.
													change	47N~4	R7:							(See Note 1)
													Within	Within	±10%							
													±15%	5R6~3	30:							
														Within								
13. Thermal Shock	1		,		t abnorma	ity.				No		No		chanic	al			al dama				Conditions for 1 cycle
	Imped	ance ch	nange	With	in ±30%					mecha		mechanical	damag					nange		n ±10	%	Step 1: Minimum operating temperature
										damag		damage.	Induct			Qchan	ige : V	Vithin ±	20%			+0 °C 30±3 min.
										Induct		Induc-	change									Step 2: Room temperature 2 to 3 min.
										change Within ±		tance change:	Within	±10% ge :								Step 3 : Maximum operating temperature +0 -3 °C 30±3 min.
										Qchan	ge :	Within	Within	±30%	5							Step 4 : Room temperature 2 to 3 min.
										Within ±	30%	±30%										Number of cycles : 5
																						Recovery : 2 to 3 hrs of recovery under the
																						standard condition after the test. (See Note 1)

(Note 1) When there are questions concerning mesurement result ; measurement shall be made after 48 ± 2 hrs of recovery under the standard condition.

													Specif	ied Valu	ie											
Item						AF	RAY				T											T				Test Methods and Remarks
	BK0603	BK1005	BI	K1608 B	K2125	BK2010	BK3216	BKF	P0603 BKP100	5 BKP160	18 BK	(P2125	CK1608	CK2125	CKP2520	LK1005	LK1608	LK2125	HK0603	HK1005	HK1608	H	HK2125 H	(Q0603S	AQ105	
14. Damp Heat	Appea	rance	: N	No signit	fican	t abn	ormalit	y.					No		No	No me	L chani-	No	No me	chanic	l al dam	ag	je.			BBK Series:
(Steady state)	Imped	ance c	hai	nge : Wi	ithin	±30%	6						mecha	anical	mechanical	cal da	mage.	mechanical	Induct	ance c	hange:	: w	/ithin ±	10%		Temperature: 40±2°C
													dama	ge.	damage.	Induct	ance	damage.	1	nge : W						Humidity: 90 to 95%RH Duration: 500 +24 hrs
													Induct	ance	Inductance	chang		Inductance								Recovery: 2 to 3 hrs of recovery under the
													chang		change :	Within		change:								standard condition after the removal from test
													Within		Within	±10%		Within								chamber. (See Note 1)
															±30%			±20%								LK, CK, CKP, HK, HKQ, AQ Series:
													Q cha	nae:		Q cha	nae:	Q change:								Temperature: 40±2°C (LK, CK, CKPSeries)
													Within			Within		Within								: 60±2°C (HK, HKQ, AQ Series)
																±30%		±30%								Humidity: 90 to 95%RH
																										Duration: 500±12 hrs
																										Recovery: 2 to 3 hrs of recovery under the
																										standard condition after the removal from test
																										chamber. (See Note 1)
15. Loading under	Appea	rance	: N	No signi	fican	t abn	ormalit	٧.					No		No	No	No	No	No me	chanic	al dam	aa	1e.			BK Series :
Damp Heat				nge : Wi				•					mecha	anical	mechanical	mechanical	mechanical	mechanical	1				/ithin ±	10%		Temperature: 40±2°C
	'												dama		damage.	damage.	damage.	damage.	1	nge : W						Humidity: 90 to 95%RH
																	'			-						Duration: 500 +24 hrs
													Induct	ance	Induc-	Induc-	Induc-	Induc-								Recovery : 2 to 3 hrs of recovery under the
													chang	e:	tance	tance	tance	tance								standard condition after the removal from test
													Within		change:	change:	change:	change:								chamber. (See Note 1)
															Within	Within	0.047 to	Within								LK, CK, CKP, HK, HKQ, AQ Series:
													Q cha	nge:	±30%	±10%	12.0µH:	±20%								Temperature: 40±2°C (LK, CK, CKPSeries)
													Within	±30%			Within									:60±2°C (HK, HKQ, AQ Series)
																Q	±10%	Q								Humidity: 90 to 95%RH
																change:	15.0 to	change:								Duration: 500±12 hrs
																Within	33.0µH:	Within								Recovery: 2 to 3 hrs of recovery under the
																±30%	Within	±30%								standard condition after the removal from test
																	±15%									chamber. (See Note 1)
																	Q abanas :									
																	change: Within									
																	±30%									
16. Loading at High	Appea	rance	: N	No signi	fican	t abn	ormalit	γ.					No		No	No	No	No	No me	chanic	al dam	aq	ıe.			BK Series :
Temperature				nge : Wi									mecha	anical	mechanical	mechanical	mechanical	mechanical	1				/ /ithin ±	10%		Temperature: 125±3°C
													dama	ge.	damage.	damage.	damage.	damage.	Q char	nge : W	ithin ±	20	0%			Applied current : Rated current
																										Duration: 500 +24 hrs
													Induct	ance	Induc-	Induc-	Induc-	Induc-								Recovery: 2 to 3 hrs of recovery under the
													chang	e:	tance	tance	tance	tance								standard condition after the removal from test
													Within	±20%	change:	change:	change:	change:								chamber. (See Note 1)
															Within	Within	0.047 to	Within								LK, CK, CKP, HK, HKQ, AQ Series, BK Series
													Q cha	nge:	±30%	±10%	12.0µH:	±20%								P type:
													Within	±30%			Within									Temperature: 85±2°C (LK, CK, CKPSeries)
																Q	±10%	Q								:85±3°C (BK Series P type)
																change:	15.0 to	change:								:85±2°C (HK1608, 2125)
																Within	33.0µH:	Within								:85±2°C (HK1005, AQ105 operating
																±30%	Within	±30%								temperature range -55 to +85°C)
																	±15%									:125±2°C (HK0603, HK1005, HKQ0603S,
																										AQ105 operating temperature range -55 to +125°C)
																	Q									Applied current : Rated current
																	change:									Duration: 500±12 hrs
																	Within									Recovery : 2 to 3 hrs of recovery under the
																	±30%									standard condition after the test. (See Note 1)

Note on standard condition: "standard condition" referred to herein is defined as follows:

5 to $35^{\circ}\!\text{C}$ of temperature, 45 to 85% relative humidity, and 86 to 106kPa of air pressure.

When there are questions concerning measurement results:

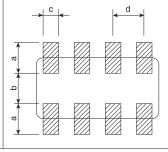
In order to provide correlation data, the test shall be conducted under condition of 20 \pm 2°C of temperature, 60 to 70% relative humidity, and 86 to 106kPa of air pressure. Unless otherwise specified, all the tests are conducted under the "standard condition."

(Note 1)

measurement shall be made after 48 \pm 2 hrs of recovery under the standard condition.

(Design of Land-patterns) 1. When inductors are mounted on a PCB, the size of land patterns and the amount of solder used (size of fillet) can directly affect inductor performance. Therefore, the following items must be carefully considered in the design of solder land patterns: (1) The amount of solder applied can affect the ability of chips to withstand mechanical stresses which may lead to breaking or cracking. Therefore, when designing land-patterns it is necessary to consider the appropriate size and configuration of the solder pads which in turn determines the amount of solder necessary to form the fillets. (2) When more than one part is jointly soldered onto the same land or pad, the pad must be designed so that each component's soldering point is separated by solder-resist. (3) The larger size of land patterns and amount of solder, the smaller Q value after mounting on PCB. It makes higher the Q value to design land patterns smaller than terminal electrode of chips.		Technical co	onsideratio	ons		
(Design of Land-patterns) 1. When inductors are mounted on a PCB, the size of land patterns and the amount of solder used (size of fillet) can directly affect inductor performance. Therefore, the following items must be carefully considered in the design of solder land patterns: (1) The amount of solder applied can affect the ability of chips to withstand mechanical stresses which may lead to breaking or cracking. Therefore, when designing land-patterns it is necessary to consider the appropriate size and configuration of the solder pads which in turn determines the amount of solder necessary to form the fillets. (2) When more than one part is jointly soldered onto the same land or pad, the pad must be designed so that each component's soldering point is separated by solder-resist. (3) The larger size of land patterns and amount of solder, the smaller Q value after mounting on PCB. It makes higher the Q value to design land patterns smaller than terminal electrode of chips.						
(3) The larger size of land patterns and amount of solder, the smaller Q value after mounting on PCB. It makes higher the Q value to design land patterns smaller than terminal electrode of chips. B C	Chip indu BAA nended land dime	xcessive solder tend termination. dimensions for Land pattern ctor	er amount tions). E or a typica Solder-resi	ts (larger to examples of the control of the contro	fillets which of improper luctor land Chip induct	n extend r pattern patterns
Type	0.5~0.8 0.6~0.8	0.8~1.5 0.9~1.2	0.8~·	1.6	· mm)	
	0603 100		1608	2125	3216	2520
ži W	0.6 1.0		1.6	2.0	3.2	2520
	0.6 1.0		0.8	1.25	1.6	2.5
		0.55 0.50~0.55	0.6~0.8	0.8~1.2	1.8~2.5	1.0~1.
		0.50 0.30~0.50	0.6~0.8	0.8~1.2	0.6~1.5	0.6~1.
		0.55 0.60~0.70	0.6~0.8	0.8~1.2	1.2~2.0	1.8~2

Excess solder can affect the ability of chips to withstand mechanical stresses. Therefore, please take proper precautions when designing land-patterns.



Recommended land dimension for Reflow-soldering (unit: mm)

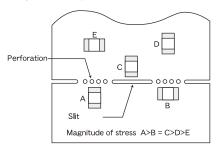
		3216	2010
Size	L	3.2	2.0
že	W	1.6	1.0
a		0.7~0.9	0.5~0.6
b		0.8~1.0	0.5~0.6
С		0.4~0.5	0.2~0.3
d		0.8	0.5

performed to minimize stress.

Stages	Precautions	Technical considerations		
PCB Design		(2) Example	s of good and bad solde	r application
			Not recommended	Recommended
◆Pattern configurations (Inductor layout on panelized [breakaway] PC boards) 1. After inductors have been mounted on the boards, chips can be subjected to mechanical stresses in subsequent manufacturing processes (PCB cutting, board inspection, mounting of additional parts, assembly into the chassis, wave soldering the reflow soldered boards etc.) For this reason, planning pattern configurations and the position of SMD inductors should be carefully	Mixed mount- ing of SMD and leaded compo- nents	Lead wire of component	Solder-resist	
	C o m p o n e n t placement close to the chassis	Chassis Solder(for grounding)	Solder-resist	
	Hand-soldering of leaded components near mounted components	Lead wire of component- Soldering iron	Solder-resist-	
		Horizontal component placement		Solder-resist
	tors should		and bad inductor layout; SMD i	
	Item	Not recommended	Recommended	
	Deflection of the board		Position the component at a right angle to the direction of the mechanical stresses that are anticipated.	

Item	Not recommended	Recommended
Deflection of the board		Position the component at a right angle to the direction of the mechanical stresses that are anticipated.

1-2. To layout the inductors for the breakaway PC board, it should be noted that the amount of mechanical stresses given will vary depending on inductor layout. An example below should be counted for better design.



1-3. When breaking PC boards along their perforations, the amount of mechanical stress on the inductors can vary according to the method used. The following methods are listed in order from least stressful to most stressful: push-back, slit, V-grooving, and perforation. Thus, any ideal SMD inductor layout must also consider the PCB splitting procedure.

Stages	Precautions	Technical considerations	
3.Considerations for automatic placement	◆Adjustment of mounting machine 1. Excessive impact load should not be imposed on the inductors when mounting onto the PC boards. 2. The maintenance and inspection of the mounter should be conducted periodically.	If the lower limit of the pick-up nozzle is low, too much force may be imposed on the inductors, causing damage. To avoid this, the following points should be considered before lowering the pick-up nozzle: (1) The lower limit of the pick-up nozzle should be adjusted to the surface level of the PC board after correcting for deflection of the board. (2) The pick-up pressure should be adjusted between 1 and 3 N static loads. (3) To reduce the amount of deflection of the board caused by impact of the pick-up nozzle, supporting pins or back-up pins should be used under the PC board. The following diagrams show some typical examples of good pick-up nozzle placement:	
		Improper method Proper method	
		Single-sided mounting chipping or cracking supporting pins or back-up pins	
		Double-sided mounting chapter or cracking or back-up pins	
		2. As the alignment pin wears out, adjustment of the nozzle height can cause chipping or cracking of the inductors because of mechanical impact on the inductors. To avoid this, the monitoring of the width between the alignment pin in the stopped position, and maintenance, inspection and replacement of the pin should be conducted periodically.	
	◆Selection of Adhesives 1. Mounting inductors with adhesives in preliminary assembly, before the soldering stage, may lead to degraded inductor characteristics unless the following factors are appropriately checked; the size of land patterns, type of adhesive, amount applied, hardening temperature and hardening period. Therefore, it is imperative to consult the manufacturer of the adhesives on proper usage and amounts of adhesive to use.	1. Some adhesives may cause reduced insulation resistance. The difference between the shrinkage percentage of the adhesive and that of the inductors may result in stresses on the inductors and lead to cracking. Moreover, too little or too much adhesive applied to the board may adversely affect component placement, so the following precautions should be noted in the application of adhesives. (1) Required adhesive characteristics a. The adhesive should be strong enough to hold parts on the board during the mounting & solder process. b. The adhesive should have sufficient strength at high temperatures. c. The adhesive should have good coating and thickness consistency. d. The adhesive should be used during its prescribed shelf life. e. The adhesive should harden rapidly f. The adhesive must not be contaminated. g. The adhesive should have excellent insulation characteristics. h. The adhesive should not be toxic and have no emission of toxic gasses.	

Please read the "Notice for TAIYO YUDEN products" before using this catalog.

Stages	Precaution	Technical considerations
3.Considerations for automatic placement		When using adhesives to mount inductors on a PCB, inappropriate amounts of adhesive on the board may adversely affect component placement. Too little adhesive may cause the inductors to fall off the board during the solder process. Too much adhesive may cause defective soldering due excessive flow of adhesive on to the land or solder pad. [Recommended conditions]
		Figure 0805 case sizes as examples
		a 0.3mm min
		b 100 ~120 μm
		c Area with no adhesive
		Amount of adhesives After inductors are bonded
4.Soldering	◆Selection of Flux 1. Since flux may have a significant effect on the performance of inductors, it is necessary to verify the following conditions prior to use; (1) Flux used should be with less than or equal to 0.1 wt% (Chlorine conversion method) of halogenated content. Flux having a strong acidity content should not be applied. (2) When soldering inductors on the board, the amount of flux applied should be controlled at the optimum level. (3) When using water-soluble flux, special care should be taken to properly clean the boards.	1-1. When too much halogenated substance (Chlorine, etc.) content is used to activate the flux, or highly acidic flux is used, an excessive amount of residue after soldering may lead to corrosion of the terminal electrodes or degradation of insulation resistance on the surface of the Inductor. 1-2. Flux is used to increase solderability in flow soldering, but if too much is applied, a large amount of flux gas may be emitted and may detrimentally affect solderability. To minimize the amount of flux applied, it is recommended to use a flux-bubbling system. 1-3. Since the residue of water-soluble flux is easily dissolved by water content in the air, the residue on the surface of Inductor in high humidity conditions may cause a degradation of insulation resistance and therefore affect the reliability of the components. The cleaning methods and the capability of the machines used should also be considered carefully when selecting water-soluble flux.
	◆Soldering Temperature, time, amount of solder, etc. are specified in accordance with the following recommended conditions.	1-1. Preheating when soldering Heating: Chip inductor components should be preheated to within 100 to 130°C of the soldering. Cooling: The temperature difference between the components and cleaning process should not be greater than 100°C. Chip inductors are susceptible to thermal shock when exposed to rapid or concentrated heating or rapid cooling. Therefore, the soldering process must be conducted with a great care so as to prevent malfunction of the components due to excessive thermal shock.

Please read the "Notice for TAIYO YUDEN products" before using this catalog.

Stages	Precautions	Technical considerations
4.Soldering	◆And please contact us about peak temperature	Recommended conditions for soldering
	when you use lead-free paste.	[Reflow soldering]
		Temperature profile Temperature (°C) (Pb free soldering)
		Temperature 300 Peak 260°C max▶: ◄
		(C) 10 sec max 10 sec max
		250 Gradually cooling
		150 100 Preheating
		50 150°C Heating above 230°C 60 sec min 40 sec max
		*Ceramic chip components should be preheated t
		Over 1 minute Over 1 minute Gradual cooling water 100 to 130 or 14th solidering for 2 times. **Assured to be reflow solidering for 2 times. 10 seconds
		Caution
		1. The ideal condition is to have solder mass (fillet) controlled to 1/2 to
		1/3 of the thickness of the inductor, as shown below:
		1/2T~1/3T
		Solder
		PC board
		2. Because excessive dwell times can detrimentally affect solderability,
		soldering duration should be kept as close to recommended times
		as possible.
		[Wave soldering]
		Temperature profile Temperature (°C) (Pb free soldering)
		Temperature 230°C Peak 260°C max▶;
		(°C) 250°C 10 sec max 200
		250 Preheating Gradually cooling
		150
		50 120 sec min
		%Ceramic chip components should be preheated to Over 2 minutes Gradual cooling within 100 to 130°C of the soldering.
		Within #Assured to be wave soldering for 1 time. 3 seconds #Except for reflow soldering type.
		October
		Caution
		Make sure the inductors are preheated sufficiently.
		2. The temperature difference between the inductor and melted solder should
		not be greater than 100 to 130°C
		Cooling after soldering should be as gradual as possible.
		4. Wave soldering must not be applied to the inductors designated as for re-
		flow soldering only.
		[Hand soldering]
		Temperature profile
		Temperature (°C) (Pb free soldering)
		Temperature 230°C 400350°C max
		(°C)
		250 200 200 21 cooling
		150
		50 60 sec min
		(** △TT190°C (3216Type max), △T≦130°C (3225
		Within #It is recommended to use 20W soldering iron and
		the tip is 1 to or less. #The soldering iron should not directly touch the components.
		**Assured to be soldering iron for 1 time.
		Note: The above profiles are the maximum allowabl soldering condition, therefore these profiles a not always recommended.
		Caution
		Caution
		Caution 1. Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. 2. The soldering iron should not directly touch the inductor.
5.Cleaning	◆Cleaning conditions	Caution 1. Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. 2. The soldering iron should not directly touch the inductor. 1. The use of inappropriate solutions can cause foreign substances such
5.Cleaning	◆Cleaning conditions 1. When cleaning the PC board after the Inductors are	Caution 1. Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. 2. The soldering iron should not directly touch the inductor.
5.Cleaning		Caution 1. Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. 2. The soldering iron should not directly touch the inductor. 1. The use of inappropriate solutions can cause foreign substances such
5.Cleaning	1. When cleaning the PC board after the Inductors are	Caution 1. Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. 2. The soldering iron should not directly touch the inductor. 1. The use of inappropriate solutions can cause foreign substances such as flux residue to adhere to the inductor, resulting in a degradation of the
5.Cleaning	When cleaning the PC board after the Inductors are all mounted, select the appropriate cleaning solu-	Caution 1. Use a 20W soldering iron with a maximum tip diameter of 1.0 mm. 2. The soldering iron should not directly touch the inductor. 1. The use of inappropriate solutions can cause foreign substances such as flux residue to adhere to the inductor, resulting in a degradation of the

263

Stages	Precautions	Technical considerations
5.Cleaning	Cleaning conditions should be determined after verifying, through a test run, that the cleaning process does not affect the inductor's characteristics.	2. Inappropriate cleaning conditions (insufficient or excessive cleaning) may detrimentally affect the performance of the inductors. (1) Excessive cleaning In the case of ultrasonic cleaning, too much power output can cause excessive vibration of the PC board which may lead to the cracking of the inductor or the soldered portion, or decrease the terminal electrodes' strength. Thus the following conditions should be carefully checked; Ultrasonic output Below 20 w/ℓ Ultrasonic frequency Below 40 kHz Ultrasonic washing period 5 min. or less
6. Post cleaning processes	 ◆Application of resin coatings, moldings, etc. to the PCB and components. 1. With some type of resins a decomposition gas or chemical reaction vapor may remain inside the resin during the hardening period or while left under normal storage conditions resulting in the deterioration of the inductor's performance. 2. When a resin's hardening temperature is higher than the inductor's operating temperature, the stresses generated by the excess heat may lead to inductor damage or destruction. 3. Stress caused by a resin's temperature generated expansion and contraction may damage inductors. The use of such resins, molding materials etc. is not recommended. 	
7. Handling	 ◆Breakaway PC boards (splitting along perforations) 1. When splitting the PC board after mounting inductors and other components, care is required so as not to give any stresses of deflection or twisting to the board. 2. Board separation should not be done manually, but by using the appropriate devices. ◆General handling precautions 1. Always wear static control bands to protect against ESD. 2. Keep the inductors away from all magnets and magnetic objects. 3. Use non-magnetic tweezers when handling inductors. 4. Any devices used with the inductors (soldering irons, measuring instruments) should be properly grounded. 5. Keep bare hands and metal products (i.e., metal desk) away from chip electrodes or conductive areas that lead to chip electrodes. 6. Keep inductors away from items that generate magnetic fields such as speakers or coils. ◆Mechanical considerations 1. Be careful not to subject the inductors to excessive mechanical shocks. (1) If inductors are dropped on the floor or a hard surface they should not be used. (2) When handling the mounted boards, be careful that the mounted components do not come in contact with or bump against other boards or components. 	

Stages	Precautions	Technical considerations
8. Storage conditions	◆Storage 1. To maintain the solderability of terminal electrodes and to keep the packaging material in good condition, care must be taken to control temperature and humidity in the storage area. Humidity should especially be kept as low as possible. Recommended conditions Ambient temperature Below 40 °C Humidity Below 70% RH The ambient temperature must be kept below 30 °C. Even under ideal storage conditions inductor electrode solderability decreases as time passes, so inductors should be used within 6 months from the time of delivery.	If the parts are stocked in a high temperature and humidity environment, problems such as reduced solderability caused by oxidation of terminal electrodes and deterioration of taping/packaging materials may take place. For this reason, components should be used within 6 months from the time of delivery. If exceeding the above period, please check solderability before using the inductors
	*The packaging material should be kept where no chlorine or sulfur exists in the air.	