



**NOTES:**

- 20 CIRCUIT SIZE SHOWN FOR ILLUSTRATION PURPOSES.
- PIN PUSHOUT FORCE (17.79 N/4 LBS).
- PINS MUST MEET SOLDERABILITY SPEC. MIL-STD-202 METHOD 208B.
- WINDOW NOT AVAILABLE ON G AND B CIRCUIT SIZE.
- FOR CHARTED DIMENSIONS AND ASSEMBLY N.O.S NOT SHOWN ON THIS SHEET - SEE SHEET 2.
- ROW TO ROW PIN HEIGHT VARIATION MUST NOT EXCEED (0.13) ± 0.005 IN.
- PRODUCT CONFORMS TO PS-8723.

**MATERIAL:**

SHROUDED WAFER: GLASS FILLED POLYESTER, 94V-0; COLOR: BLACK.  
PINS: PHOSPHOR BRONZE.  
SEE SHEET 2 FOR FINISHES.

**ASSEMBLY STYLE "A" AND "B"**

CKT.	DIM. "A"	DIM. "B"	DIM. "C"	DIM. "D"	DIM. "E"
06	(1.68) ± 0.06	(8.43) ± 0.332	(5.08) ± 0.200	(1.68) ± 0.06	
08	(1.68) ± 0.06	(10.97) ± 0.432	(7.62) ± 0.300	(1.68) ± 0.06	
10	(1.68) ± 0.06	(13.51) ± 0.532	(10.16) ± 0.400	(1.68) ± 0.06	
12	(1.68) ± 0.06	(16.05) ± 0.632	(12.70) ± 0.500	(1.68) ± 0.06	
14	(1.68) ± 0.06	(18.59) ± 0.732	(15.24) ± 0.600	(1.68) ± 0.06	
16	(1.68) ± 0.06	(21.13) ± 0.832	(17.78) ± 0.700	(1.68) ± 0.06	
18	(1.68) ± 0.06	(23.67) ± 0.932	(20.32) ± 0.800	(1.68) ± 0.06	
20	(1.68) ± 0.06	(26.21) ± 1.032	(22.86) ± 0.900	(1.68) ± 0.06	
22	(1.68) ± 0.06	(28.75) ± 1.132	(25.40) ± 1.000	(1.68) ± 0.06	
24	(1.68) ± 0.06	(31.29) ± 1.232	(27.94) ± 1.100	(1.68) ± 0.06	
26	(1.68) ± 0.06	(33.83) ± 1.332	(30.48) ± 1.200	(1.68) ± 0.06	
28	(1.68) ± 0.06	(36.37) ± 1.432	(33.02) ± 1.300	(1.68) ± 0.06	
30	(1.68) ± 0.06	(38.91) ± 1.532	(35.56) ± 1.400	(1.68) ± 0.06	
32	(1.68) ± 0.06	(41.45) ± 1.632	(38.10) ± 1.500	(1.68) ± 0.06	
34	(1.68) ± 0.06	(43.99) ± 1.732	(40.64) ± 1.600	(1.68) ± 0.06	
36	(1.68) ± 0.06	(46.53) ± 1.832	(43.18) ± 1.700	(1.68) ± 0.06	
38	(1.68) ± 0.06	(49.07) ± 1.932	(45.72) ± 1.800	(1.68) ± 0.06	
40	(1.68) ± 0.06	(51.61) ± 2.032	(48.26) ± 1.900	(1.68) ± 0.06	
42	(1.68) ± 0.06	(54.15) ± 2.132	(50.80) ± 2.000	(1.68) ± 0.06	
44	(1.68) ± 0.06	(56.69) ± 2.232	(53.34) ± 2.100	(1.68) ± 0.06	
46	(1.68) ± 0.06	(59.23) ± 2.332	(55.88) ± 2.200	(1.68) ± 0.06	
48	(1.68) ± 0.06	(61.77) ± 2.432	(58.42) ± 2.300	(1.68) ± 0.06	
50	(1.68) ± 0.06	(64.31) ± 2.532	(60.96) ± 2.400	(1.68) ± 0.06	
52	(1.68) ± 0.06	(66.85) ± 2.632	(63.50) ± 2.500	(1.68) ± 0.06	
54	(1.68) ± 0.06	(69.39) ± 2.732	(66.04) ± 2.600	(1.68) ± 0.06	
56	(1.68) ± 0.06	(71.93) ± 2.832	(68.58) ± 2.700	(1.68) ± 0.06	
58	(1.68) ± 0.06	(74.47) ± 2.932	(71.12) ± 2.800	(1.68) ± 0.06	
60	(1.68) ± 0.06	(77.01) ± 3.032	(73.66) ± 2.900	(1.68) ± 0.06	
62	(1.68) ± 0.06	(79.55) ± 3.132	(76.20) ± 3.000	(1.68) ± 0.06	
64	(1.68) ± 0.06	(82.09) ± 3.232	(78.74) ± 3.100	(1.68) ± 0.06	
66	(1.68) ± 0.06	(84.63) ± 3.332	(81.28) ± 3.200	(1.68) ± 0.06	
68	(1.68) ± 0.06	(87.17) ± 3.432	(83.82) ± 3.300	(1.68) ± 0.06	
70	(1.68) ± 0.06	(89.71) ± 3.532	(86.36) ± 3.400	(1.68) ± 0.06	
72	(1.68) ± 0.06	(92.25) ± 3.632	(88.90) ± 3.500	(1.68) ± 0.06	

REVISIONS

7	U	ADD -0003 OPT
6	T	LEAD FREE
5	S	PL WHITE 04/04/26
4	T	ADD -0004 OPTION
3	R2	TAB DIF. DIM. & CR
2	R1	FOR USE WITH 04/04/26
1	R	ADDED SHEET 1 FOR
	G	REVISIONS
	P	CHG WINDOW NOTES
	O	FOR USE WITH 04/04/26
	N	REVISED A REGRAN
	M	FOR USE WITH 04/04/26
	L	REVISED A REGRAN
	K	FOR USE WITH 04/04/26
	J	REVISED A REGRAN
	I	FOR USE WITH 04/04/26
	H	REVISED A REGRAN
	G	FOR USE WITH 04/04/26
	F	REVISED A REGRAN
	E	FOR USE WITH 04/04/26
	D	REVISED A REGRAN
	C	FOR USE WITH 04/04/26
	B	REVISED A REGRAN
	A	FOR USE WITH 04/04/26

SALES ASSEMBLY  
WAFER: STRAIGHT MI-FULLY SHROUDED  
+100 GRID-FOR USE W/ 825 SO. PINS  
WITH 0.050 END WALLS  
MOLEX INCORPORATED  
SEE CHART SDA-8723-\*\*\*\*\*  
REV. 4:1

