COMPLIANT

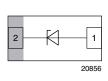
GREEN

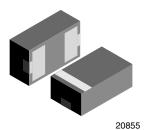
(5-2008)



### Vishay Semiconductors

#### **ESD-Protection Diode in LLP1006-2L**





#### **MARKING** (example only)



Bar = cathode marking

X = date code

Y = type code (see table below)

#### **FEATURES**

- Ultra compact LLP1006-2L package
- Low package height < 0.4 mm
- 1-line ESD-protection
- Low leakage current < 1 μA</li>
- Low load capacitance C<sub>D</sub> = 76 pF  $(V_R = 2.5 \text{ V}; f = 1 \text{ MHz})$
- ESD-protection acc. IEC 61000-4-2
  - ± 30 kV contact discharge
  - ± 30 kV air discharge
- High surge current acc. IEC61000-4-5 IPP > 16 A
- Soldering can be checked by standard vision inspection. No X-ray necessary
- Pin plating NiPdAu (e4) no whisker growth
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

ORDERING INFORMATION					
DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE on 7" REEL)	MINIMUM ORDER QUANTITY		
VESD05A1A-HD1	VESD05A1A-HD1-GS08	8000	8000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VESD05A1A-HD1	LLP1006-2L	Н	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	acc. IEC 61000-4-5; t <sub>P</sub> = 8/20 μs; single shot	I <sub>PPM</sub>	16	Α		
Peak pulse power	acc. IEC 61000-4-5; t <sub>P</sub> = 8/20 μs; single shot	P <sub>PP</sub>	192	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses	V <sub>ESD</sub>	± 30	kV		
Operating temperature	Junction temperature	TJ	- 40 to + 125	°C		
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C		

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<sup>\*\*</sup> Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

### Vishay Semiconductors

## ESD-Protection Diode in LLP1006-2L



ELECTRICAL CHARACTERISTICS VESD05A1A-HD1 BIAS mode (between pin 1 and pin 2)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N <sub>lines</sub>	-	-	1	lines	
Reverse stand-off voltage	at I <sub>R</sub> = 1 μA	$V_{RWM}$	5	-	-	V	
Reverse current	at V <sub>R</sub> = 5 V	I <sub>R</sub>	-	< 0.1	1	μA	
Reverse breakdown voltage	at I <sub>R</sub> = 1 mA	$V_{BR}$	6	6.8	7.5	V	
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	7.2	8.5	V	
	at I <sub>PP</sub> = I <sub>PPM</sub> = 16 A	V <sub>C</sub>	-	10.5	12	V	
Forward clamping voltage	at I <sub>PP</sub> = 1 A	$V_{F}$	-	1	1.5	V	
	at I <sub>PP</sub> = I <sub>PPM</sub> = 16 A	V <sub>F</sub>	-	3.2	4.5	V	
Capacitance	at V <sub>R</sub> = 0 V; f = 1 MHz	C <sub>D</sub>	-	130	150	pF	
	at V <sub>R</sub> = 2.5 V; f = 1 MHz	C <sub>D</sub>	-	76	-	pF	

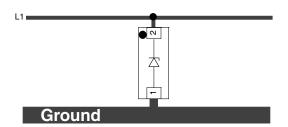
#### Note

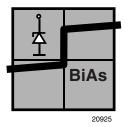
#### BIAS-MODE (BIDIRECTIONAL ASYMMETRICAL PROTECTION MODE)

With the **VESD05A1A-HD1** one signal- or data-lines (L1) can be protected against voltage transients. With pin 1 connected to ground and pin 2 connected to a signal- or data-line which has to be protected. As long as the voltage level on the data- or signal-line is between 0 V (ground level) and the specified **M**aximum **R**everse **W**orking **V**oltage ( $\mathbf{V}_{RWM}$ ) the protection diode between data line and ground offers a high isolation to the ground line. The protection device behaves like an open switch. As soon as any positive transient voltage signal exceeds the break through voltage level of the protection diode, the diode becomes conductive and shorts the transient current to ground. Now the protection device behaves like a closed switch. The **C**lamping **V**oltage ( $\mathbf{V}_{C}$ ) is defined by the **BR**eakthrough **V**oltage ( $\mathbf{V}_{BR}$ ) level plus the voltage drop at the series impedance (resistance and inductance) of the protection device.

Any negative transient signal will be clamped accordingly. The negative transient current is flowing in the forward direction of the protection diode. The low Forward Voltage ( $V_F$ ) clamps the negative transient close to the ground level.

Due to the different clamping levels in forward and reverse direction the **VESD05A1A-HD1** clamping behaviour is <u>Bi</u>directional and <u>Asymmetrical</u> (BiAs).





<sup>·</sup> Ratings at 25 °C, ambient temperature unless otherwise specified



## ESD-Protection Diode in LLP1006-2L

### Vishay Semiconductors

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

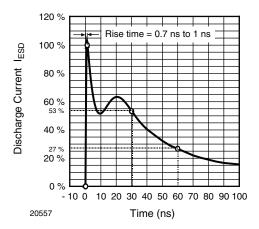


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega/150$  pF)

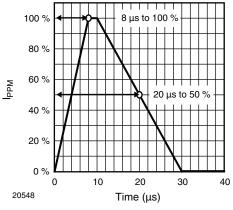


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form (acc. IEC 61000-4-5)

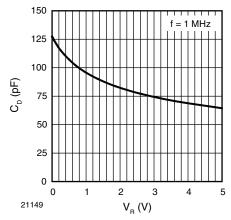


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

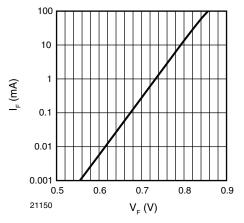


Fig. 4 - Typical Forward Current  $I_F$  vs. Forward Voltage  $V_F$ 

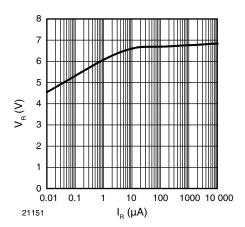


Fig. 5 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

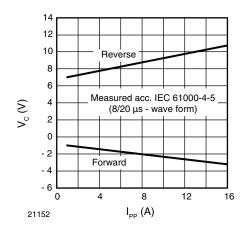


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current I<sub>PP</sub>

## Vishay Semiconductors

# ESD-Protection Diode in LLP1006-2L



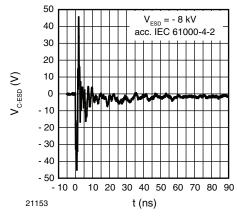


Fig. 7 - Typical Clamping Performance on - 8 kV ESD Events (acc. IEC 61000-4-2)

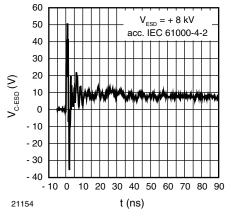


Fig. 8 - Typical Clamping Performance on + 8 kV ESD Events (acc. IEC 61000-4-2)

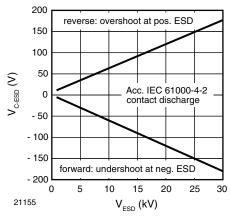


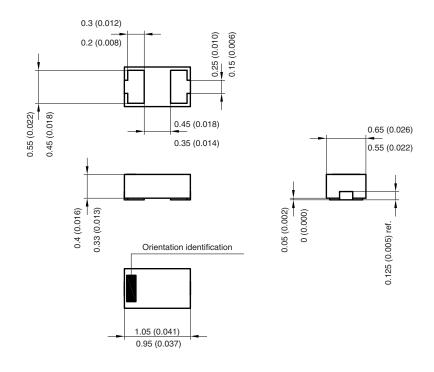
Fig. 9 - Typical max. Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

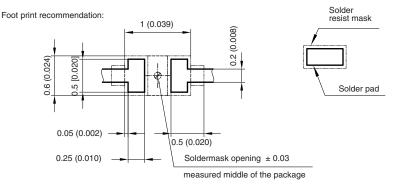


# ESD-Protection Diode in LLP1006-2L

## Vishay Semiconductors

#### PACKAGE DIMENSIONS in millimeters (inches): LLP1006-2L





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