



DZQA5V6AXV5

QUAD SURFACE MOUNT TVS ARRAY

Features

- Quad TVS in Common Anode Configuration
- Ultra-Small Surface Mount Package
- Ideal For Transient Suppression and ESD Protection
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green Device" (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

ESD Capability

- IEC 61000-4-2 Contact Method ±8kV
- IEC 61000-4-2 Air Discharge Method ±15kV

Mechanical Data

- Case: SOT-553
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Finish: Matte Tin, Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Polarity: Pin 1 Indicator
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.002g (approx.)

	C	A1, 2 A3, 3 2	<u>A4</u> <u>C</u>	1
		D2	D1	
		D3	D4	4
¥	C D	3	C C Schema	4

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	Value	Unit
Forward Voltage @ I _F = 10mA	V _F	0.9	V

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 4, 7)	PD	380	mW
Peak Power Dissipation, 8x20µS Waveform (Note 5)	P _{pk}	20	W
Thermal Resistance, Junction-to-Ambient (Note 4)	R _{0JA}	327	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics $@T_A = 25^{\circ}C$ unless otherwise specified

Туре	Marking Code	Breakdown Voltage (Note 3)			Leakage Current (Note 3)		Clamping Voltage (Note 5)		Typ Capacitance @0V Bias(pF) (Note 6)		Typ Capacitance @3V Bias(pF) (Note 6)	
Number		VB	_R @ I _T = 1n	nA	I _{RM} @	V _{RM}	V _C @	D I _{PP}	C	τ	С	т
		Min (V)	Nom (V)	Max (V)	Max(μA)	(V)	V _c (V)	I _{PP} (A)	Тур	Max	Тур	Max
DZQA5V6AXV5	T56	5.3	5.6	5.9	1	3.0	13	1.6	18.7	20	11.4	12.3

Notes: 1. No purposefully added lead.

2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

3. Short duration pulse test used to minimize self-heating effect.

4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. Suggested Pad Layout Document AP02001, which are to found an another than the found and an another than the found and the found

which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

5. Non-repetitive current pulse per Figure 3 and derate above $T_A = 25^{\circ}$ C per Figure 1.

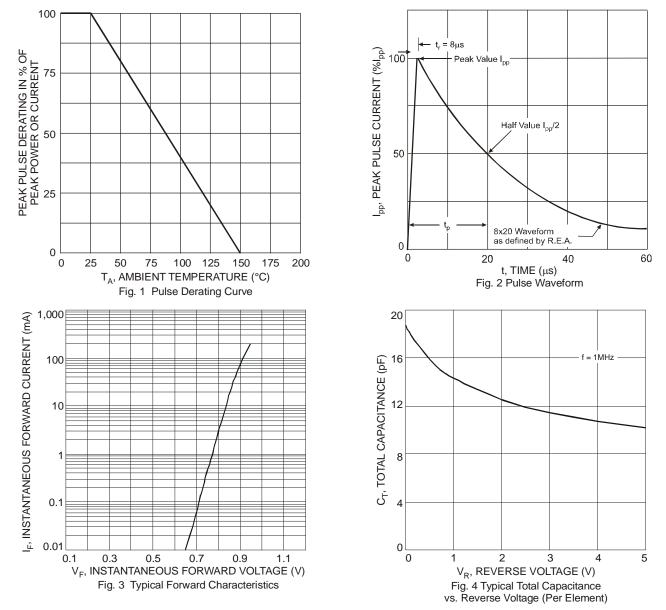
6. Per element, f = 1MHZ, $T_A = 25^{\circ}C$

7. Only 1 diode under power. For all 4 diodes under power, P_D will be 25% of the listed value.

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DZQA5V6AXV5



Ordering Information (Note 8)

Part Number	Case	Packaging
DZQA5V6AXV5-7	SOT-553	3000/Tape & Reel

Notes: 8. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

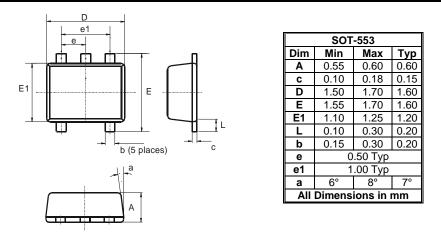
				 T56	YM	T56 = Produ YM = Date (Y = Year (e) M = Month (Code Mark c: W = 200	ing 9)				
Date Code Key Year	200	9	2010		2011	20	12	2013		2014	2	015
Code	W	-	X		Y		Z	A		В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

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Package Outline Dimensions



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