

## Series/Type: B37984

The following products presented in this data sheet are being withdrawn.

Ordering Code	Substitute Product	Date of Withdrawal	Deadline Last Orders	Last Shipments
B37984M5334K054		2008-08-01	2009-07-31	2009-10-31
B37984M5334K051		2008-08-01	2009-07-31	2009-10-31
B37984M5334K000		2008-08-01	2009-07-31	2009-10-31



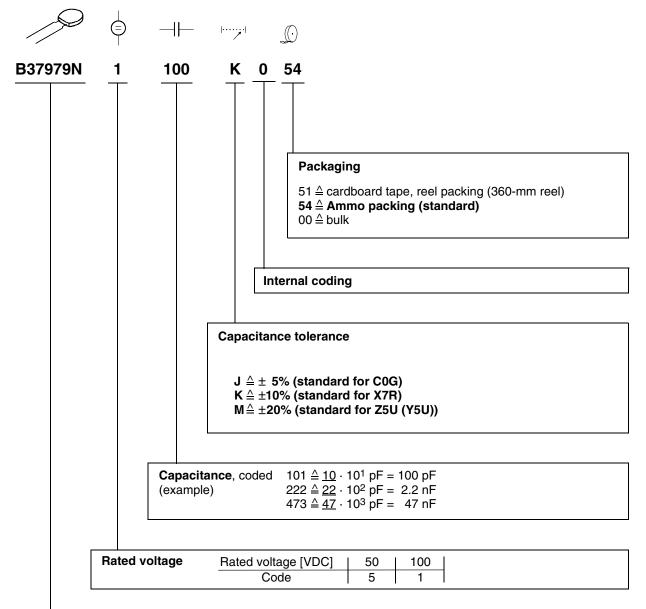
Ordering Code	Substitute Product	Date of Withdrawal	Deadline Last Orders	Last Shipments
B37984M5474K054		2008-08-01	2009-07-31	2009-10-31
B37984M5474K051		2008-08-01	2009-07-31	2009-10-31
B37984M5474K000		2008-08-01	2009-07-13	2009-10-31

For further information please contact your nearest EPCOS sales office, which will also support you in selecting a suitable substitute. The addresses of our worldwide sales network are presented at www.epcos.com/sales.



## X7R

## Ordering code system



Type and size					
With radial leads EIA standard	Temperature c C0G	haracteristic X7R	Z5U (Y5U)		
Lead spacing 2.5 mm $5.5 \times 5.0 \times 2.5$ $6.5 \times 5.0 \times 2.5$	B37979N B37981M B37986N B37987M		B37982N B37988N		
Lead spacing 5.0 mm $5.5 \times 5.0 \times 2.5$ $6.5 \times 5.0 \times 2.5$ $9.0 \times 7.5 \times 2.5$	B37979G B37986G —	B37981F B37987F B37984M	B37982G B37988G B37985N		

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#### X7R

## Features

- High volumetric efficiency
- Non-linear capacitance change
- High insulation resistance
- High pulse strength
- To AEC-Q200

## Applications

- Blocking
- Coupling and decoupling
- Interference suppression

#### Termination

- Parallel wire leads, iron-nickel, tinned
- Crimped leads
- Non-standard lead lengths on request

## Marking

Rated capacitance, tolerance, manufacturer's logo, ceramic material, voltage

## **Delivery mode**

- Cardboard tape in Ammo packing (standard)
- Cardboard tape on 360-mm reel or bulk on request

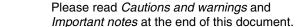
## **Electrical data**

Temperature characteristic		X7R	
Max. relative capacitance change			
within –55 °C to +125 °C	$\Delta C/C$	±15	%
Climatic category (IEC 60068-1)		55/125/56	
Standard		EIA	
Dielectric		Class 2	
Rated voltage <sup>1)</sup>	V <sub>R</sub>	50, 100	VDC
Test voltage	V <sub>test</sub>	2.5 · V <sub>R</sub> /5 s	VDC
Capacitance range / E series	C <sub>R</sub>	470 pF … 1 μF (E6)	
Dissipation factor (limit value)	tan δ	<25 · 10 <sup>-3</sup>	
Insulation resistance <sup>2)</sup> at + 25 °C	R <sub>ins</sub>	>10 <sup>5</sup>	MΩ
Insulation resistance <sup>2)</sup> at +125 °C	R <sub>ins</sub>	>104	MΩ
Time constant <sup>2)</sup> at + 25 °C	τ	>1000	s
Time constant <sup>2)</sup> at +125 °C	τ	>100	s
Operating temperature range	T <sub>op</sub>	-55 +125	°C
Ageing <sup>3)</sup>	L_	yes	

1) Note: No operation on AC line.

2) For  $C_R > 10$  nF the time constant  $\tau = C \cdot R_{ins}$  is given.

3) Refer to chapter "General technical information", "Ageing".



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Leaded

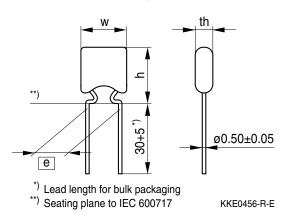


## **Capacitance tolerances**

Code letter	K (standard)	М
Tolerance	±10%	±20%

## **Dimensional drawing**

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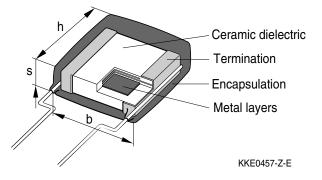


## **Dimensions (mm)**

	Lead spacing e = 2.5 +0.6/-0.1 mm				
Туре	B37981M	B37987M			
h <sub>max</sub>	5.5	6.5			
w <sub>max</sub>	5.0	5.0			
th <sub>max</sub>	2.5	2.5			

	Lead spacing $\boxed{e}$ = 5.0 +0.6/-0.1 mm				
Туре	B37981F	B37987F	B37984M		
h <sub>max</sub> w <sub>max</sub> th <sub>max</sub>	5.5 5.0 2.5	6.5 5.0 2.5	9.0 7.5 2.5		

## Termination



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## Product range leaded capacitors, X7R

Lead	spacing		2.5	mm				5.0	mm		
$\mathbf{h} \times \mathbf{b}$	$\times$ s (mm)	5.5  imes 5	.0 × 2.5	6.5  imes 5	.0 × 2.5	5.5  imes 5	.0 × 2.5	$6.5 \times 5$	.0 × 2.5	9.0  imes 7	.5 × 2.5
Туре		B379	981M	B379	987M	B379	981F	B37	987F	B379	984M
C <sub>R</sub>	V <sub>R</sub> (VDC)	50	100	50	100	50	100	50	100	50	
470	pF										
680	pF										
1.0	) nF										
1.5	5 nF										
2.2	2 nF										
3.3	3 nF										
4.7	7 nF										
6.8	3 nF										
10	nF										
15	nF										
22	nF										
33	nF										
47	nF										
68	nF										
100	nF										
150	nF										
220	nF										
330	nF										
470	nF										
680	nF										
1.0	DμF										





## X7R

## Ordering codes and packing for X7R, 50 VDC, lead spacing 2.5 mm

			Ammo packing	Reel packing	Bulk
			** ≙ 54	** ≙ 51	** ≙ 00
C <sub>R</sub>		Ordering code <sup>1)</sup>	pcs	pcs/reel	pcs
B379	981, 50	VDC			
3.3	3 nF	B37981M5332K0**	2500	2500	2000
4.7	7 nF	B37981M5472K0**	2500	2500	2000
6.8	3 nF	B37981M5682K0**	2500	2500	2000
10	nF	B37981M5103K0**	2500	2500	2000
15	nF	B37981M5153K0**	2500	2500	2000
22	nF	B37981M5223K0**	2500	2500	2000
33	nF	B37981M5333K0**	2500	2500	2000
47	nF	B37981M5473K0**	2500	2500	2000
B379	987, 50	VDC	·	·	·
68	nF	B37987M5683K0**	2500	2500	2000
100	nF	B37987M5104K0**	2500	2500	2000
150	nF	B37987M5154K0**	2500	2500	2000
220	nF	B37987M5224K0**	2500	2500	2000

1) The table contains the ordering codes for the standard capacitance tolerance. For other available capacitance tolerances see page 4.

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X7R

## Ordering codes and packing for X7R, 50 VDC, lead spacing 5.0 mm

		Ammo packing	Reel packing	Bulk
		** ≙ 54	** ≙ 51	** ≙ 00
C <sub>R</sub>	Ordering code <sup>1)</sup>	pcs	pcs/reel	pcs
B37981, 50	) VDC			
3.3 nF	B37981F5332K0**	2500	2500	2000
4.7 nF	B37981F5472K0**	2500	2500	2000
6.8 nF	B37981F5682K0**	2500	2500	2000
10 nF	B37981F5103K0**	2500	2500	2000
15 nF	B37981F5153K0**	2500	2500	2000
22 nF	B37981F5223K0**	2500	2500	2000
33 nF	B37981F5333K0**	2500	2500	2000
47 nF	B37981F5473K0**	2500	2500	2000
B37987, 50	VDC			
68 nF	B37987F5683K0**	2500	2500	2000
100 nF	B37987F5104K0**	2500	2500	2000
150 nF	B37987F5154K0**	2500	2500	2000
220 nF	B37987F5224K0**	2500	2500	2000
B37984, 50	) VDC			
330 nF	B37984M5334K0**	2000	2000	2000
470 nF	B37984M5474K0**	2000	2000	2000
680 nF	B37984M5684K0**	2000	2000	2000
1.0 μF	B37984M5105K0**	2000	2000	2000

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<sup>1)</sup> The table contains the ordering codes for the standard capacitance tolerance. For other available capacitance tolerances see page 4.





## X7R

## Ordering codes and packing for X7R, 100 VDC, lead spacing 2.5 mm

			Ammo packing	Reel packing	Bulk
			** ≙ 54	** ≙ 51	** ≙ 00
C <sub>R</sub>		Ordering code <sup>1)</sup>	pcs	pcs/reel	pcs
B379	81, 100	VDC			
470	pF	B37981M1471K0**	2500	2500	2000
680	pF	B37981M1681K0**	2500	2500	2000
1.0	) nF	B37981M1102K0**	2500	2500	2000
1.5	5 nF	B37981M1152K0**	2500	2500	2000
2.2	2 nF	B37981M1222K0**	2500	2500	2000
3.3	3 nF	B37981M1332K0**	2500	2500	2000
4.7	∕ nF	B37981M1472K0**	2500	2500	2000
6.8	3 nF	B37981M1682K0**	2500	2500	2000
10	nF	B37981M1103K0**	2500	2500	2000
15	nF	B37981M1153K0**	2500	2500	2000
B379	87, 100	VDC			
22	nF	B37987M1223K0**	2500	2500	2000
33	nF	B37987M1333K0**	2500	2500	2000
47	nF	B37987M1473K0**	2500	2500	2000
68	nF	B37987M1683K0**	2500	2500	2000
100	nF	B37987M1104K0**	2500	2500	2000
150	nF	B37987M1154K0**	2500	2500	2000

1) The table contains the ordering codes for the standard capacitance tolerance. For other available capacitance tolerances see page 4.

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X7R

## Ordering codes and packing for X7R, 100 VDC, lead spacing 5.0 mm

			Ammo packing	Reel packing	Bulk
			** ≙ 54	** ≙ 51	** ≙ 00
C <sub>R</sub>		Ordering code <sup>1)</sup>	pcs/reel	pcs	pcs
B379	81, 100	) VDC			
470	pF	B37981F1471K0**	2500	2500	2000
680	pF	B37981F1681K0**	2500	2500	2000
1.0	) nF	B37981F1102K0**	2500	2500	2000
1.5	5 nF	B37981F1152K0**	2500	2500	2000
2.2	2 nF	B37981F1222K0**	2500	2500	2000
3.3	3 nF	B37981F1332K0**	2500	2500	2000
4.7	′ nF	B37981F1472K0**	2500	2500	2000
6.8	3 nF	B37981F1682K0**	2500	2500	2000
10	nF	B37981F1103K0**	2500	2500	2000
15	nF	B37981F1153K0**	2500	2500	2000
B379	87, 100	) VDC			
22	nF	B37987F1223K0**	2500	2500	2000
33	nF	B37987F1333K0**	2500	2500	2000
47	nF	B37987F1473K0**	2500	2500	2000
68	nF	B37987F1683K0**	2500	2500	2000
100	nF	B37987F1104K0**	2500	2500	2000
150	nF	B37987F1154K0**	2500	2500	2000

1) The table contains the ordering codes for the standard capacitance tolerance. For other available capacitance tolerances see page 4.

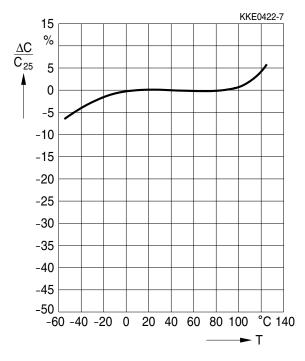
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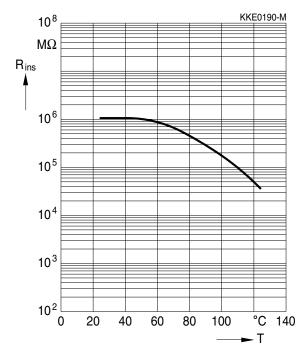


## **Typical characteristics**

Capacitance change  ${\scriptstyle \Delta C/C_{25}}$  versus temperature T

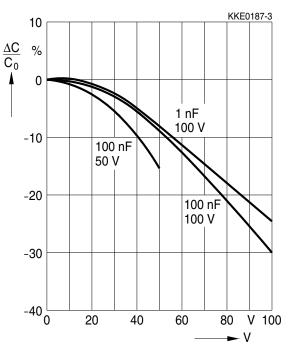


Insulation resistance  ${\rm R}_{\rm ins}$  versus temperature T

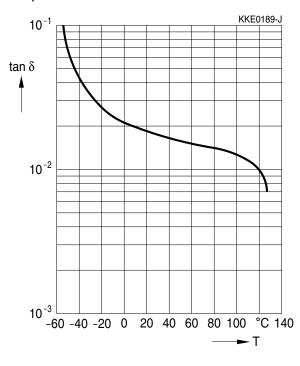


Capacitance change  $\Delta C/C_0$  versus superimposed DC voltage V

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Dissipation factor tan  $\delta$  versus temperature T



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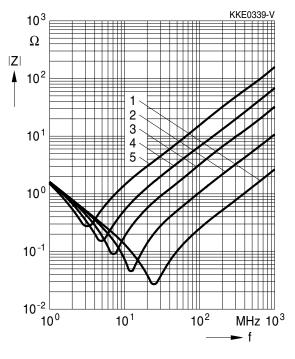




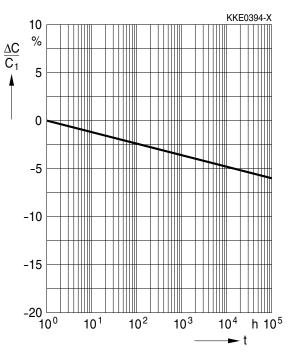
X7R

## **Typical characteristics**

Impedance |Z| versus frequency f



Capacitance change  ${\it \Delta C/C_1}$  versus time t



- 1: SMD chip capacitor
- 2: 1.5 mm lead length
- 3: 5.0 mm lead length
- 4: 10.0 mm lead length
- 5: 20.0 mm lead length



#### **Cautions and warnings**

#### Notes on the selection of ceramic capacitors

In the selection of ceramic capacitors, the following criteria must be considered:

- 1. Depending on the application, ceramic capacitors used to meet high quality requirements should at least satisfy the specifications to AEC-Q200. They must meet quality requirements going beyond this level in terms of ruggedness (e.g. mechanical, thermal or electrical) in the case of critical circuit configurations and applications (e.g. in safety-relevant applications such as ABS and airbag equipment or durable industrial goods).
- 2. At the connection to the battery or power supply (e.g. clamp 15 or 30 in the automobile) and at positions with stranding potential, to reduce the probability of short circuits following a fracture, two ceramic capacitors must be connected in series and/or a ceramic capacitor with integrated series circuit should be used. The MLSC from EPCOS contains such a series circuit in a single component.
- 3. Ceramic capacitors with the temperature characteristics Z5U and Y5V do not satisfy the requirements to AEC-Q200 and are mechanically and electrically less rugged than C0G or X7R/X8R ceramic capacitors. In applications that must satisfy high quality requirements, therefore, these capacitors should not be used as discrete components (see the chapter "Effects on mechanical, thermal and electrical stress", point 1.4).
- 4. For ESD protection, preference should be given to the use of multilayer varistors (MLV) (see the chapter "Effects on mechanical, thermal and electrical stress", point 1.4).
- 5. An application-specific derating or continuous operating voltage must be considered in order to cushion (unexpected) additional stresses (see the chapter "Reliability").

## The following should be considered in circuit board design

- 1. If technically feasible in the application, preference should be given to components having an optimal geometrical design.
- 2. At least FR4 circuit board material should be used.
- 3. Geometrically optimal circuit boards should be used, ideally those that cannot be deformed.
- 4. Ceramic capacitors must always be placed a sufficient minimum distance from the edge of the circuit board. High bending forces may be exerted there when the panels are separated and during further processing of the board (such as when incorporating it into a housing).
- 5. Ceramic capacitors should always be placed parallel to the possible bending axis of the circuit board.
- 6. No screw connections should be used to fix the board or to connect several boards. Components should not be placed near screw holes. If screw connections are unavoidable, they must be cushioned (for instance by rubber pads).



#### **Cautions and warnings**

## The following should be considered in the placement process

- 1. Ensure correct positioning of the ceramic capacitor on the solder pad.
- 2. Caution when using casting, injection-molded and molding compounds and cleaning agents, as these may damage the capacitor.
- 3. Support the circuit board and reduce the placement forces.
- 4. A board should not be straightened (manually) if it has been distorted by soldering.
- 5. Separate panels with a peripheral saw, or better with a milling head (no dicing or breaking).
- 6. Caution in the subsequent placement of heavy or leaded components (e.g. transformers or snap-in components): danger of bending and fracture.
- 7. When testing, transporting, packing or incorporating the board, avoid any deformation of the board not to damage the components.
- 8. Avoid the use of excessive force when plugging a connector into a device soldered onto the board.
- 9. Ceramic capacitors must be soldered only by the mode (reflow or wave soldering) permissible for them (see the chapter "Soldering directions").
- 10. When soldering the most gentle solder profile feasible should be selected (heating time, peak temperature, cooling time) in order to avoid thermal stresses and damage.
- 11. Ensure the correct solder meniscus height and solder quantity.
- 12. Ensure correct dosing of the cement quantity.
- 13. Ceramic capacitors with an AgPd external termination are not suited for the lead-free solder process: they were developed only for conductive adhesion technology.

This listing does not claim to be complete, but merely reflects the experience of EPCOS AG.



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