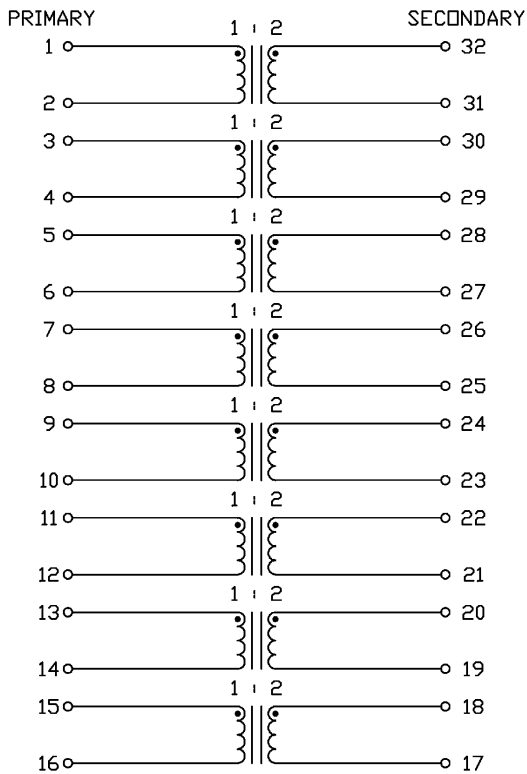


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SCHEMATIC

ELECTRICAL CHARACTERISTICS @25°C



<p>URNS RATIO PRIMARY : SECONDARY</p> <p>PHASE</p> <p>INDUCTANCE PRIMARY AT -40°C</p> <p>LEAKAGE INDUCTANCE (Le) PRIMARY (SECONDARY SHORTED)</p> <p>INTERWINDING CAPACITANCE (Cw/W) PRIMARY TO SECONDARY</p> <p>DC RESISTANCE PRIMARY SECONDARY</p> <p>HIPOT</p>	<p>1 : 2</p> <p>PER DOT CONVENTION</p> <p>1.5mH MIN. @10kHz, 20mV 640µH MIN.</p> <p>0.30µH MAX. @1MHz, 20mV</p> <p>35pF MAX. @1MHz, 20mV</p> <p>0.60Ω MAX. 1.20Ω MAX.</p> <p>PER HAND-WORK-03</p>
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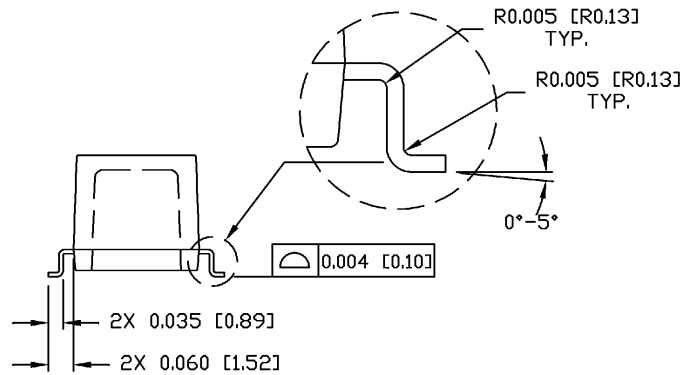
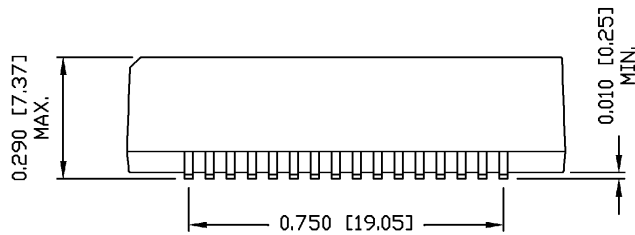
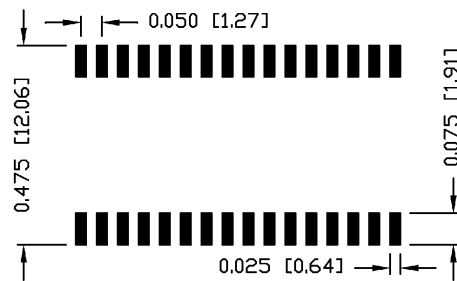
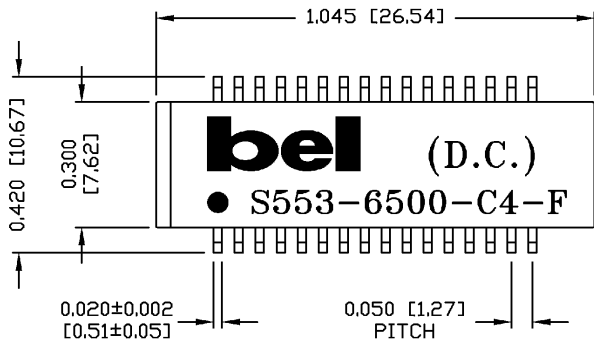
ORIGINATED BY CHAN CH	DATE 10-02-07	TITLE ELECTRICAL SPECIFICATION S553-6500-C4-F	PART NO. / DRAWING NO. X5536500C4-F	STANDARD DIM. TOL IN INCH .X .XX .XXX	[] METRIC DIM. AS REFERENCE UNIT : INCH [mm] SCALE : N/A	REV. : A SIZE : A4 PAGE : 2	bel COMPONENTS ON A CONNECTED PLANET
DRAWN BY DQ Chen	DATE 10-02-07		FILE NAME X5536500C4FA.DWG				

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SUGGESTED PCB PAD LAYOUT



NOTES:

1. STANDARD MARKING REFER TO DDC, HAND-WORK-04.
2. PACKAGE CODE: "RDS001".

ORIGINATED BY Lawrence Tsang	DATE 10-02-07	TITLE MECHANICAL OUTLINE S553-6500-C4-F	PART NO. / DRAWING NO. X5536500C4-F	STANDARD DIM. TOL IN INCH	[] METRIC DIM. AS REFERENCE	REV. : A SIZE : A4 PAGE : 3	 COMPONENTS ON A CONNECTED PLANET			
DRAWN BY DQ Chen	DATE 10-02-07		FILE NAME X5536500C4FA.DWG	<table border="1"> <tr> <td>X</td> <td>±0.01</td> </tr> <tr> <td>.XX</td> <td>±0.01</td> </tr> <tr> <td>.XXX</td> <td>±0.005</td> </tr> </table>	X			±0.01	.XX	±0.01
X	±0.01									
.XX	±0.01									
.XXX	±0.005									

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