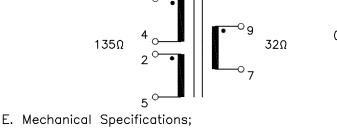
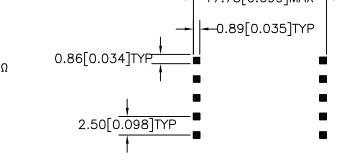
HDSL transformer designed for use with Brooktree HDSL IC chips BT8921/BT8970 REV. Status at 1168kbps rates

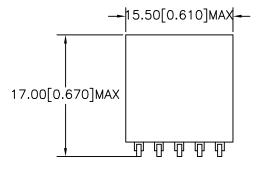
- A. Electrical Specifications (@ 25°C)
 - 1. Primary (Line side) Impedance; 135Ω
 - 2. Secondary (IC side) Impedance; 32Ω
 - 3. Longitudinal Balance; 50dBm MIN @ 40KHz TO 300KHz, 0dBm
 - 4. Frequency Response; ±0.1dB @ 33KHz to 110KHz, 0dBm, 40Khz reference
 - 5. Insertion Loss; 0.5dB MAX @ 40KHz, 0dBm
 - 6. Total Harmonic Distortion; -70dB MAX @ 20KHz, 1.85Vrms
 - 7. Primary Open Circuit Inductance; 2.0mH ±10% @ 10KHz, 0.1Vrms Measured (1-5) with 2 and 4 shorted
 - 8. Leakage Inductance; 11 $\mu\rm H$ MAX @ 100KHz, 0.1Vrms Measured (1-5) with 2 & 4, and 7 & 9 shorted
 - 9. DC Current; 160mA
 - 10. DC Resistance; $(1-4)=1.25 \Omega \pm 10\%$ $(2-5)=1.25 \Omega \pm 10\%$ (7-9)=1.25Ω ±10%
 - 11. Turns Ratio; $(1-4):(2-5)=1:1.00\pm2\%$ $(7-9):(1-5)=1:2.00\pm2\%$ 2 & 4 shorted
 - 12. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec
- B. Marking; TTC-4021, TAMURA, date code and country of origin
- C. Safety; Complies with UL1950 3rd Edition and IEC950
- D. Schematic; SEC PRI IC Side Line Side 0.86[0.034]1 135Ω 32Ω

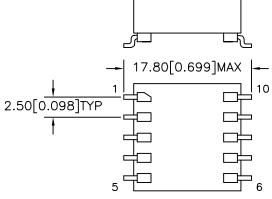




15.50[0.610]MAX

Date Code -





PREPARED BY:

PRELIMINARY 02/03/00 MP

D. Rund			<u> </u>			
ENGINEER:	DWG CONTROL NO.	REV	HDSL			
M. Pitchai	P-A1-12349 ACAD\TTC\A1123491.DWG	PRE	TRANSFORMER	T	TC-40	021
QUALITY CONTROL:	CONTENTS OF THIS DRAWING ARE		TAMURA CORPORATION OF AMERICA	MODE	EL SPECIF	ICATIO
D. Kelley	PRIOR NOTICE	1001	43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624 (909) 699-1270 FAX 9096769482	DIM: mm(In)	SCL: 2/1	SH: 1
APPROVED:			RINT OR DOCUMENT AND SUBJECT MATTER DISCLOSED HE		DIETARY ITEMS	TO WHIC