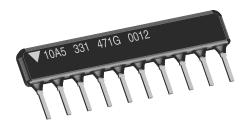
### Vishay Dale



## Thick Film Resistor Networks, Single-In-Line, **Conformal Coated SIP**



#### **FEATURES**

· Isolated, bussed and dual terminator schematics available



RoHS\*

COMPLIANT

Body height: "A" profile = 0.195" (4.95 mm) and "B" profile = 0.295" (7.50 mm) standard; custom "C" profile = 0.350" (8.89 mm) also available
 "A" profile standard in 4 thru 12 pins

• Thick film resistive elements

Reduces total assembly costs

 Resistor elements protected by tough epoxy conformal coating

• Wide resistance range (10  $\Omega$  to 2.2 M $\Omega$ )

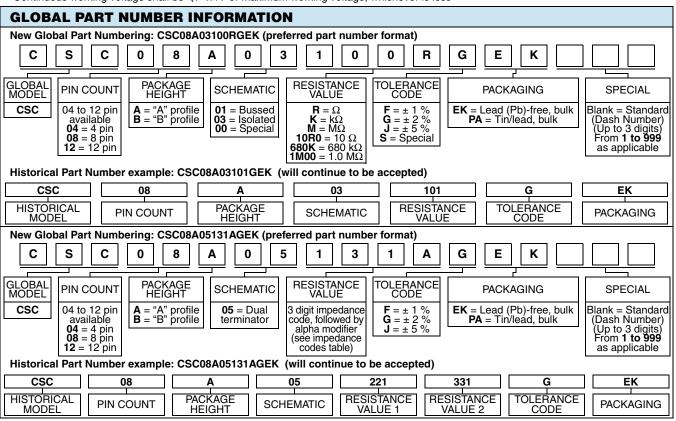
- Available in bulk pack as standard; optional tube pack is also available
- Meets EIA/ECA-CB23 rev. G whisker test requirements for Class 1A products
- Compliant to RoHS directive 2002/95/EC

STANDARD ELECTRICAL SPECIFICATIONS							
GLOBAL MODEL/ SCHEMATIC	PACKAGE HEIGHT	POWER RATING ELEMENT (1) P70 °C W	$\begin{array}{c} \textbf{RESISTANCE} \\ \textbf{RANGE} \\ \Omega \end{array}$	TEMP. COEFFICIENT (- 55 °C to + 125 °C) ± ppm/°C	TOL. <sup>(2)</sup> ± %	TCR TRACKING (1) (- 55 °C to + 125 °C) ± ppm/°C	MAX. WORKING VOLTAGE <sup>(3)</sup> V <sub>DC</sub>
CSCxxx01	A B	0.20 0.25	10 to 50 50.1 to 2.2M	250 100	1, 2, 5	50	100
CSCxxx03	A B	0.30 0.40	10 to 50 50.1 to 2.2M	250 100	1, 2, 5	50	100
CSCxxx05	A B	0.20 0.25	10 to 50 50.1 to 2.2M	250 100	1, 2, 5	150	100

#### Notes

See derating curves for package power rating
 For resistor power ratings at + 25 °C see derating curves
 ± 2 % standard, ± 1 % and ± 5 % available

(3) Continuous working voltage shall be  $\sqrt{P \times R}$  or maximum working voltage, whichever is less



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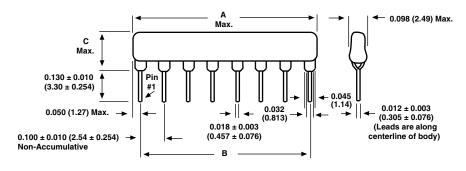


### Thick Film Resistor Networks, Single-In-Line, Conformal Coated SIP

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TECHNICAL SPECIFICATIONS				
PARAMETER	UNIT	CSC SERIES		
Voltage Coefficient of Resistance	V <sub>eff</sub>	< 50 ppm typical		
Dielectric Strength	V <sub>AC</sub>	200		
Isolation Resistance (03 Schematic)	Ω	> 100M		
Operating Temperature Range	°C	- 55 to + 125		

### **DIMENSIONS** in inches (millimeters)



01 SCHEMATIC	GLOBAL MODEL	NUMBER OF RESISTORS	A (Maximum)	В	C (Maximum)
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	CSC04	3	0.390 (9.91)	0.300 (7.62)	
	CSC05	4	0.490 (12.45)	0.400 (10.16)	
	CSC06	5	0.590 (14.99)	0.500 (12.70)	
	CSC07	6	0.690 (17.53)	0.600 (15.24)	"A" 61 0 405 (4.05)
	CSC08	7	0.790 (20.07)	0.700 (17.78)	"A" profile = 0.195 (4.95) "B" profile = 0.295 (7.50)
0 0 0 0 0 0 1 2 3 n-1 n	CSC09	8	0.890 (22.61)	0.800 (20.32)	
	CSC10	9	0.990 (25.15)	0.900 (22.86)	
	CSC11	10	1.09 (27.69)	1.00 (25.40)	
	CSC12	11	1.19 (30.23)	1.100 (27.94)	
03 SCHEMATIC	GLOBAL MODEL	NUMBER OF RESISTORS	A (Maximum)	В	C (Maximum)
	CSC04	2	0.390 (9.91)	0.300 (7.62)	
	CSC06	3	0.590 (14.99)	0.500 (12.70)	"A" ("I 0 405 (4 05)
	CSC08	4	0.790 (20.07)	0.700 (17.78)	"A" profile = 0.195 (4.95) "B" profile = 0.295 (7.50)
1 2 3 4 n-1 n	CSC10	5	0.990 (25.15)	0.900 (22.86)	B prome = 0.200 (7.00)
1 2 3 4 11-1 11	CSC12	6	1.19 (30.23)	1.100 (27.94)	
05 SCHEMATIC	GLOBAL MODEL	NUMBER OF RESISTORS	A (Maximum)	В	C (Maximum)
\$ \$ R <sub>2</sub> \$	CSC04	4	0.390 (9.91)	0.300 (7.62)	
	CSC05	6	0.490 (12.45)	0.400 (10.16)	
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	CSC06	8	0.590 (14.99)	0.500 (12.70)	
	CSC07	10	0.690 (17.53)	0.600 (15.24)	"A" 61 0 405 (4.05)
	CSC08	12	0.790 (20.07)	0.700 (17.78)	"A" profile = 0.195 (4.95) "B" profile = 0.295 (7.50)
	CSC09	14	0.890 (22.61)	0.800 (20.32)	
1 2 3 n-1 n	CSC10	16	0.990 (25.15)	0.900 (22.86)	
	CSC11	18	1.09 (27.69)	1.00 (25.40)	
	CSC12	20	1.19 (30.23)	1.100 (27.94)	

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### Thick Film Resistor Networks, Single-In-Line, Conformal Coated SIP



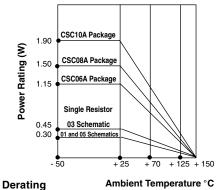
MECHANICAL SPECIFICATIONS				
Marking Resistance to Solvents	Permanency testing per MIL-STD-202, method 215			
Solderability	Per MIL-STD-202, method 208E, RMA flux			
Body	High alumina, epoxy coated			
Terminals	Solder plated leads			

#### STOCKED RESISTANCE VALUES IN OHMS ("G" TOLERANCE)

Standard E-24 resistance values stocked. Consult factory. Many dual terminator resistance values stocked. Consult factory.

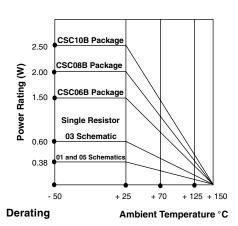
IMPEDANCE CODES					
CODE	R <sub>1</sub> (Ω)	$R_2(\Omega)$	CODE	R <sub>1</sub> (Ω)	R <sub>2</sub> (Ω)
500B	82	130	141A	270	270
750B	120	200	181A	330	390
800C	130	210	191A	330	470
990A	160	260	221B	330	680
101C	180	240	281B	560	560
111C	180	270	381B	560	1.2K
121B	180	390	501C	620	2.7K
121C	220	270	102A	1.5K	3.3K
131A	220	330	202B	3K	6.2K

#### "A" Profile



"B" Profile

Ambient Temperature °C



"A" PROFILE + 70 °C PACKAGE RATINGS				
CSC12A	1.5 W			
CSC11A	1.37 W			
CSC10A	1.25 W			
CSC09A	1.12 W			
CSC08A	1.00 W			
CSC07A	0.87 W			
CSC06A	0.75 W			
CSC05A	0.62 W			
CSC04A	0.40 W			

"B" PROFILE + 70 °C PACKAGE RATINGS				
CSC12B	1.90 W			
CSC11B	1.75 W			
CSC10B	1.60 W			
CSC09B	1.45 W			
CSC08B	1.30 W			
CSC07B	1.15 W			
CSC06B	1.00 W			
CSC05B	0.80 W			
CSC04B	0.60 W			

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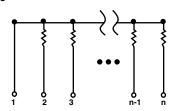


### Thick Film Resistor Networks, Single-In-Line, Conformal Coated SIP

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#### **CIRCUIT APPLICATIONS**

01 Schematic

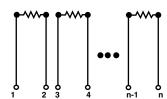


#### Bussed

The CSCxxx01 single-in-line resistor networks provide the user with nominally equal resistors, each connected to a common pin (pin no. 1). Commonly used in the following applications:

- "Wired OR" Pull-up Power Gate Pull-up
- Open Collector Pull-up
- Pull-up TTL Input Pull-down
- MOS/ROM Pull-up/Pull-down
   TTL Unused Gate Pull-up
- \* "A" profile standard, "B" Profile available.

#### 03 Schematic

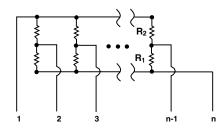


#### Isolated

The CSCxxx03 single-in-line resistor networks provide the user with nominally equal resistors. Each resistor is isolated from all others. Commonly used in the following applications:

- "Wired OR" Pull-up
- Long-Line Impedance Balancing
- Power Driven Pull-up
   Dewer Cete Pull-up
- LED Current LimitingECL Output Pull-down
- Power Gate Pull-upLine Termination
- TTL Input Pull-down
- \* "A" Profile standard, "B" Profile available.

#### 05 Schematic



#### **Dual Terminator**

The CSCxxx05 circuits contain series pairs of resistors. Each series pair is connected between two common lines. The junction of these resistor pairs is connected to the input terminals. The 05 circuits are designed for TTL dual-line termination and pulse squaring.

\* "A" profile standard, "B" Profile available.

PERFORMANCE				
TEST	CONDITIONS	MAX. △R (TYPICAL TEST LOTS)		
Thermal Shock	5 cycles between - 65 °C and + 125 °C	± 0.50 % ΔR		
Short Time Overload	2.5 x rated working voltage, 5 s	± 0.25 % ΔR		
Low Temperature Operation	45 min at full rated working voltage at - 65 °C	± 0.25 % ΔR		
Moisture Resistance	240 h with humidity ranging from 80 $\%$ RH to 98 $\%$ RH	± 1.00 % ΔR		
Resistance to Soldering Heat	Leads immersed in + 350 °C solder to within 1/16" of body for 3 s	± 0.25 % ΔR		
Shock	Total of 18 shocks at 100 g's	± 0.25 % ΔR		
Vibration	12 h at maximum of 20 g's between 10 Hz and 2000 Hz	± 0.25 % ΔR		
Load Life	1000 h at + 70 °C, rated power applied 1.5 h "ON", 0.5 h "OFF" for full 1000 h period. Derated according to the curve.	± 1.00 % ΔR		
Terminal Strength	4.5 pound pull for 30 s	± 0.25 % ΔR		
Insulation Resistance	10 000 M $\Omega$ (minimum)	-		
Dielectric Withstanding Voltage	No evidence of arcing or damage (200 V <sub>RMS</sub> for 1 min)	-		

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