



PLA191 Single Pole, Normally Open OptoMOS[®] Relay

Parameters	Ratings	Units
Blocking Voltage	400	V _P
Load Current	250	mA
Max On-resistance	8	Ω

Features

- 5000V_{rms} Input/Output Isolation
- Small 6-Pin Package
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

Applications

- Telecommunications
 - Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebooks, Pocket Size)
 - Hook Switch
 - Dial Pulsing
 - Ground Start
 - Ringing Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

PLA191 is a single-pole, normally open (1-Form-A) solid state relay that uses optically coupled MOSFET technology to provide 5000V_{rms} of enhanced input to output isolation.

The efficient MOSFET switches and photovoltaic die use Clare's patented OptoMOS architecture while the optically coupled output is controlled by a highly efficient GaAIAs infrared LED.

Approvals

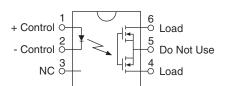
- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: TUV Certificate B 09 07 49410 004

Ordering Information

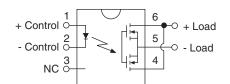
Part #	Description
PLA191	6-Pin DIP (50/Tube)
PLA191S	6-Pin Surface Mount (50/Tube)
PLA191STR	6-Pin Surface Mount (1000/Reel)

Pin Configuration

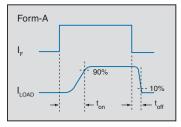
AC/DC Configuration



DC Only Configuration



Switching Characteristics of Normally Open Devices





DS-PLA191-R02



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	400	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Package Dissipation ²	800	mW
Isolation Voltage, Input to Output	5000	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	٥C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

¹ Derate Linearly 1.33 mW / °C ² Derate Linearly 6.67 mW / °C

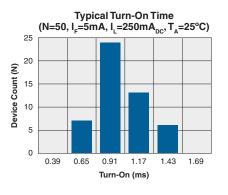
Electrical Characteristics @ 25°C

Parameters	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics	,,-					
Load Current, Continuous						
AC/DC Configuration	-		-	-	250	
DC Configuration	-		-	-	350	mA
Peak Load Current	t=10ms	I _{LPK}	-	-	500	mA
On-Resistance ¹						
AC/DC Configuration	I _L =150mA		-	-	8	
DC Configuration	I _L =250mA	– R _{ON} –	-	-	3	Ω
Off-State Leakage Current	V _L =400V _P	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On		t _{on}	-	-	3	ms
Turn-Off	I _F =5mA, V _L =10V	t _{off}	-	-	1	ms
Output Capacitance	V _L =50V, f=1MHz	C _{OUT}	-	65	-	pF
Input Characteristics			ļ		1	1
Input Control Current	I _L =150mA	I _F	-	-	5	mA
Input Dropout Current	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics		I				1
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

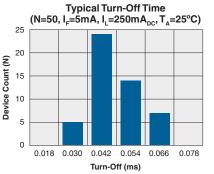
¹ Measurement taken within 1 second of on-time.

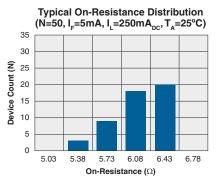


PLA191

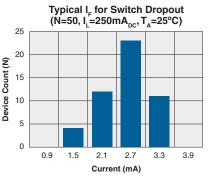


PERFORMANCE DATA*

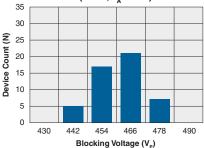


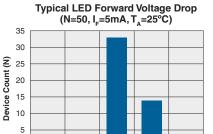


Typical I_F for Switch Operation $(N=50, I_{L}=250 \text{mA}_{DC}, T_{A}=25^{\circ}\text{C})$ 25 20 Device Count (N) 15 10 5 0 2.1 1.5 2.7 3.3 3.9 4.5 Current (mA)



Typical Blocking Voltage Distribution (N=50, T₄=25°C)

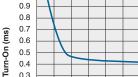




1.21

LED Forward Voltage Drop (V)

1.23



1.0

0.3

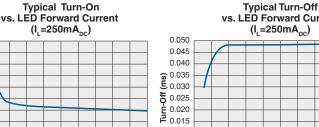
0.2

0.1

1.25

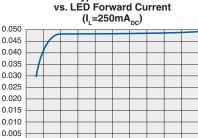
0

0 5 10 15

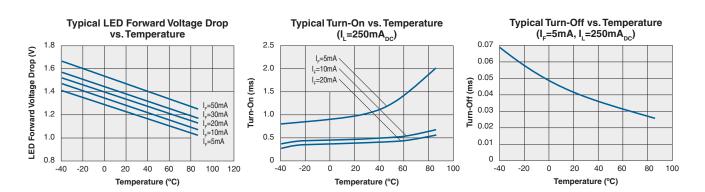


0

0



5 10 15 20 25 30 35 40 45 50 LED Forward Current (mA)



20 25 30

LED Forward Current (mA)

35 40 45 50

*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

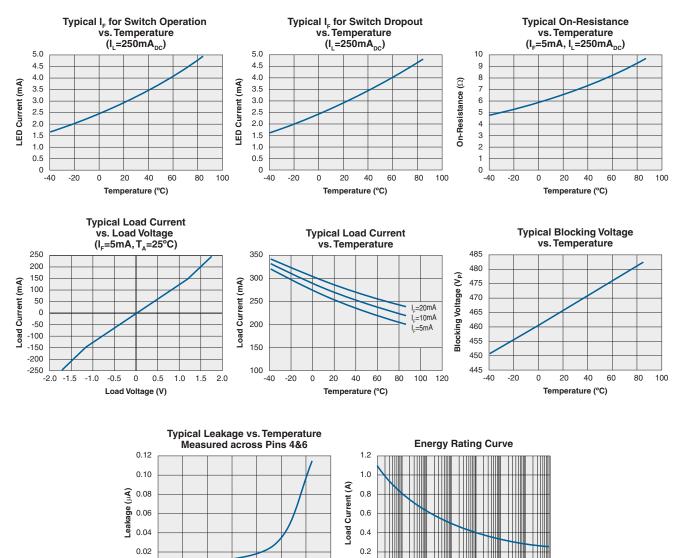
0

1.17

1.19



PERFORMANCE DATA*



0

10µs 100µs 1ms 10ms 100ms 1s

Time

10s 100s

80 100

Temperature (°C)

*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

0

-40 -20 0 20 40 60



Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. Clare classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
PLA191 / PLA191S	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
PLA191 / PLA191S	250°C for 30 seconds

Board Wash

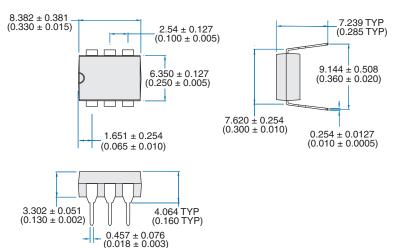
Clare recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since Clare employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.



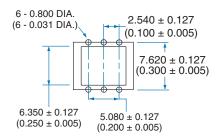


MECHANICAL DIMENSIONS

PLA191

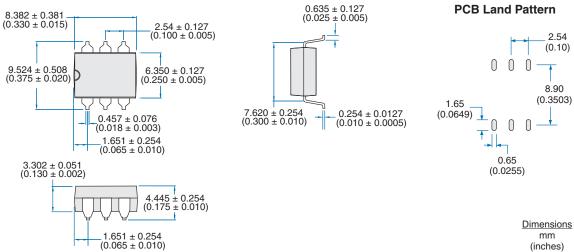


PCB Hole Pattern



Dimensions mm (inches)

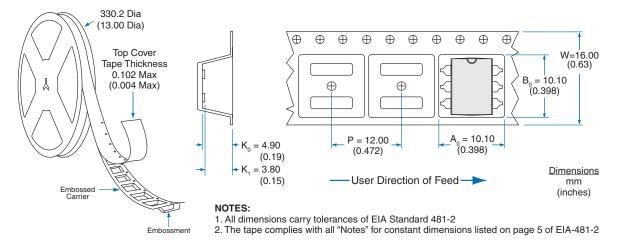
PLA191S



mm (inches)



PLA191S Tape & Reel



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