



	LCA210	Units
Load Voltage	350	V
Load Current	85	mA
Max R _{ON}	35	Ω

Features

- Small 8 Pin DIP Package
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- 3750V_{RMS} Input/Output Isolation
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Surface Mount and Tape & Reel Versions Available

Applications

- Telecom Switching
 - Tip/Ring Circuits
 - Modem Switching (Laptop, Notebook, Pocket Size)
 - Hookswitch
 - Dial Pulsing
 - Ground Start
 - Ringer Injection
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
 - Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

The LCA210 is a common input 1-Form 2A solid state relay which has two independent optically couple MOSFETS controlled by a common input signal. The efficient MOSFET switches and photovoltaic die use Clare's patented OptoMOS® architecture to provide 3750 V_{RMS} of input to output isolation. The optically coupled input is controlled by highly efficient GaAIAs infrared LEDs. Common input OptoMOS relays can replace standard dual pole relays in a variety of applications. The common input relay eliminates the need to make an external circuit connection when both poles are controlled by a common signal.

Approvals

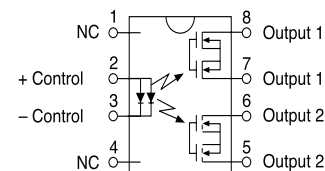
- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-10
- BSI Certified to:
 - BS EN 60950:1992 (BS7002:1992) Certificate #: 7344
 - BS EN 41003:1993 Certificate #: 7344

Ordering Information

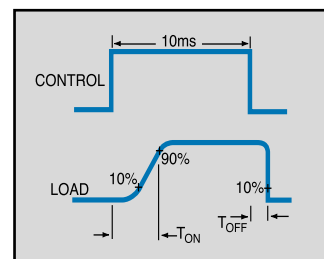
Part #	Description
LCA210	8 Pin DIP (50/Tube)
LCA210S	8 Pin Surface Mount (50/Tube)
LCA210STR	8 Pin Surface Mount (1000/Reel)

Pin Configuration

LCA210 Pinout



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings (@ 25° C)

Parameter	Min	Typ	Max	Units
Input Power Dissipation	-	-	150 ¹	mW
Input Control Current Peak (10ms)	-	-	100 1	mA A
Reverse Input Voltage	-	-	5	V
Total Power Dissipation	-	-	800 ²	mW
Isolation Voltage Input to Output	3750	-	-	V _{RMS}
Operational Temperature	-40	-	+85	°C
Storage Temperature	-40	-	+125	°C
Soldering Temperature DIP Package	-	-	+260	°C
Flatpack/Surface Mount Package (10 Seconds Max.)	-	-	+220	°C

¹ Derate Linearly 1.33 mw/°C

² Derate Linearly 6.67 mw/°C

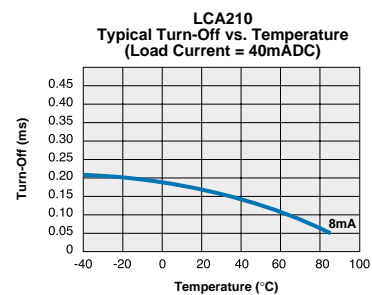
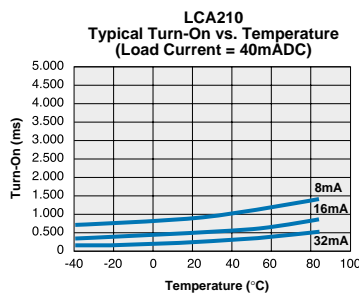
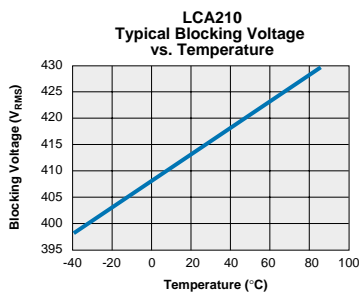
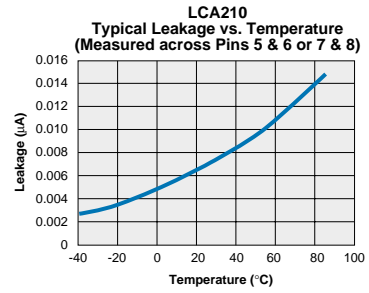
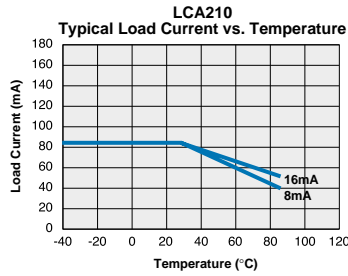
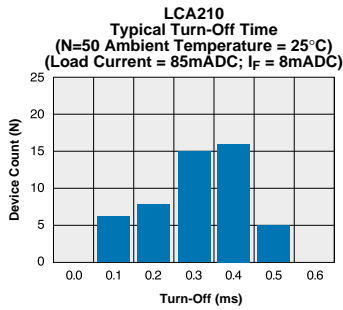
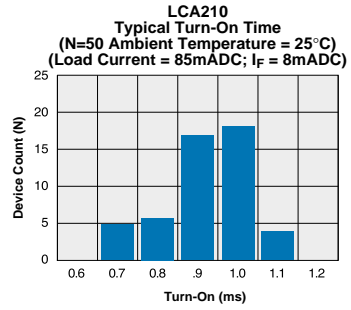
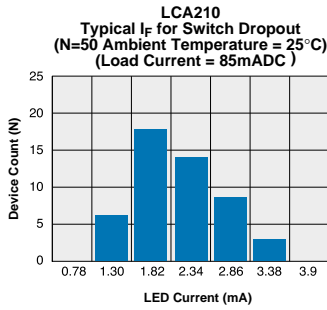
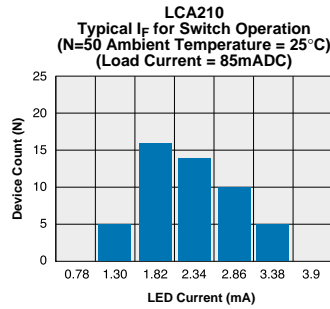
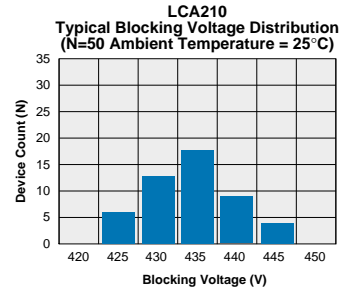
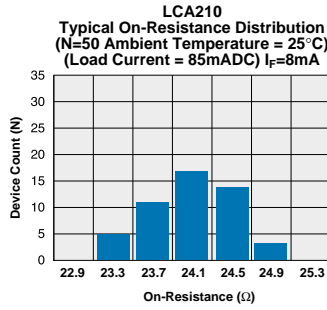
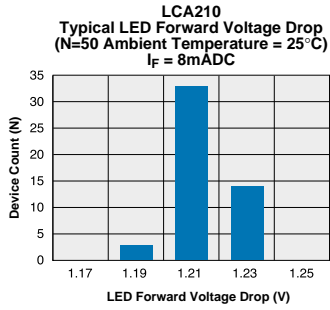
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.

Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Voltage (Peak)	-	V _L	-	-	350	V
Load Current (Continuous)	-	I _L	-	-	85	mA
Peak Load Current	10ms max	I _{LPK}	-	-	170	mA
On-Resistance	I _L =85mA	R _{ON}	-	25	35	Ω
Off-State Leakage Current	V _L =350V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =8mA, V _L =10V	T _{ON}	-	-	3	ms
Turn-Off	I _F =8mA, V _L =10V	T _{OFF}	-	-	3	ms
Output Capacitance	50V; f=1MHz	C _{OUT}	-	25	-	pF
*Input Characteristics @ 25°C						
Input Control Current	I _L =120mA	I _F	8	-	100	mA
Input Dropout Current	-	-	0.8	1.4	-	mA
Input Voltage Drop	I _F =8mA	V _F	0.9	1.2	1.4	V
Reverse Input Voltage	-	V _R	-	-	5	V
Reverse Input Current	V _R =5V	I _R	-	-	20	μA
Input to Output Capacitance	-	C _{VO}	-	3	-	pF
Input to Output Isolation	-	V _{VO}	3750	-	-	V _{RMS}

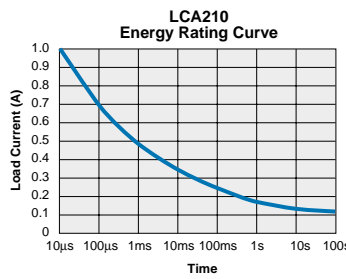
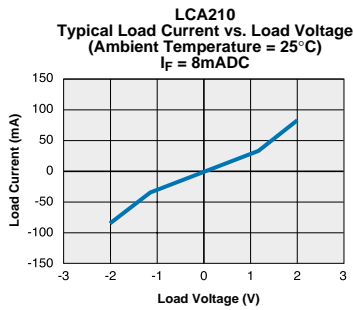
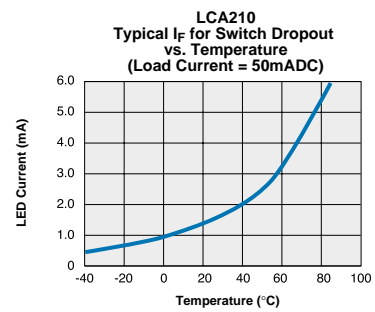
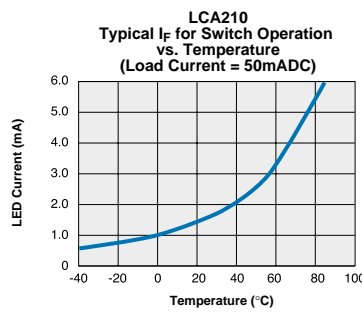
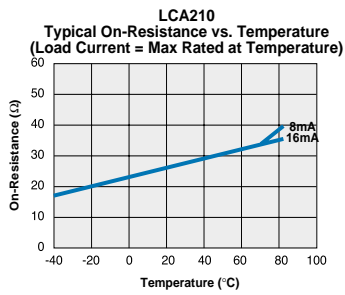
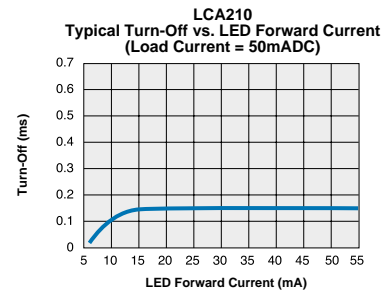
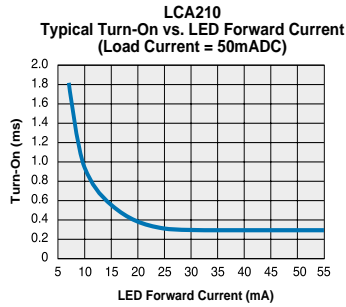
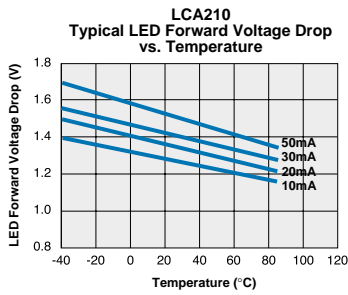
*Input characteristics represent requirements of two parallel connected LEDs.

PERFORMANCE DATA*



The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

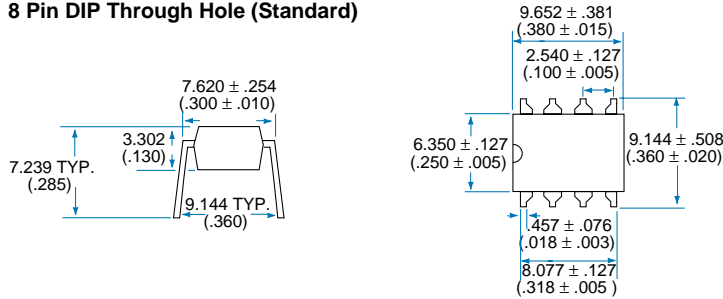
PERFORMANCE DATA*



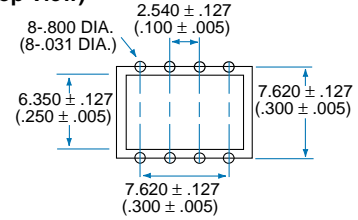
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Mechanical Dimensions

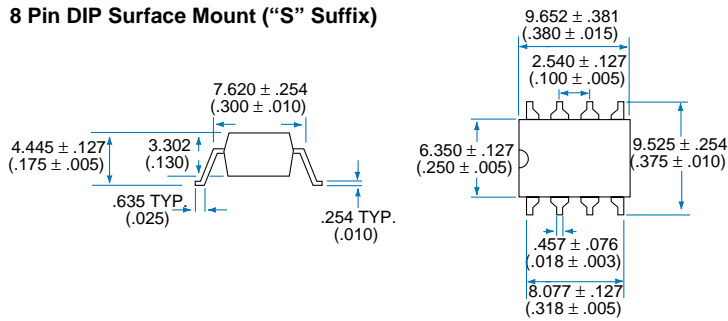
8 Pin DIP Through Hole (Standard)



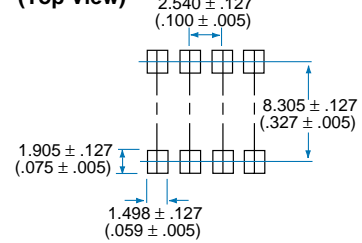
PC Board Pattern (Top View)



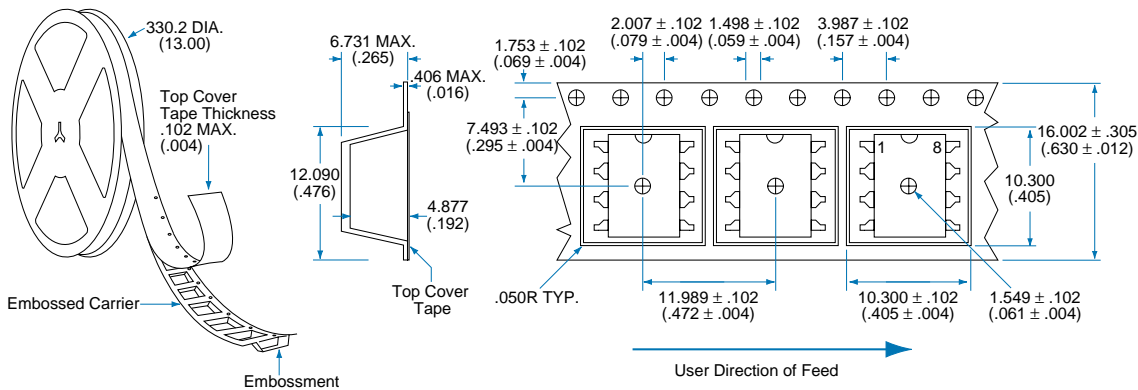
8 Pin DIP Surface Mount ("S" Suffix)



PC Board Pattern (Top View)



Tape and Reel Packaging for 8 Pin Surface Mount Package



Dimensions
 mm
 (inches)



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Specification: DS-LCA210-R7.0
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6/25/02