

S102S12 Series S202S12 Series

*Non-zero cross type is also available. (\$102\$11 Series/\$202\$11 Series)

I_T(rms)≤8A, Built-in snubber circuit Zero Cross type SIP 4pin Triac output SSR

■ Description

S102S12 Series and S202S12 Series Solid State Relays (SSR) are an integration of an infrared emitting diode (IRED), a Phototriac Detector and a main output Triac. These devices are ideally suited for controlling high voltage AC loads with solid state reliability while providing 4.0kV isolation (V_{iso}(rms)) from input to output.

■ Features

- 1. Output current, I_T(rms)≤8.0A
- 2. Zero crossing functionary (V_{OX}: MAX. 35V)
- 3. 4 pin SIP package
- 4. High repetitive peak off-state voltage

(V_{DRM}: 600V, **S202S12 Series**)

(V_{DRM}: 400V, S102S12 Series)

- 5. Built-in snubber circuit
- 6. High isolation voltage between input and output (V_{iso}(rms) : 4.0kV)
- 7. Lead-free terminal components are also available (see Model Line-up section in this datasheet)
- 8. Screw hole for heat sink

■ Agency approvals/Compliance

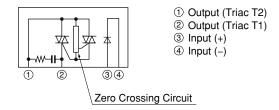
- 1. Recognized by UL508, file No. E94758 (as models No. **\$102\$12**/**\$202\$12**)
- Approved by CSA 22.2 No.14, file No. LR63705 (as models No. S102S12/S202S12)
- 3. Package resin: UL flammability grade (94V-0)

■ Applications

- Isolated interface between high voltage AC devices and lower voltage DC control circuitry.
- 2. Switching motors, fans, heaters, solenoids, and valves.
- 3. Power control in applications such as lighting and temperature control equipment.

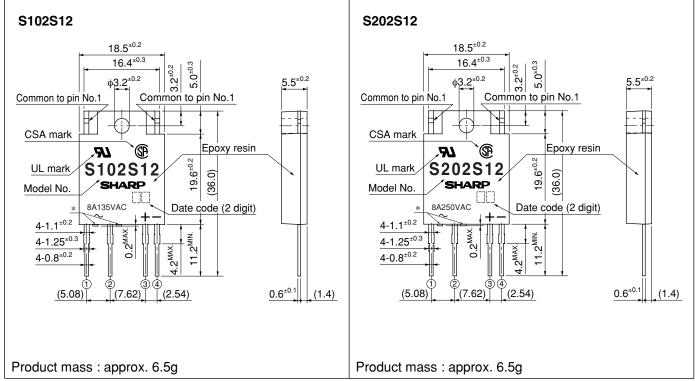


■ Internal Connection Diagram



■ Outline Dimensions

(Unit: mm)



- *: Do not allow external connection.
- (): Typical dimensions



Date code (2 digit)

1st digit				2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Country of origin Japan

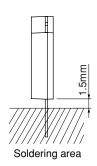
Rank mark

There is no rank mark indicator and currently there are no rank offered for this device.



Absolute Maximum Ratings

$\blacksquare \text{ Absolute waximum Ratings} \qquad \qquad (T_a=25^{\circ}\text{C})$							
	Parameter	Symbol	Rating	Unit			
T .	Forward current	I_F	50 *3	mA			
Input	Reverse voltage		V_R	6	V		
	RMS ON-state current	I _T (rms)	8 *3	A			
	Peak one cycle surge of	current	I _{surge}	80 *4	A		
	Repetitive	S102S12		400	•		
0	peak OFF-state voltage	S202S12	VDRM	600	V		
Output	Non-Repetitive	S102S12	17	400	T 7		
	peak OFF-state voltage	S202S12	VDSM	600	V		
	Critical rate of rise of ON	dI _T /dt	50	A/μs			
	Operating frequency	f	45 to 65	Hz			
*1 Isolatio	n voltage	V _{iso} (rms)	4.0	kV			
Operati	ng temperature	Topr	-20 to +80	°C			
Storage	temperature	T_{stg}	-30 to +100	°C			
*2Soldering temperature			T_{sol}	260	°C		



■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

							`	- /	
Parameter			Symbol	Conditions		TYP.	MAX.	Unit	
T .	Forward voltage		$V_{\rm F}$	I _F =20mA	_	1.2	1.4	V	
Input	Reverse current		I_R	V _R =3V		_	100	μΑ	
	ON-state voltage		V _T (rms)	I _T (rms)=2A, Resistance load, I _F =20mA		_	1.5	V	
	Minimum Operating current	S102S12	I (mmag)	V _{OUT} (rms)=120V			50	A	
		S202S12	I _{OP} (rms)	V _{OUT} (rms)=240V	_	_	50	mA	
Output	0	S102S12	T ()	V _{OUT} (rms)=120V	-	_	5	4	
	Open circuit leak current	S202S12	I _{leak} (rms)	V _{OUT} (rms)=240V	-	_	10	mA	
	Critical rate of rise of OFF-state voltage		dV/dt	$V_D=2/3 \bullet V_{DRM}$	30	_	_	V/µs	
	Critical rate of rise of OFF-state voltage at commutaion		(dV/dt)c	$T_j=125^{\circ}C, V_D=2/3 \bullet V_{DRM}, dI_T/dt=-4.0A/ms$	5	_	-	V/µs	
	Minimum trigger current		I_{FT}	$V_D=6V, R_L=30\Omega$	1	_	8	mA	
	Zero cross voltage		Vox	I _F =8mA	_	_	35	V	
	Isolation resistance		R _{ISO}	DC500V, 40 to 60%RH	10^{10}	-	-	Ω	
	Turn-on time	S102S12	$V_D(rms)=100V$, AC60Hz			9.3			
Transfer				I _T (rms)=2A, Resistance load, I _F =20mA	_	_	9.3	ms	
charac-		S202S12	612 t _{on}	V _D (rms)=200V, AC60Hz			0.2		
teristics		3202312		$I_T(rms)=2A$, Resistance load, $I_F=20mA$	-	_	9.3		
	Turn-off time	S102S12		$V_D(rms)=100V$, AC60Hz		_	9.3	ms	
				I _T (rms)=2A, Resistance load, I _F =20mA					
		6000610	S202S12	V _D (rms)=200V, AC60Hz			9.3		
		3202312		$I_T(rms)=2A$, Resistance load, $I_F=20mA$	_ -		9.3		
Thermal resistance		R _{th} (j-c)	Between junction and case	ı	4.0	_	°C/W		
		R _{th} (j-a)	Between junction and ambient	1	40	_	C/W		

^{*1 40} to 60%RH, AC for 1minute, f=60Hz *2 For 10s

^{*3} Refer to Fig.1, Fig.2 *4 f=50Hz sine wave, T_j=25°C start



■ Model Line-up (1) (Lead-free terminal components)

Shipping Package	Case 200pcs/case	V _{DRM} [V]	$I_{FT}[mA]$ $(V_D=6V,$ $R_L=30\Omega)$	
Model No.	S102S12F	400	MAX.8	
Wiodel No.	S202S12F	600	MAX.8	

■ Model Line-up (2) (Lead solder plating components)

Shipping Package	Case 200pcs/case	V _{DRM} [V]	$I_{FT}[mA] \\ (V_D=6V, \\ R_L=30\Omega)$	
Model No.	S102S12	400	MAX.8	
wiodel No.	S202S12	600	MAX.8	

Please contact a local SHARP sales representative to see the actual status of the production.



Fig.1 Forward Current vs. Ambient Temperature

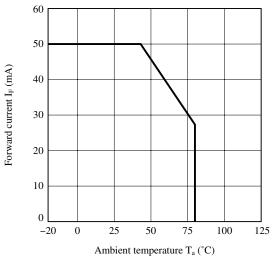


Fig.3 RMS ON-state Current vs. Case Temperature

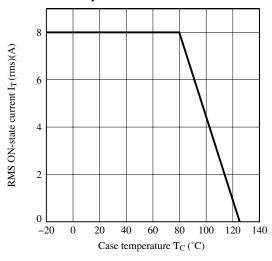


Fig.5 Surge Current vs. Power-on Cycle

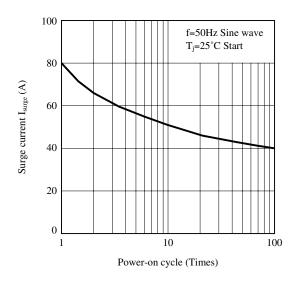


Fig.2 RMS ON-state Current vs. Ambient Temperature

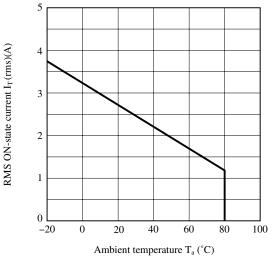


Fig.4 Forward Current vs. Forward Voltage

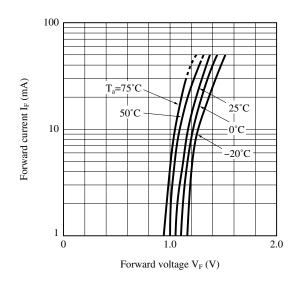
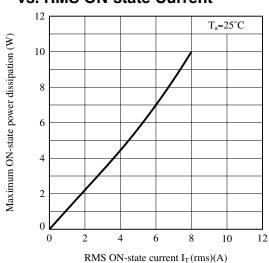


Fig.6 Maximum ON-state Power Dissipation vs. RMS ON-state Current



Sheet No.: D4-A02901EN



Fig.7 Minimum Trigger Current vs.
Ambient Temperature

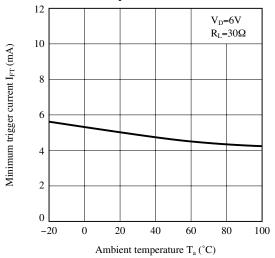
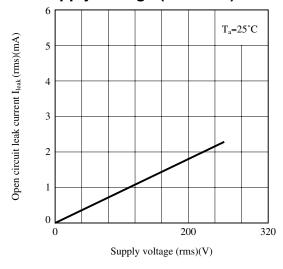
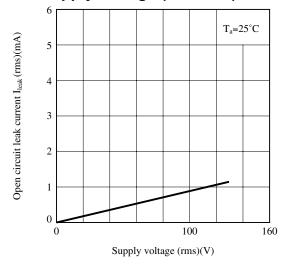


Fig.8-b Open Circuit Leak Current vs. Supply Voltage (S202S12)



Remarks: Please be aware that all data in the graph are just for reference.

Fig.8-a Open Circuit Leak Current vs. Supply Voltage (S102S12)





■ Design Considerations

Recommended Operating Conditions

Parameter			Symbol	Conditions	MIN.	MAX.	Unit
T4	Input signal current at ON state		$I_F(ON)$	1	16	24	mA
Input	Input signal current at	ut signal current at OFF state		Т	0	0.1	mA
Load su	Load aumnly voltage	S102S12	V_{i} (rmc)	_	80	120	V
	Load supply voltage	S202S12			80	240	
Output	t Load supply current		$I_{OUT}(rms) \\$	-	0.1	I _T (rms)×80%(*)	mA
	Frequency		f	_	47	63	Hz
Operating temperature		T_{opr}	ŀ	-20	80	°C	

^(*) See Fig.2 about derating curve (I_T(rms) vs. ambient temperature).

Design guide

In order for the SSR to turn off, the triggering current (I_F) must be 0.1mA or less.

For over voltage protection, a Varistor may be used.

A varistor used for the above mentioned scenarios should be located as close to the main output triac as possible.

Particular attention needs to be paid when utilizing SSRs that incorporate zero crossing circuitry.

If the phase difference between the voltage and the current at the output pins is large enough, zero crossing type SSRs cannot be used. The result, if zero crossing SSRs are used under this condition, is that the SSR may not turn on and off irregardless of the input current. In this case, only a non zero cross type SSR should be used in combination with the above mentioned snubber circuit selection process.

The load current should be within the bounds of derating curve. (Refer to Fig.2) Also, please use the optional heat sink when necessary.

In case the optional heat sink is used and the isolation voltage between the device and the optional heat sink is needed, please locate the insulation sheet between the device and the heat sink.

When the optional heat sink is equipped, please set up the M3 screw-fastening torque at 0.3 to 0.5N· m. In order to dissipate the heat generated from the inside of device effectively, please follow the below suggestions.

- (a) Make sure there are no warps or bumps on the heat sink, insulation sheet and device surface.
- (b) Make sure there are no metal dusts or burrs attached onto the heat sink, insulation sheet and device surface.
- (c) Make sure silicone grease is evenly spread out on the heat sink, insulation sheet and device surface.

Silicone grease to be used is as follows;

- 1) There is no aged deterioration within the operating temperature ranges.
- 2) Base oil of grease is hardly separated and is hardly permeated in the device.
- 3) Even if base oil is separated and permeated in the device, it should not degrade the function of a device.

Recommended grease: G-746 (Shin-Etsu Chemical Co., Ltd.)

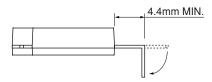
: G-747 (Shin-Etsu Chemical Co., Ltd.)

: SC102 (Dow Corning Toray Silicone Co., Ltd.)



In case the optional heat sink is screwed up, please solder after screwed.

In case of the lead frame bending, please keep the following minimum distance and avoid any mechanical stress between the base of terminals and the molding resin.



Some of AC electromagnetic counters or solenoids have built-in rectifier such as the diode.

In this case, please use the device carefully since the load current waveform becomes similar with rectangular waveform and this results may not make a device turn off.

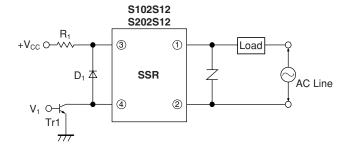
Degradation

In general, the emission of the IRED used in SSR will degrade over time.

In the case where long term operation and / or constant extreme temperature fluctuations will be applied to the devices, please allow for a worst case scenario of 50% degradation over 5years.

Therefore in order to maintain proper operation, a design implementing these SSRs should provide at least twice the minimum required triggering current from initial operation.

Standard Circuit



[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

Soldering Method

Flow Soldering (No solder bathing)

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please solder within one time.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below. Immersion time should be 3minutes or less.

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol.

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.



■ Package specification

Package materials

Packing case: Corrugated cardboard
Partition: Corrugated cardboard
Pad: Corrugated cardboard
Cushioning material: Polyethylene

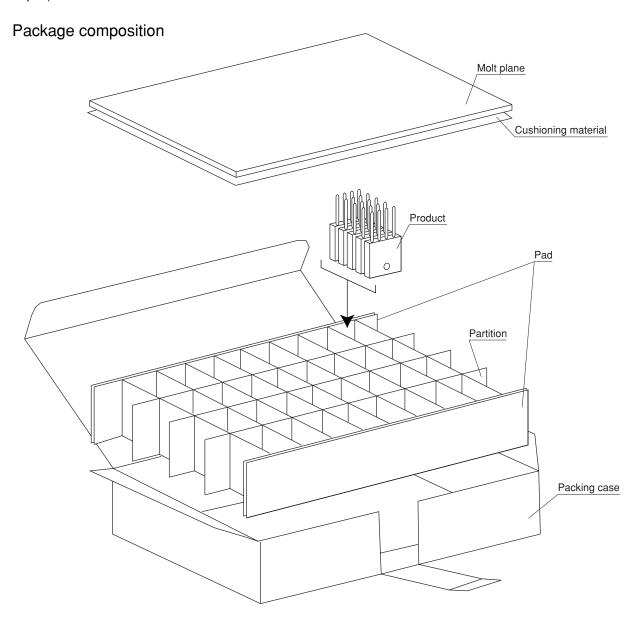
Molt plane: Urethane

Package method

The product should be located after the packing case is partitioned and protected inside by 4 pads.

Each partition should have 5 products with the lead upward.

Cushioning material and molt plane should be located after all products are settled (1 packing contains 200 pcs).





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