



Parameter	Rating	Units
Blocking Voltage	250	$V_P$
Load Current	0.7	$A_{DC}/A_{rms}$
On-Resistance	1.4	$\Omega$

## Features

- Handle Load Currents Up to 0.7  $A_{DC}/A_{rms}$
- 2500V<sub>rms</sub> Input/Output Isolation
- Power SIP Package
- High Reliability
- No Moving Parts
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

## Applications

- Industrial Controls
- Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
  - Meters (Watt-Hour, Water, Gas)
- IC Equipment
- Home Appliances

## Description

Clare and IXYS have combined to bring OptoMOS® technology, reliability, and compact size to a new family of high-power, solid state relays. As part of that family, the CPC1926 is a single-pole, normally open (1-Form-A) solid state relay. The CPC1926 employs optically coupled MOSFET technology to provide 2500V<sub>rms</sub> of input to output isolation. The efficient MOSFET switches and photovoltaic die use Clare's patented OptoMOS architecture while the output is controlled by a highly efficient GaAIAs infrared LED. The combination of low on-resistance and high load-current handling capabilities makes the relay suitable for a variety of high-performance switching applications.

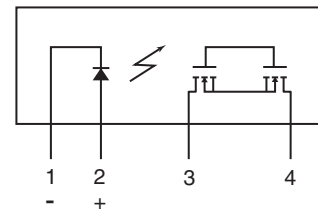
## Approvals

- UL 508 Certified Component: File E69938

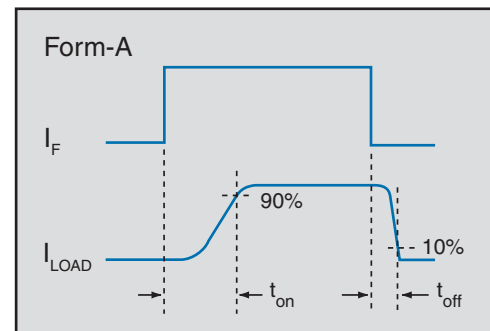
## Ordering Information

Part #	Description
CPC1926Y	4-Pin (8-Pin Body) Power SIP Package (25 per tube)

## Pin Configuration



## Switching Characteristics of Normally Open Devices



### Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	250	V <sub>P</sub>
Reverse Input Voltage	5	V
Input control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation <sup>1</sup>	150	mW
Isolation voltage Input to Output	2500	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

<sup>1</sup> Derate linearly 3.33 mW / °C

*Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.*

### Electrical Characteristics @ 25°C

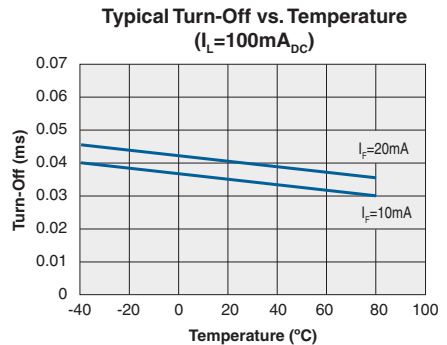
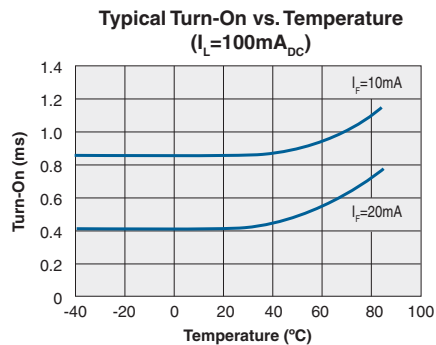
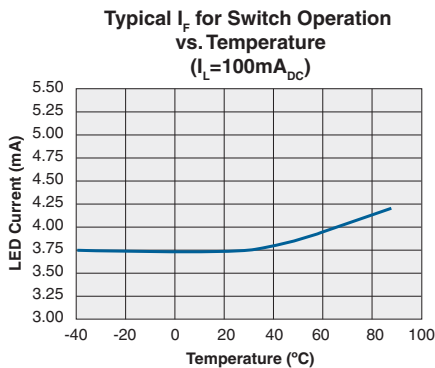
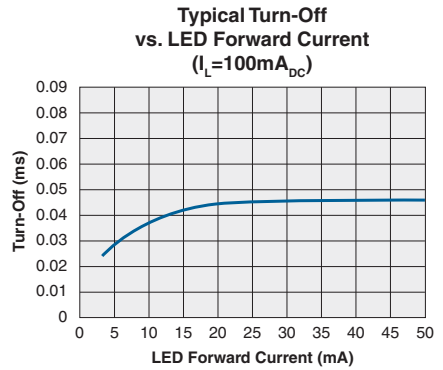
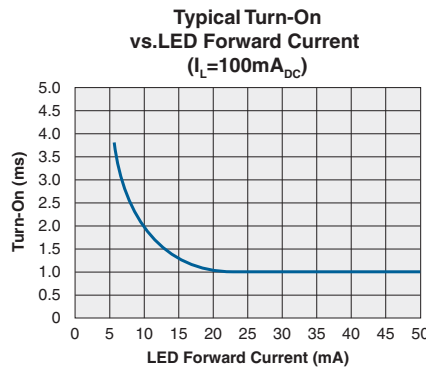
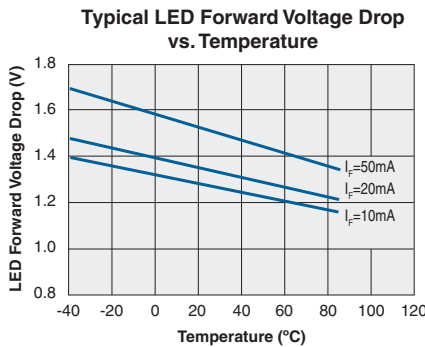
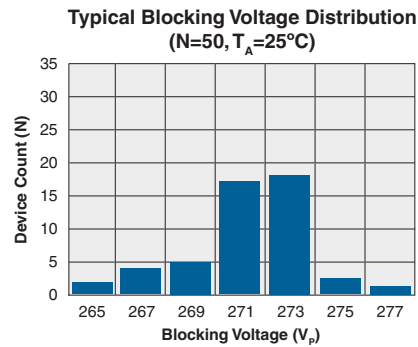
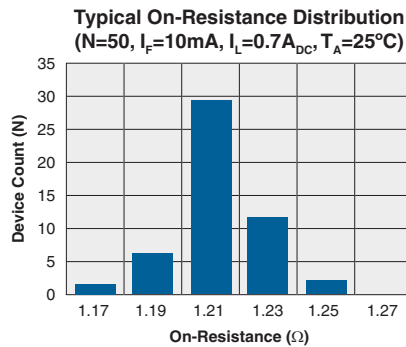
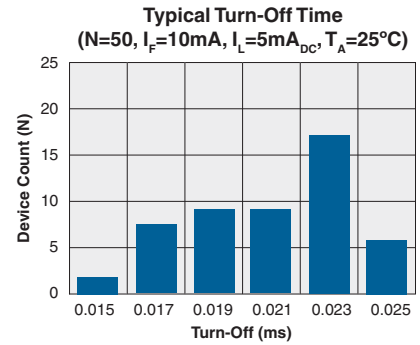
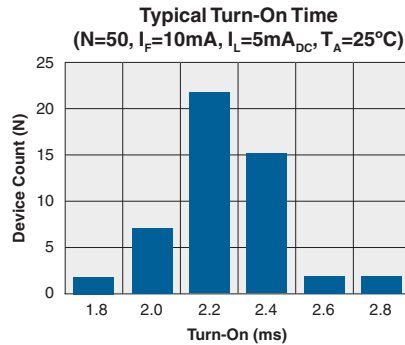
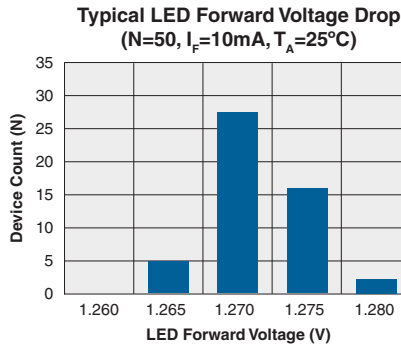
Parameter	Conditions	Symbol	Min	Typ	Max	Units
<b>Output Characteristics</b>						
Load Current, Continuous	Free air	I <sub>L</sub>	-	-	0.7	A <sub>DC</sub> /A <sub>rms</sub>
Peak Load Current	t ≤ 10ms	I <sub>LPK</sub>	-	-	3	A <sub>P</sub>
On-Resistance <sup>1</sup>	I <sub>L</sub> =100mA	R <sub>ON</sub>	-	1.2	1.4	Ω
Off-State Leakage Current	V <sub>L</sub> =250V <sub>P</sub>	I <sub>LEAK</sub>	-	-	1	μA
<b>Switching Speeds</b>						
Turn-On	I <sub>F</sub> =10mA, V <sub>L</sub> =10V	t <sub>on</sub>	-	2.3	10	ms
Turn-Off		t <sub>off</sub>	-	0.022	10	
Output Capacitance	V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	60	-	pF
<b>Input Characteristics</b>						
Input Control Current	I <sub>L</sub> =120mA	I <sub>F</sub>	-	3.75	10	mA
Input Dropout Current	-	I <sub>F</sub>	0.6	-	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>R</sub>	-	-	10	μA
<b>Input/Output Characteristics</b>						
Capacitance Input/Output	f=1MHz	C <sub>IO</sub>	-	2	-	pF

<sup>1</sup> Measurement taken within 1 second of on-time.

### Thermal Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Thermal Resistance (junction to case)	-	R <sub>θJC</sub>	-	1.5	-	°C/W

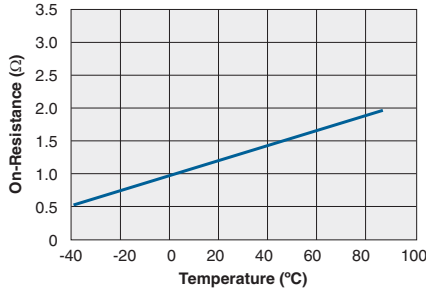
**PERFORMANCE DATA\***



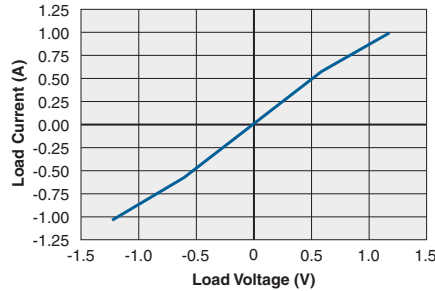
\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

**PERFORMANCE DATA \***

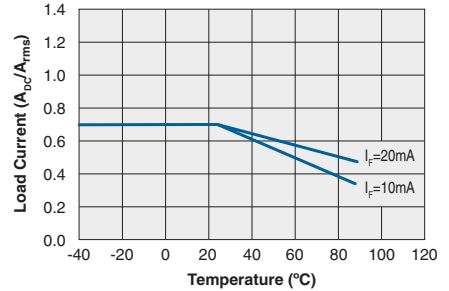
**Typical On-Resistance vs. Temperature**  
( $I_F=10\text{mA}$ ,  $I_L=100\text{mA}_{\text{DC}}$ )



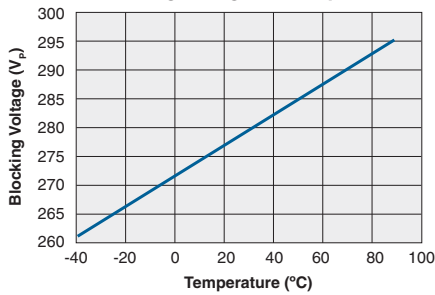
**Typical Load Current vs. Load Voltage**  
( $I_F=10\text{mA}$ ,  $T_A=25^\circ\text{C}$ )



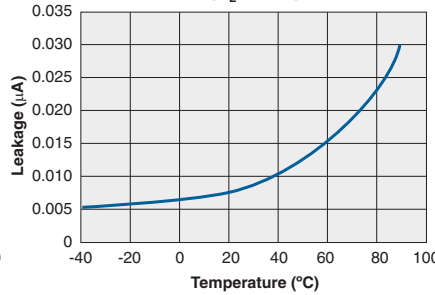
**Typical Load Current vs. Temperature**



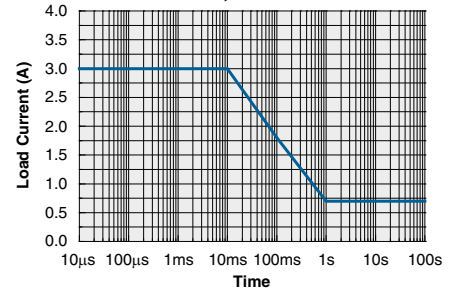
**Blocking Voltage vs. Temperature**



**Typical Leakage vs. Temperature**  
Measured across Pins 3 & 4  
( $V_L=250\text{V}$ )



**Energy Rating Curve**  
Free Air, No Heat Sink



\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

## Manufacturing Information

### Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. Clare classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC1926Y	MSL 1

### ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

### Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
CPC1926Y	245°C for 30 seconds

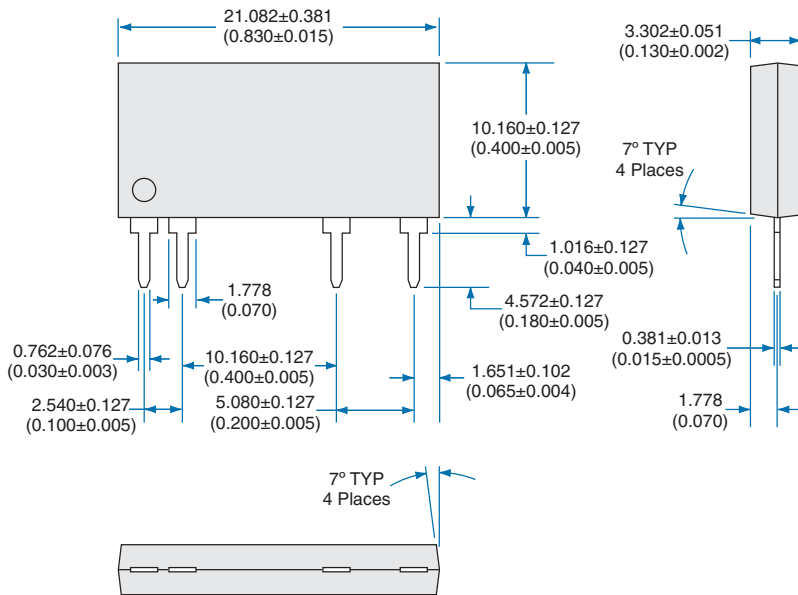
### Board Wash

Clare recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since Clare employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

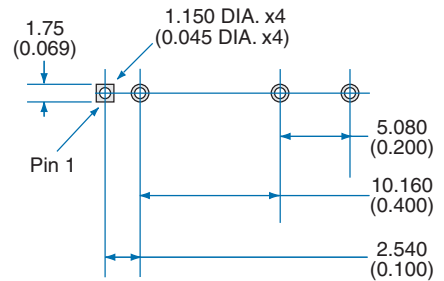


**MECHANICAL DIMENSIONS**

**CPC1926Y**



**PCB Hole Pattern**



Dimensions  
mm  
(inches)

**For additional information please visit our website at: [www.clare.com](http://www.clare.com)**

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Specification: DS-CPC1926-R05  
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