

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

Notice

1. All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice. Before purchasing or using any Renesas Electronics products listed herein, please confirm the latest product information with a Renesas Electronics sales office. Also, please pay regular and careful attention to additional and different information to be disclosed by Renesas Electronics such as that disclosed through our website.
2. Renesas Electronics does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of Renesas Electronics products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of Renesas Electronics or others.
3. You should not alter, modify, copy, or otherwise misappropriate any Renesas Electronics product, whether in whole or in part.
4. Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. Renesas Electronics assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
5. When exporting the products or technology described in this document, you should comply with the applicable export control laws and regulations and follow the procedures required by such laws and regulations. You should not use Renesas Electronics products or the technology described in this document for any purpose relating to military applications or use by the military, including but not limited to the development of weapons of mass destruction. Renesas Electronics products and technology may not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable domestic or foreign laws or regulations.
6. Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
7. Renesas Electronics products are classified according to the following three quality grades: “Standard”, “High Quality”, and “Specific”. The recommended applications for each Renesas Electronics product depends on the product’s quality grade, as indicated below. You must check the quality grade of each Renesas Electronics product before using it in a particular application. You may not use any Renesas Electronics product for any application categorized as “Specific” without the prior written consent of Renesas Electronics. Further, you may not use any Renesas Electronics product for any application for which it is not intended without the prior written consent of Renesas Electronics. Renesas Electronics shall not be in any way liable for any damages or losses incurred by you or third parties arising from the use of any Renesas Electronics product for an application categorized as “Specific” or for which the product is not intended where you have failed to obtain the prior written consent of Renesas Electronics. The quality grade of each Renesas Electronics product is “Standard” unless otherwise expressly specified in a Renesas Electronics data sheets or data books, etc.
 - “Standard”: Computers; office equipment; communications equipment; test and measurement equipment; audio and visual equipment; home electronic appliances; machine tools; personal electronic equipment; and industrial robots.
 - “High Quality”: Transportation equipment (automobiles, trains, ships, etc.); traffic control systems; anti-disaster systems; anti-crime systems; safety equipment; and medical equipment not specifically designed for life support.
 - “Specific”: Aircraft; aerospace equipment; submersible repeaters; nuclear reactor control systems; medical equipment or systems for life support (e.g. artificial life support devices or systems), surgical implantations, or healthcare intervention (e.g. excision, etc.), and any other applications or purposes that pose a direct threat to human life.
8. You should use the Renesas Electronics products described in this document within the range specified by Renesas Electronics, especially with respect to the maximum rating, operating supply voltage range, movement power voltage range, heat radiation characteristics, installation and other product characteristics. Renesas Electronics shall have no liability for malfunctions or damages arising out of the use of Renesas Electronics products beyond such specified ranges.
9. Although Renesas Electronics endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Further, Renesas Electronics products are not subject to radiation resistance design. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a Renesas Electronics product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures. Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
10. Please contact a Renesas Electronics sales office for details as to environmental matters such as the environmental compatibility of each Renesas Electronics product. Please use Renesas Electronics products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Renesas Electronics assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
11. This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of Renesas Electronics.
12. Please contact a Renesas Electronics sales office if you have any questions regarding the information contained in this document or Renesas Electronics products, or if you have any other inquiries.

(Note 1) “Renesas Electronics” as used in this document means Renesas Electronics Corporation and also includes its majority-owned subsidiaries.

(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.



User's Manual

IE-789801-NS-EM1

Emulation Board

Target Devices μ PD789800 Subseries

Document No. U13390EJ2V0UM00 (2nd edition)
Date Published May 2002 N CP(K)

© NEC Corporation 1999
Printed in Japan

[MEMO]

Windows is either a registered trademark or a trademark of Microsoft Corporation in the United States and/or other countries.

PC/AT is a trademark of International Business Machines Corporation.

- **The information in this document is current as of April, 2002. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC's data sheets or data books, etc., for the most up-to-date specifications of NEC semiconductor products. Not all products and/or types are available in every country. Please check with an NEC sales representative for availability and additional information.**

- No part of this document may be copied or reproduced in any form or by any means without prior written consent of NEC. NEC assumes no responsibility for any errors that may appear in this document.

- NEC does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from the use of NEC semiconductor products listed in this document or any other liability arising from the use of such products. No license, express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC or others.

- Descriptions of circuits, software and other related information in this document are provided for illustrative purposes in semiconductor product operation and application examples. The incorporation of these circuits, software and information in the design of customer's equipment shall be done under the full responsibility of customer. NEC assumes no responsibility for any losses incurred by customers or third parties arising from the use of these circuits, software and information.

- While NEC endeavours to enhance the quality, reliability and safety of NEC semiconductor products, customers agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize risks of damage to property or injury (including death) to persons arising from defects in NEC semiconductor products, customers must incorporate sufficient safety measures in their design, such as redundancy, fire-containment, and anti-failure features.

- NEC semiconductor products are classified into the following three quality grades:

"Standard", "Special" and "Specific". The "Specific" quality grade applies only to semiconductor products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of a semiconductor product depend on its quality grade, as indicated below. Customers must check the quality grade of each semiconductor product before using it in a particular application.

"Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

"Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

"Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC semiconductor products is "Standard" unless otherwise expressly specified in NEC's data sheets or data books, etc. If customers wish to use NEC semiconductor products in applications not intended by NEC, they must contact an NEC sales representative in advance to determine NEC's willingness to support a given application.

(Note)

(1) "NEC" as used in this statement means NEC Corporation and also includes its majority-owned subsidiaries.

(2) "NEC semiconductor products" means any semiconductor product developed or manufactured by or for NEC (as defined above).

M8E 00.4

Regional Information

Some information contained in this document may vary from country to country. Before using any NEC product in your application, please contact the NEC office in your country to obtain a list of authorized representatives and distributors. They will verify:

- Device availability
- Ordering information
- Product release schedule
- Availability of related technical literature
- Development environment specifications (for example, specifications for third-party tools and components, host computers, power plugs, AC supply voltages, and so forth)
- Network requirements

In addition, trademarks, registered trademarks, export restrictions, and other legal issues may also vary from country to country.

NEC Electronics Inc. (U.S.)

Santa Clara, California
Tel: 408-588-6000
800-366-9782
Fax: 408-588-6130
800-729-9288

NEC do Brasil S.A.

Electron Devices Division
Guarulhos-SP, Brasil
Tel: 11-6462-6810
Fax: 11-6462-6829

NEC Electronics (Europe) GmbH

Duesseldorf, Germany
Tel: 0211-65 03 01
Fax: 0211-65 03 327

• Sucursal en España

Madrid, Spain
Tel: 091-504 27 87
Fax: 091-504 28 60

• Succursale Française

Vélizy-Villacoublay, France
Tel: 01-30-67 58 00
Fax: 01-30-67 58 99

• Filiale Italiana

Milano, Italy
Tel: 02-66 75 41
Fax: 02-66 75 42 99

• Branch The Netherlands

Eindhoven, The Netherlands
Tel: 040-244 58 45
Fax: 040-244 45 80

• Branch Sweden

Taeby, Sweden
Tel: 08-63 80 820
Fax: 08-63 80 388

• United Kingdom Branch

Milton Keynes, UK
Tel: 01908-691-133
Fax: 01908-670-290

NEC Electronics Hong Kong Ltd.

Hong Kong
Tel: 2886-9318
Fax: 2886-9022/9044

NEC Electronics Hong Kong Ltd.

Seoul Branch
Seoul, Korea
Tel: 02-528-0303
Fax: 02-528-4411

NEC Electronics Shanghai, Ltd.

Shanghai, P.R. China
Tel: 021-6841-1138
Fax: 021-6841-1137

NEC Electronics Taiwan Ltd.

Taipei, Taiwan
Tel: 02-2719-2377
Fax: 02-2719-5951

NEC Electronics Singapore Pte. Ltd.

Novena Square, Singapore
Tel: 253-8311
Fax: 250-3583

Major Revisions in This Edition

Pages	Description
Throughout	<ul style="list-style-type: none"> • Addition of IE-78K0S-NS-A as supported main board • Addition of NP-H44GB-TQ as emulation probe • Addition of TGB-044SAP as conversion socket/adaptor • Deletion of NP-42CU from supported emulation probes
pp.12, 13	<p>Figure 1-1 System Configuration</p> <ul style="list-style-type: none"> • Change of supply medium for debugger ID78K0S-NS to CD-ROM • Change of Notes 1 to 3.
p.15	Modification of a part of Table 1-1 Basic Specifications
pp.20 to 22	<p>3.2.1 Overview of clock settings</p> <ul style="list-style-type: none"> • Change from “internal clock” to “clock oscillator” • Addition of V_{SS} pin in (a) Clock oscillator in Figure 3-2 External Circuit Used as System Clock Oscillator • Change from “resonator” to “clock oscillator or external clock” on target system in Figure 3-3 When Using Clock (6 MHz) That Is Already Mounted on Emulation Board and Figure 3-4 When Using User-Mounted Clock • Change from “clock generator” to “external clock” in Figure 3-5 When Using an External Clock
p.29	Addition of 3.3 Setting Power Supply Voltage of Target Interface
p.30	Addition of 3.4 External Trigger
p.31	<p>3.5 Switch and Jumper Settings</p> <ul style="list-style-type: none"> • Addition of (2) Switch and jumper settings on IE-78K0S-NS-A
p.36	Addition of APPENDIX B CAUTIONS ON DESIGNING TARGET SYSTEM

The mark ★ shows major revised points.

INTRODUCTION

Product Overview

The IE-789801-NS-EM1 is designed to be used with the IE-78K0S-NS or IE-78K0S-NS-A to debug the following target devices that belong to the 78K/0S Series of 8-bit single-chip microcontrollers.

- μ PD789800 Subseries: μ PD789800, 78F9801

Target Readers

This manual is intended for engineers who will use the IE-789801-NS-EM1 with the IE-78K0S-NS or IE-78K0S-NS-A to perform system debugging.

Engineers who use this manual are expected to be thoroughly familiar with the target device's functions and usage methods and to be knowledgeable about debugging.

Organization

When using the IE-789801-NS-EM1, refer to not only this manual (supplied with the IE-789801-NS-EM1) but also the manual that is supplied with the IE-78K0S-NS or IE78K0S-NS-A.

IE-78K0S-NS
User's Manual

- Basic specifications
- System configuration
- External interface functions

IE-789801-NS-EM1
User's Manual

- General
- Part names
- Installation
- Differences between target devices and target interface circuits

IE-78K0S-NS-A
User's Manual

- Basic specifications
- System configuration
- External interface functions

Purpose

This manual is intended to give users an understanding of the various debugging functions that can be performed when using the IE-789801-NS-EM1.

Terminology

The meanings of certain terms used in this manual are listed below.

Term	Meaning
Emulation device	This is a general term that refers to the device in the emulator that is used to emulate the target device. It includes the emulation CPU.
Emulation CPU	This is the CPU block in the emulator that is used to execute user-generated programs.
Target device	This is the device (real chip) that is the target for emulation.
Target system	This includes the target program and the hardware provided by the user. When defined narrowly, it includes only the hardware.
IE system	This refers to the combination of the IE-78K0S-NS or IE78K0S-NS-A and the IE-789801-NS-EM1.

Conventions

This document is described in accordance with the following notations.

Data significance: Higher digits on the left and lower digits on the right

Note: Footnote for item marked with **Note** in the text

Caution: Information requiring particular attention

Remark: Supplementary information

Related Documents

The related documents (user's manuals) indicated in this publication may include preliminary versions. However, preliminary versions are not marked as such.

Document Name	Document No.
IE-78K0S-NS	U13549E
IE-78K0S-NS-A	U15207E
IE-789801-NS-EM1	This manual
ID78K0-NS, ID78K0S-NS Integrated Debugger Ver. 2.20 or Later Operation (Windows™ Based)	U14910E
μPD789800 Subseries	U12978E

Caution The related documents listed above are subject to change without notice. Be sure to use the latest documents when designing.

CONTENTS

CHAPTER 1	GENERAL	11
1.1	System Configuration	12
1.2	Hardware Configuration	14
1.3	Basic Specifications	15
CHAPTER 2	PART NAMES	16
2.1	Parts of Main Unit	17
CHAPTER 3	INSTALLATION	18
3.1	Connection	19
3.2	Clock Settings	20
3.2.1	Overview of clock settings	20
3.2.2	System clock settings	23
★ 3.3	Setting Power Supply Voltage of Target Interface	29
★ 3.4	External Trigger	30
3.5	Switch and Jumper Settings	31
CHAPTER 4	DIFFERENCES BETWEEN TARGET DEVICES AND TARGET INTERFACE CIRCUITS	32
APPENDIX A	EMULATION PROBE PIN ASSIGNMENT TABLE	35
★ APPENDIX B	CAUTIONS ON DESIGNING TARGET SYSTEM	36

LIST OF FIGURES

Figure No.	Title	Page
1-1	System Configuration	12
1-2	Basic Hardware Configuration.....	14
2-1	IE-789801-NS-EM1 Part Names	17
3-1	Connection of Emulation Probe.....	19
3-2	External Circuits Used as System Clock Oscillator	20
3-3	When Using Clock (6 MHz) That Is Already Mounted on Emulation Board.....	21
3-4	When Using User-Mounted Clock	21
3-5	When Using an External Clock.....	22
3-6	Connections on Parts Board (When Using User-Mounted Clock).....	24
3-7	Crystal Oscillator (When Using User-Mounted Clock).....	26
3-8	Pin Alignment of Crystal Oscillator and Socket	26
3-9	Connection of TM1 and Power Supply Voltage of Target System.....	29
3-10	External Trigger Input Position	30
4-1	Equivalent Circuit 1 of Emulation Circuit	34
4-2	Equivalent Circuit 2 of Emulation Circuit	34
B-1	Distance Between In-Circuit Emulator and Conversion Socket.....	36
B-2	Connection Condition of Target System (NP-H44GB-TQ)	37

LIST OF TABLES

Table No.	Title	Page
1-1	Basic Specifications.....	15
3-1	System Clock Settings.....	23
3-2	Switch and Jumper Settings on IE-78K0S-NS.....	31
3-3	Switch and Jumper Settings on IE-78K0S-NS-A.....	31
A-1	NP-44GB, NP-44GB-TQ, and NP-H44GB-TQ Pin Assignments	35

CHAPTER 1 GENERAL

The IE-789801-NS-EM1 is a development tool for efficient debugging of hardware or software when using one of the following target devices that belong to the 78K/0S Series of 8-bit single-chip microcontrollers.

This chapter describes the IE-789801-NS-EM1's system configuration and basic specifications.

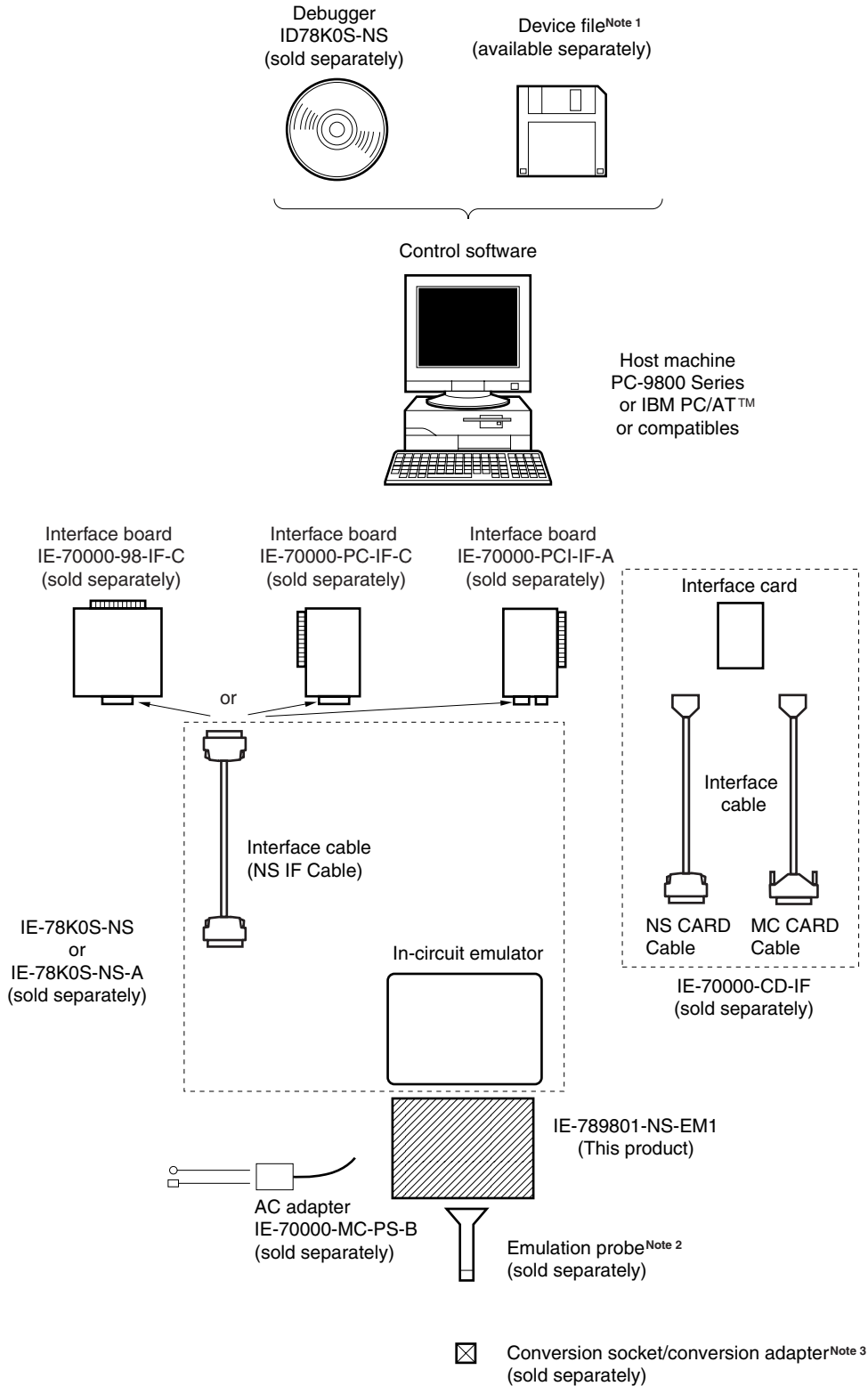
- Target device
 - μ PD789800 Subseries

1.1 System Configuration

Figure 1-1 illustrates the IE-789801-NS-EM1's system configuration.

★

Figure 1-1. System Configuration



Notes 1. The device file is as follows.

μ SxxxxDF789801

The device file can be downloaded from the NEC Electron Devices Web site
(URL: <http://www.ic.nec.co.jp/micro>).

2. The emulation probes are as follows.

NP-44GB: For 44-pin plastic QFP (GB type: EV-9200G-44 supported)

NP-44GB-TQ: For 44-pin plastic QFP (GB type: TGB-044)

NP-H44GB-TQ: Long type for 44-pin plastic QFP (GB type: TGB-044)

3. The conversion socket/conversion adapters are as follows.

EV-9200G-44: For 44-pin plastic QFP (GB-8ES type)

TGB-044SAP: For 44-pin plastic QFP (GB-8ES type)

Remark NP-44GB, NP-44GB-TQ, and NP-H44GB-TQ are products of Naito Densei Machida Mfg. Co., Ltd.

For further information, contact Naito Densei Machida Mfg. Co., Ltd. (TEL: +81-45-475-4191)

TGB-044SAP is a product of TOKYO ELETECH CORPORATION.

For further information, contact Daimaru Kogyo, Ltd.

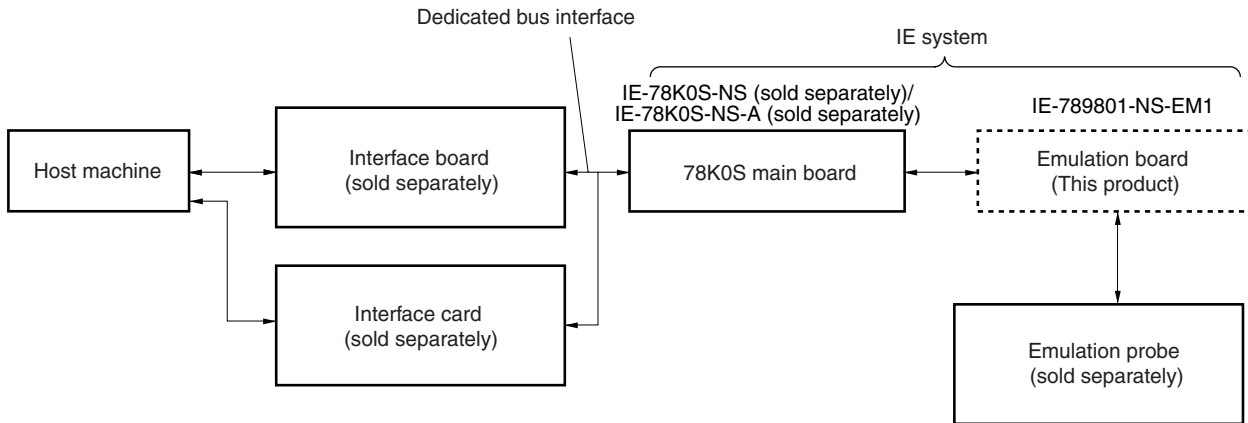
Tokyo Electronics Department (TEL: +81-3-3820-7112)

Osaka Electronics Department (TEL: +81-6-6244-6672)

1.2 Hardware Configuration

Figure 1-2 shows the IE-789801-NS-EM1's position in the basic hardware configuration.

Figure 1-2. Basic Hardware Configuration



1.3 Basic Specifications

The IE-789801-NS-EM1's basic specifications are listed in Table 1-1.

★

Table 1-1. Basic Specifications

Parameter	Description
Target device	μ PD789800 Subseries
System clock	6 MHz
Clock supply	External: Pulse input from the target system via an emulation probe Internal: Clock mounted on emulation board (6 MHz), or clock mounted on parts board by user
Target interface voltage	$V_{DD} = 4.0$ to 5.5 V (same as the target device) Operates with internal power supply (5 V) when not connected to the target system.

CHAPTER 2 PART NAMES

This chapter introduces the parts of the IE-789801-NS-EM1 main unit.

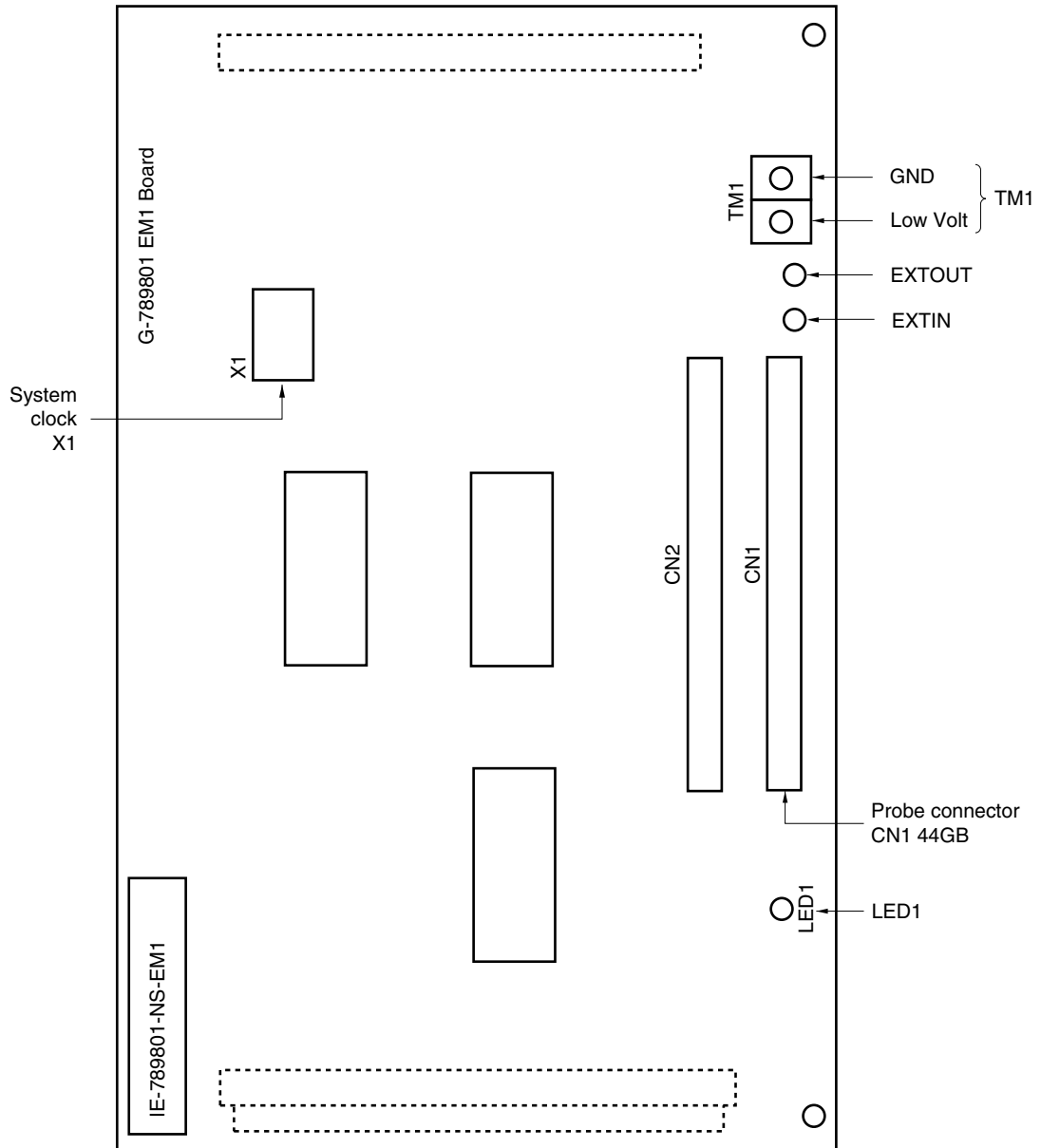
The packing box contains the emulation board (IE-789801-NS-EM1).

If there are any missing or damaged items, please contact an NEC sales representative.

Fill out and return the guarantee card that comes with the main unit.

2.1 Parts of Main Unit

Figure 2-1. IE-789801-NS-EM1 Part Names



CHAPTER 3 INSTALLATION

This chapter describes methods for connecting the IE-789801-NS-EM1 to the IE-78K0S-NS or IE-78K0S-NS-A, emulation probe, etc. Mode setting methods are also described.

Caution Connecting or removing components to or from the target system, or making switch or other setting changes must be carried out after the power supply to both the IE system and the target system has been switched OFF.

3.1 Connection

(1) Connection with IE-78K0S-NS or IE-78K0S-NS-A main unit

See the **IE-78K0S-NS User's Manual (U13549E)** for a description of how to connect the IE-789801-NS-EM1 to the IE-78K0S-NS or IE-78K0S-NS-A^{Note}.

Note When using the IE-78K0S-NS-A, refer to the **IE-78K0S-NS-A User's Manual (U15207E)**.

(2) Connection with emulation probe

See the **IE-78K0S-NS User's Manual (U13549E)** for a description of how to connect an emulation probe to the IE-789801-NS-EM1^{Note}.

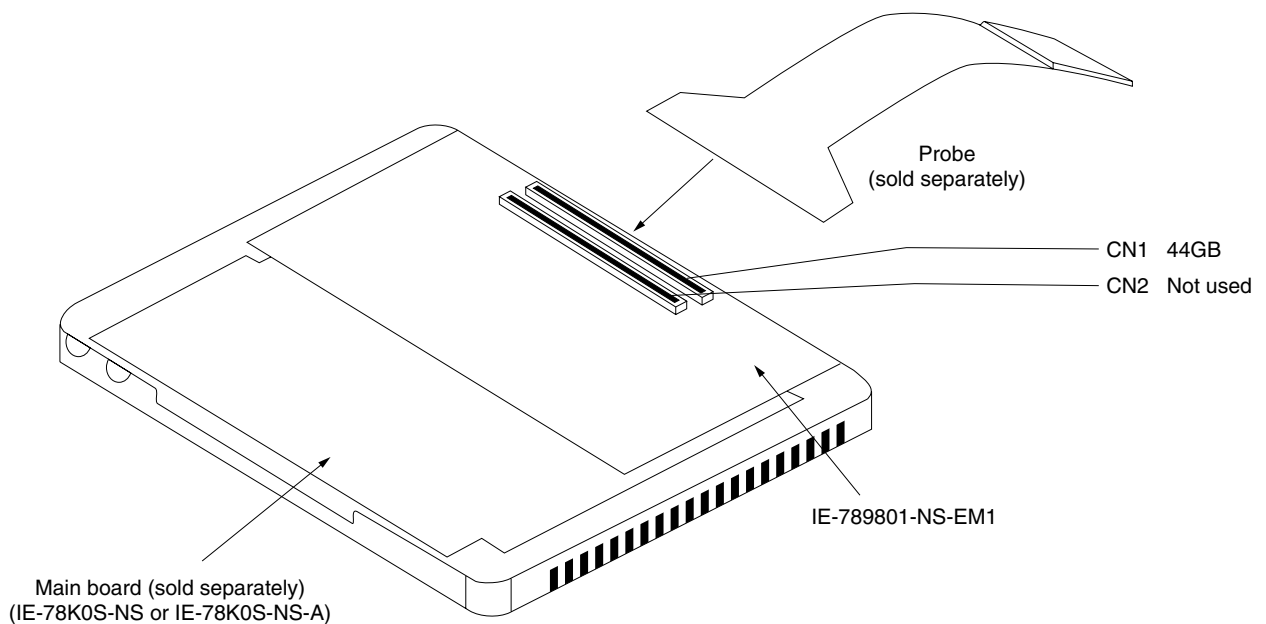
On this board, connect the emulation probe to CN1.

Note When using the IE-78K0S-NS-A, refer to the **IE-78K0S-NS-A User's Manual (U15207E)**.

Caution Incorrect connection may damage the IE system.

Be sure to read the emulation probe's user's manual for a detailed description of the connection method.

Figure 3-1. Connection of Emulation Probe



3.2 Clock Settings

3.2.1 Overview of clock settings

The system clocks to be used during debugging can be selected from (1) to (3) below.

(1) Clock that is already mounted on emulation board

(2) Clock that is mounted by user

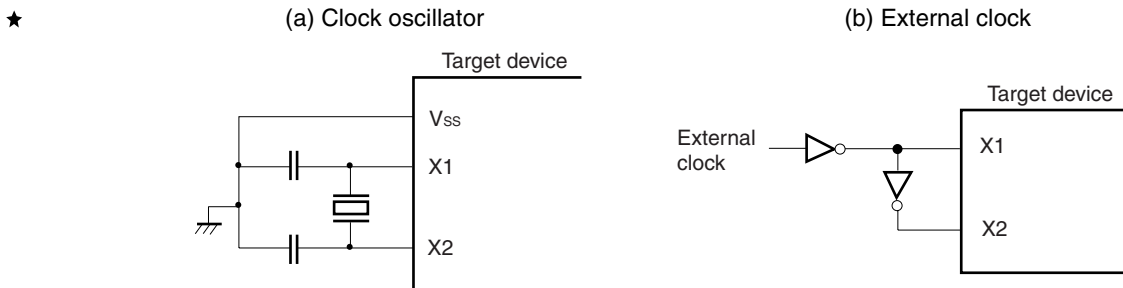
(3) Pulse input from target system

- ★ If the target system includes a clock oscillator, select either **(1) Clock that is already mounted on emulation board** or **(2) Clock that is mounted by user**. For a clock oscillator, the target device is connected to a resonator and the target device's internal oscillator is used. An example of the external circuit is shown in part (a) of Figure 3-2. During emulation, the oscillator that is mounted on the target system is not used. Instead, the clock that is mounted on the emulation board which is installed for the IE-78K0S-NS or IE-78K0S-NS-A is used.

If the target system includes an external clock, select **(3) Pulse input from target system**. For an external clock, a clock signal is supplied from outside of the target device and the target device's internal oscillator is not used. An example of the external circuit is shown in part (b) of Figure 3-2.

Caution The IE system will be hung-up if the system clock is not supplied normally. Moreover, be sure to input a rectangular wave as the clock from the target. However, it is not necessary to supply a clock to the X2 pin.

Figure 3-2. External Circuits Used as System Clock Oscillator

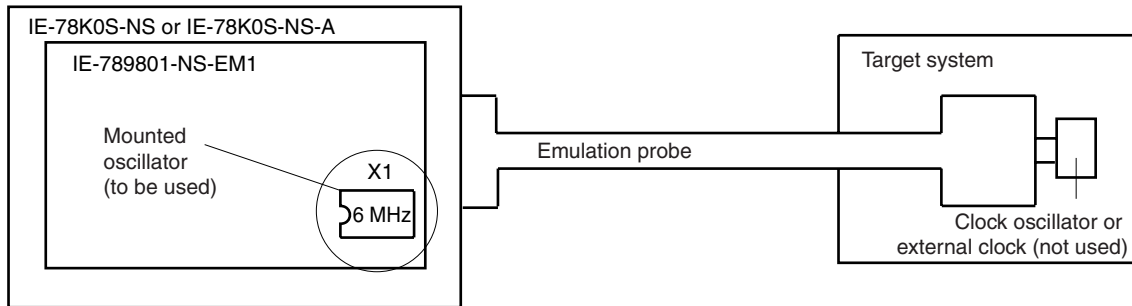


(1) Clock that is already mounted on emulation board

A crystal oscillator is already mounted in the socket of X1 on the emulation board. Its frequency is 6 MHz.

★

Figure 3-3. When Using Clock (6 MHz) That Is Already Mounted on Emulation Board



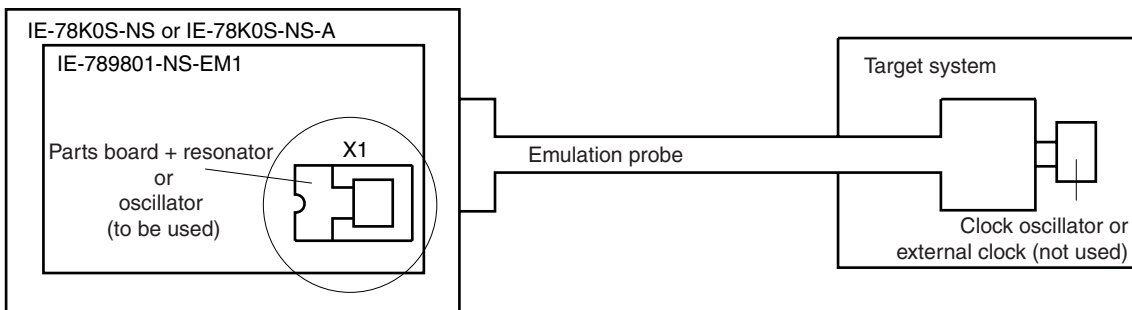
Remark The clock that is supplied by the IE-789801-NS-EM1's oscillator (encircled in the figure) is used.

(2) Clock that is mounted by user

The user is able to mount any clock supported by the set specifications on the IE-789801-NS-EM1. First mount the resonator on the parts board, then attach the parts board to the IE-789801-NS-EM1. This method is useful when using a different frequency from that of the pre-mounted clock.

★

Figure 3-4. When Using User-Mounted Clock

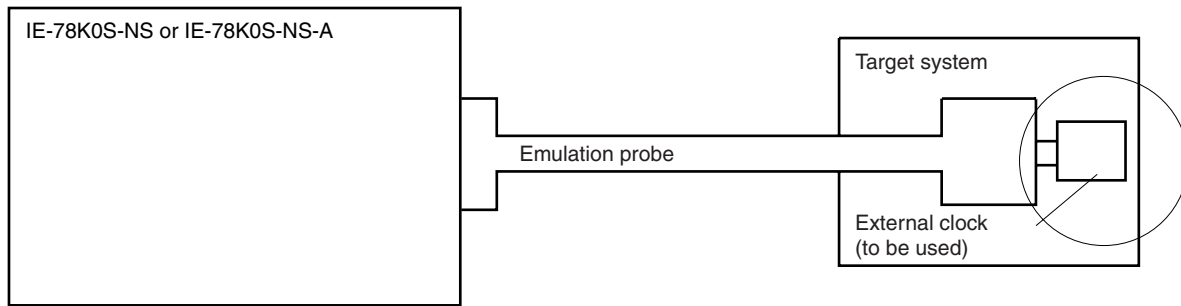


Remark The clock that is supplied by the parts board with the IE-789801-NS-EM1's resonator (encircled in the figure) or the oscillator is used.

(3) Pulse input from target system

An external clock connected to the target system can be used via an emulation probe.

★

Figure 3-5. When Using an External Clock

Remark The clock supplied by the target system's external clock (encircled in the figure) is used.

3.2.2 System clock settings

Table 3-1. System Clock Settings

Frequency of System Clock		IE-789801-NS-EM1	CPU Clock Source Selection (ID)
		Parts Board (X1)	
When using clock that is already mounted on emulation board	6 MHz	Oscillator used	Internal
When using clock mounted by user	Other than 6 MHz	Oscillator assembled by user	
When pulse is input from target system		Oscillator not used	External

Caution When a pulse is input from target system, open the configuration dialog box when starting the integrated debugger (ID78K0S-NS) and select “External” in the area (Clock) for selecting the CPU’s clock source (this selects the user’s clock).

Remark The IE-789801-NS-EM1’s factory settings are those listed above under “when using clock that is already mounted on emulation board”.

(1) When using clock that is already mounted on emulation board

When the IE-789801-NS-EM1 is shipped, a 6 MHz crystal oscillator is already mounted in the IE-789801-NS-EM1’s X1 socket. If the IE-789801-NS-EM1 is in the same state as when shipped, no hardware settings are required.

When starting the integrated debugger (ID78K0S-NS), open the configuration dialog box and select “Internal” in the area (Clock) for selecting the CPU’s clock source (this selects the emulator’s internal clock).

(2) When using clock mounted by user

The settings described under either (a) or (b) are required, depending on the type of clock to be used. When starting the integrated debugger (ID78K0S-NS), open the configuration dialog box and select “Internal” in the area (Clock) for selecting the CPU’s clock source (this selects the emulator’s internal clock).

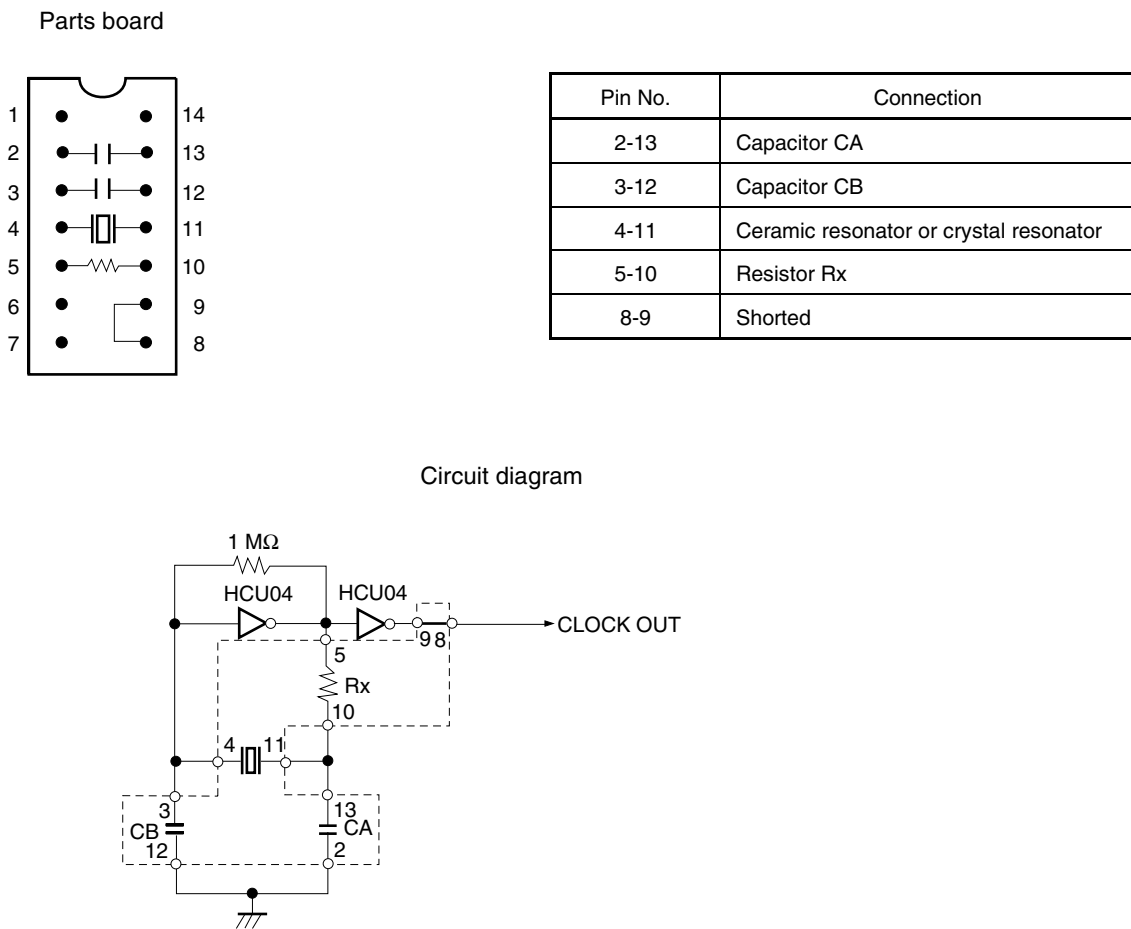
(a) When using a ceramic resonator or crystal resonator

- Items to be prepared
 - Parts board
 - Ceramic resonator or crystal resonator
 - Resistor Rx
- Capacitor CA
- Capacitor CB
- Solder kit

<Steps>

<1> Solder the target ceramic resonator or crystal resonator, resistor Rx, capacitor CA, and capacitor CB (all with suitable oscillation frequency) onto the parts board (as shown below).

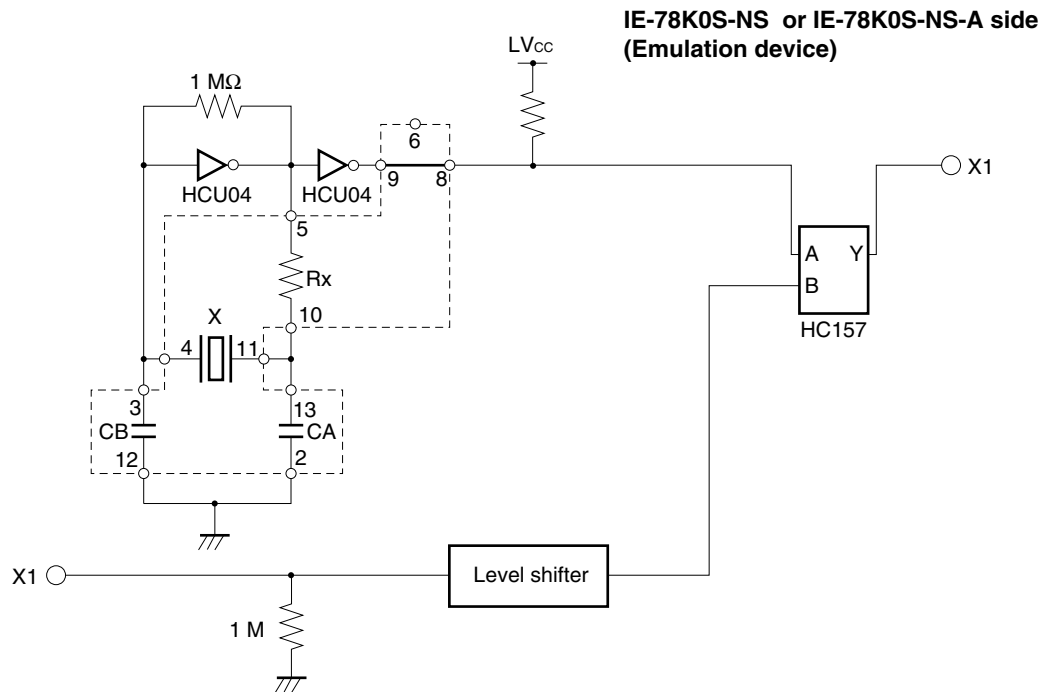
Figure 3-6. Connections on Parts Board (When Using User-Mounted Clock)



Remark The sections enclosed in broken lines indicate parts that are attached to the parts board.

- <2> Prepare the IE-789801-NS-EM1.
- <3> Remove the crystal oscillator that is mounted in the IE-789801-NS-EM1's socket (the socket marked as X1).
- <4> Connect the parts board (from <1> above) to the socket (X1) from which the crystal oscillator was removed. Check the pin 1 mark to make sure the board is mounted in the correct direction.
- <5> Make sure that the parts board mounted in the X1 socket on the emulation board is wired as shown in Figure 3-6 above.
- <6> Install the IE-789801-NS-EM1 in the IE-78K0S-NS or IE-78K0S-NS-A.

The above steps configure the following circuit and enable supply of the clock from the mounted resonator to the emulation device.

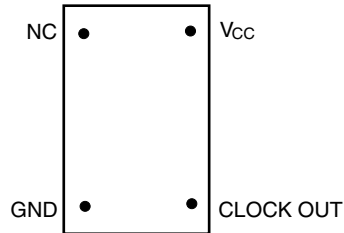


Remark The sections enclosed in broken lines indicate parts that are attached to the parts board.

(b) When using a crystal oscillator

- Items to be prepared
 - Crystal oscillator (see pinouts shown in Figure 3-7)

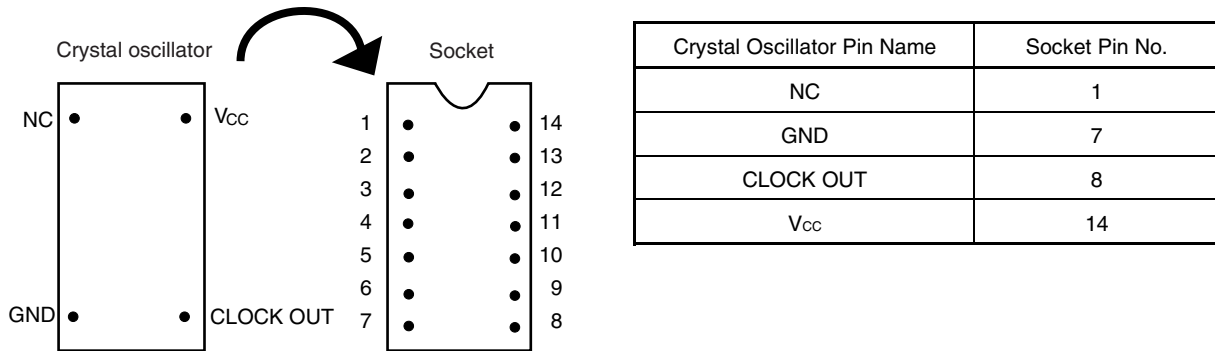
Figure 3-7. Crystal Oscillator (When Using User-Mounted Clock)



<Steps>

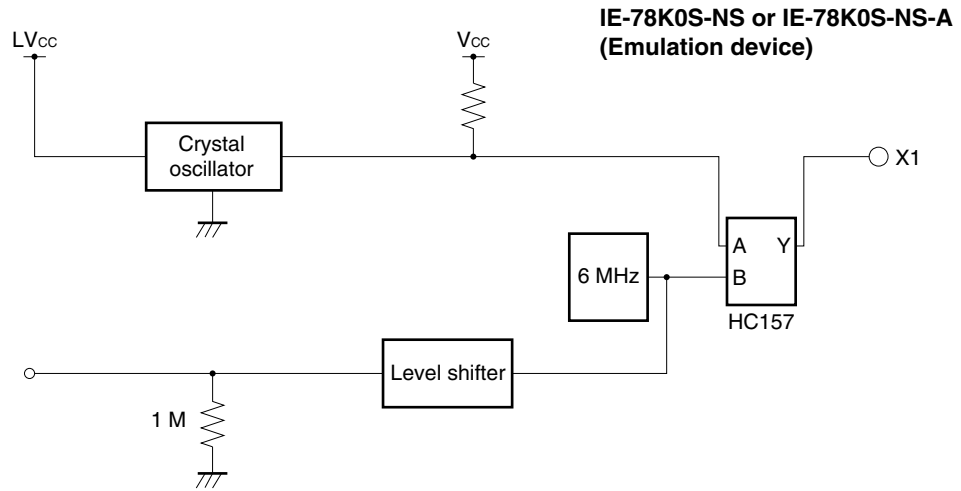
- <1> Prepare the IE-789801-NS-EM1.
- <2> Remove the crystal oscillator that is mounted in the IE-789801-NS-EM1's socket (the socket marked as X1).
- <3> Connect the crystal oscillator (from <2> above) to the socket (X1) from which the parts board for external clock was removed. Insert the crystal oscillator pin into the socket aligning the pins as shown in the figure below.

Figure 3-8. Pin Alignment of Crystal Oscillator and Socket



- <4> Install the IE-789801-NS-EM1 in the IE-78K0S-NS or IE-78K0S-NS-A.

The above steps configure the following circuit and enable supply of the clock from the mounted resonator to the emulation device.

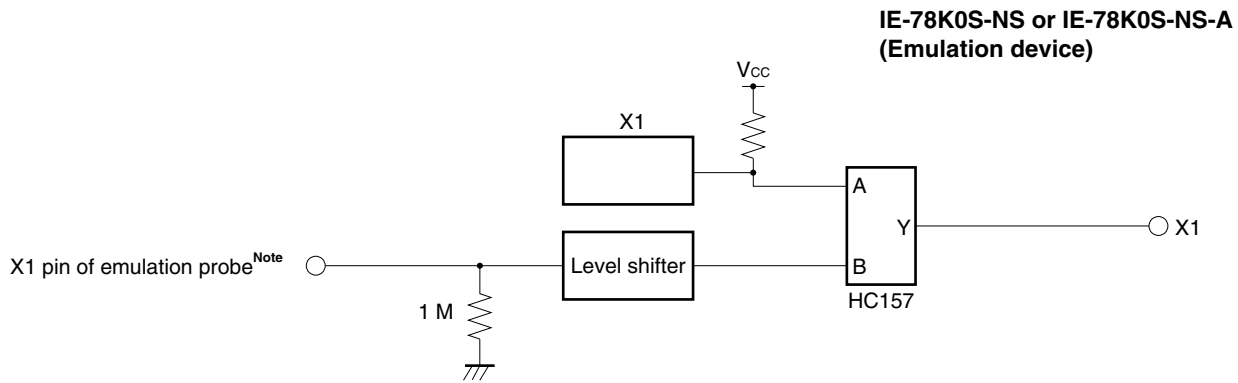


(3) When inputting pulse from target system

No hardware settings are required for this situation.

When starting the integrated debugger (ID78K0S-NS), open the configuration dialog box and select “External” in the area (Clock) for selecting the CPU’s clock source (this selects the user’s clock).

The above steps configure the following circuit and enable supply of the clock on the target device to the emulation device.



Note Indicates the pin name of the target device.

★ 3.3 Setting Power Supply Voltage of Target Interface

In the IE system, emulation is possible with a voltage of the same level as the power supply voltage of the target system.

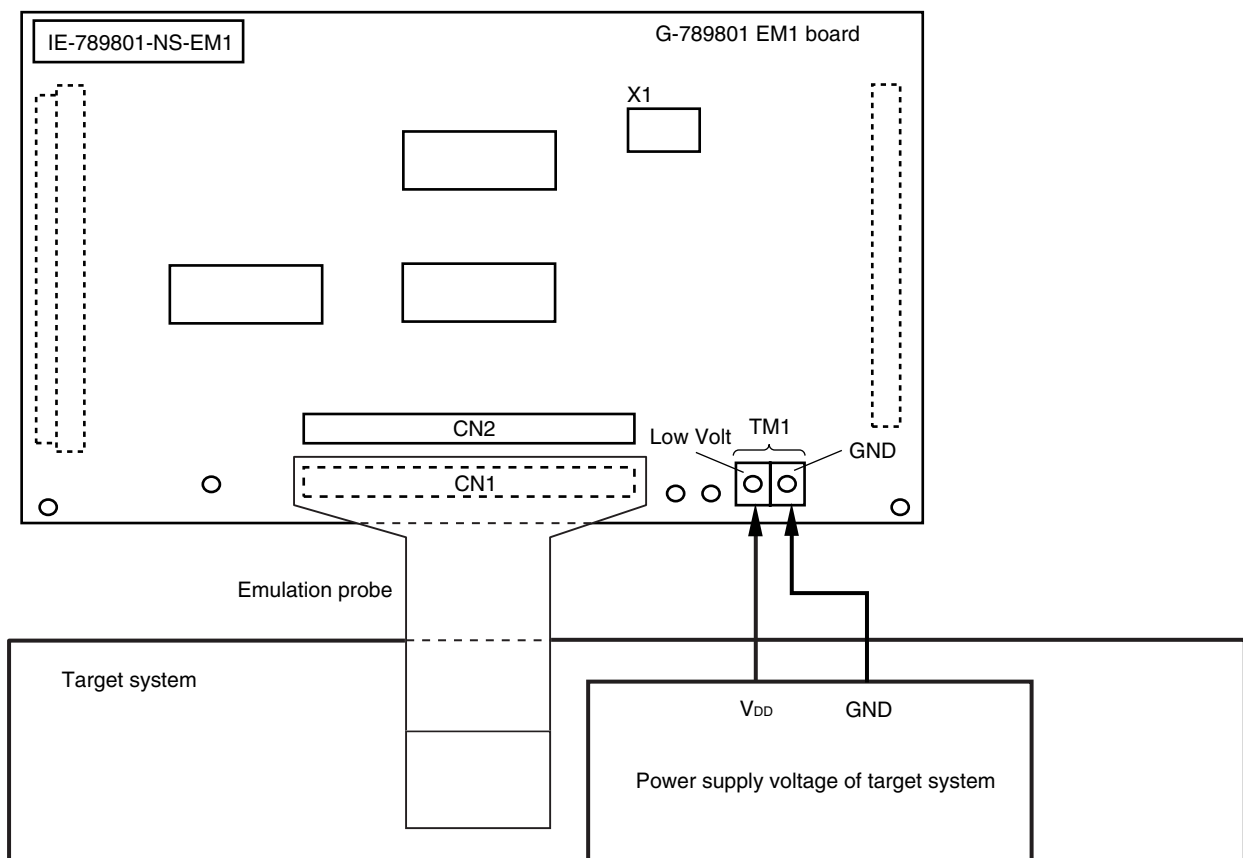
When the target system is not connected, the IE system automatically operates with the emulator's internal power supply (5 V).

When debugging with a voltage that is the same level as that of the target system, supply the same voltage as that of the target system to the TM1 terminal pin of the IE-789801-NS-EM1 (the same applies when the voltage is 5 V).

Set the target voltage to between 4.0 and 5.0 V.

- Maximum current consumption of TM1
4.0 to 5.0 V: Approximately 100 mA

Figure 3-9. Connection of TM1 and Power Supply Voltage of Target System



Caution Connect TM1 on the board and the power supply voltage of the target system after turning off the power of the IE-78K0S-NS or IE-78K0S-NS-A.

Remark The V_{DD} pin of the target system is only used for controlling LED1 (TV_{CC}), which monitors whether the power supply of the target system is connected in the IE-789801-NS-EM1.

★ 3.4 External Trigger

To set up an external trigger, connect it to the IE-789801-NS-EM1's check pins EXTOUT and EXTIN as shown below.

See the **IE-78K0S-NS User's Manual (U13549E)** or **IE-78K0S-NS-A User's Manual (U15207E)** for pin characteristics, and the **ID78K0-NS, ID78K0S-NS Integrated Debugger Ver. 2.20 or Later Operation User's Manual (Windows Based) (U14910E)** for usage methods.

(1) EXTOUT

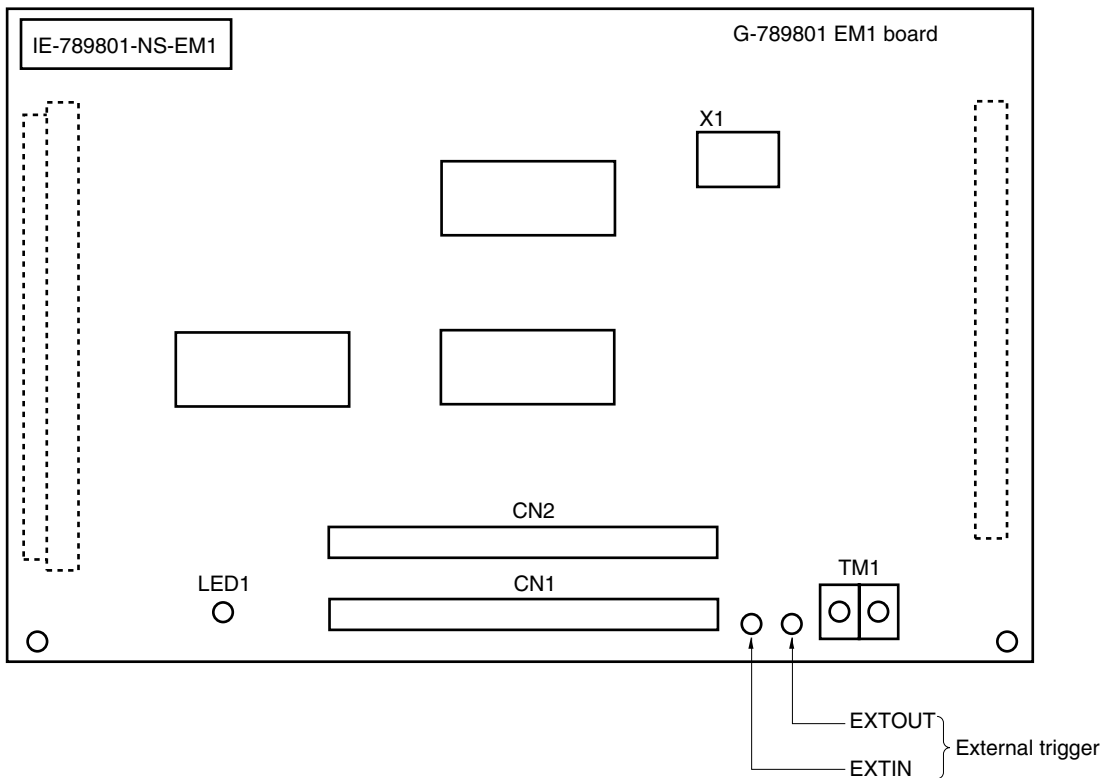
Output a low level from the EXTOUT pin on the IE-789801-NS-EM1 for 1.3 μ s upon the occurrence of a break event.

Caution Connect a pull-up resistor on the target system since this is an open-drain output.

(2) EXTIN

An event signal can be input from the EXTIN pin on the IE-789801-NS-EM1. Use a high-level pulse signal of 2 CPU operation clocks or more for the event signal.

Figure 3-10. External Trigger Input Position



3.5 Switch and Jumper Settings

(1) Switch and jumper setting on IE-78K0S-NS

When using the IE-789801-NS-EM1, set the switches and jumpers on the IE-78K0S-NS as shown in Table 3-2. For the position of the switches and jumpers, refer to the **IE-78K0S-NS User's Manual (U13549E)**.

Table 3-2. Switch and Jumper Settings on IE-78K0S-NS

	JP1	JP4	SW1	SW3	SW4
Setting	Short 2-3	Short 1-2	OFF	All switches ON	All switches ON

Caution If the jumpers and switches are set incorrectly, the board may be damaged.

★ **(2) Switch and jumper setting on IE-78K0S-NS-A**

When using the IE-789801-NS-EM1, set the switches and jumpers on the IE-78K0S-NS-A as shown in Table 3-3. For the position of the switches and jumpers, refer to the **IE-78K0S-NS-A User's Manual (U15207E)**.

Table 3-3. Switch and Jumper Settings on IE-78K0S-NS-A

	SW1	JP1	JP3
Setting	OFF	1-2	Shorted (fixed)

Caution If the jumpers and switches are set incorrectly, the board may be damaged.

CHAPTER 4 DIFFERENCES BETWEEN TARGET DEVICES AND TARGET INTERFACE CIRCUITS

This chapter describes differences between the target device's signal lines and the signal lines of the IE-789801-NS-EM1's target interface circuit.

Although the target device is a CMOS circuit, the IE-789801-NS-EM1's target interface circuit consists of an emulation chip, TTL, CMOS-IC, and other components.

When the IE system is connected with the target system for debugging, the IE system performs emulation so as to operate as the actual target device would operate in the target system.

However, some minor differences exist since the operations are performed via the IE system's emulation.

- (1) Signals input to or output from the EVA chip and the peripheral EVA chip
- (2) Signals input from the target system via a gate
- (3) Other signals

The IE-789801-NS- EM1's circuit is used as follows for signals listed in (1) to (3) above.

(1) Signals input to or output from the EVA chip and the peripheral EVA chip

The following signals perform the same operations as in the μ PD789800 Subseries.

- Signals related to port 0
- Signals related to port 1
- Signals related to port 2
- Signals related to port 4
- Signals related to USB

(2) Signals input from the target system via a gate

Since the following signals are input via a gate, their timing shows a delay compared to the μ PD789800 Subseries.

- RESET signal
- Signals related to clock input

(3) Other signals

- V_{DD} pin

The emulation CPU operates with the internal power supply of the IE system (5 V) when not connected to the target system, and operates with the voltage (LV_{CC}) supplied from the voltage supply pin (TM1) when connected to the target system.

The V_{DD} pin of the target system is used only for control of LED1 (USER V_{DD}), which indicates whether the target system's power is on or off.

- V_{SS} pin

The V_{SS} pin is internally connected to GND in the IE-789801-NS-EM1.

- V_{PP}/IC pins

These are not used for the IE-789801-NS-EM1

- X2 pin

This is not used for the IE-789801-NS-EM1.

Figure 4-1. Equivalent Circuit 1 of Emulation Circuit

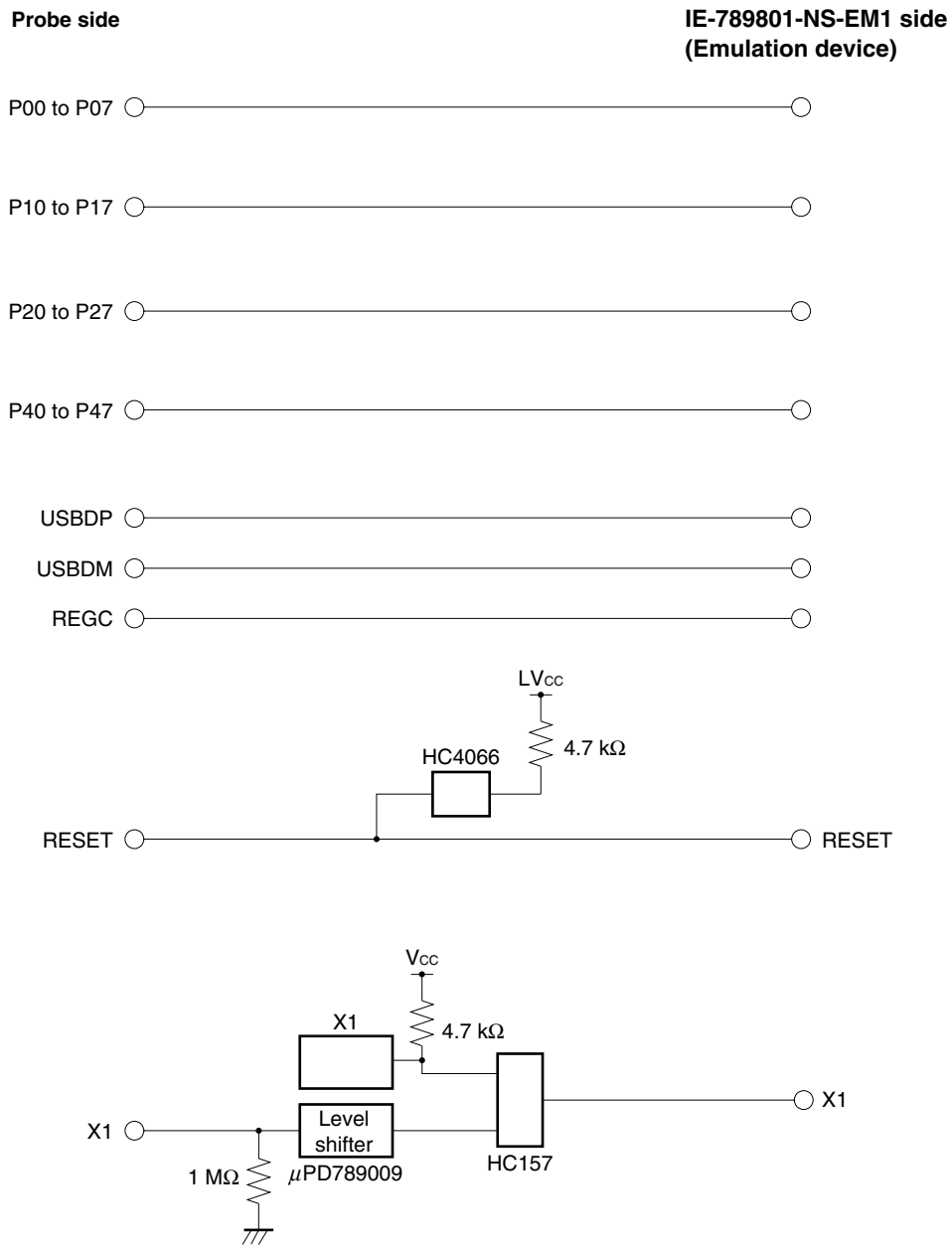
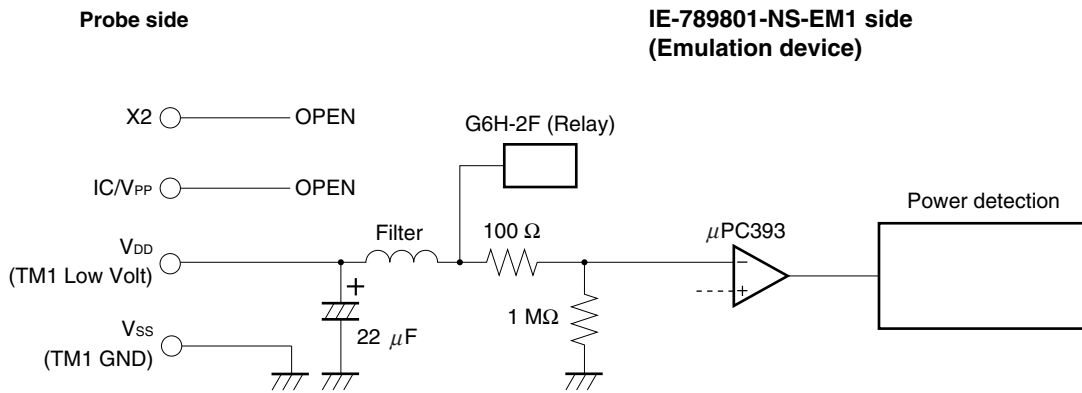


Figure 4-2. Equivalent Circuit 2 of Emulation Circuit



APPENDIX A EMULATION PROBE PIN ASSIGNMENT TABLE

Table A-1. NP-44GB, NP-44GB-TQ, and NP-H44GB-TQ Pin Assignments

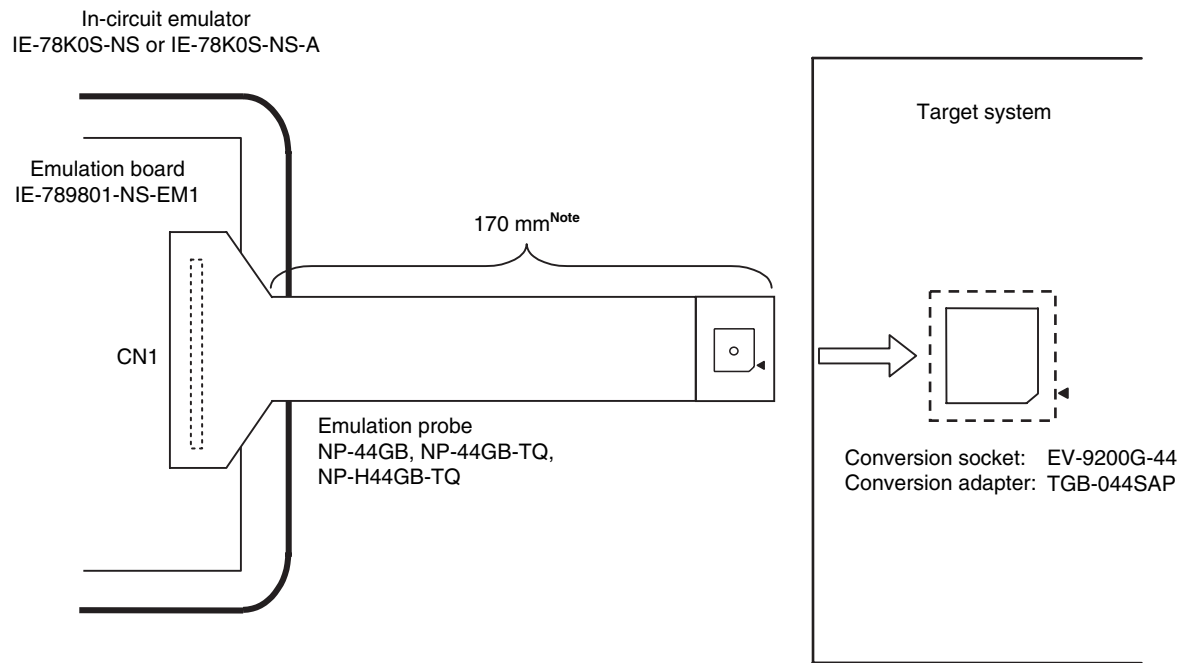
Emulation Probe	CN1 Pin No.	Emulation Probe	CN1 Pin No.
1	104	23	18
2	103	24	17
3	100	25	22
4	99	26	21
5	94	27	28
6	93	28	27
7	30	29	92
8	29	30	91
9	24	31	98
10	23	32	97
11	20	33	102
12	47	34	73
13	48	35	72
14	51	36	69
15	52	37	70
16	57	38	63
17	58	39	64
18	59	40	61
19	60	41	62
20	55	42	65
21	56	43	66
22	49	44	71

- Remarks**
1. The NP-44GB, NP-44GB-TQ, and NP-H44GB-TQ are products of Naito Densai Machida Mfg. Co., Ltd.
 2. The numbers in the “Emulation Probe” column indicate the corresponding pin number on the emulation probe tip.

APPENDIX B CAUTIONS ON DESIGNING TARGET SYSTEM

Figures B-1 and B-2 show the conditions when connecting the emulation probe to the conversion adapter or conversion socket. Follow the configuration below and consider the shape of parts to be mounted on the target system when designing a system.

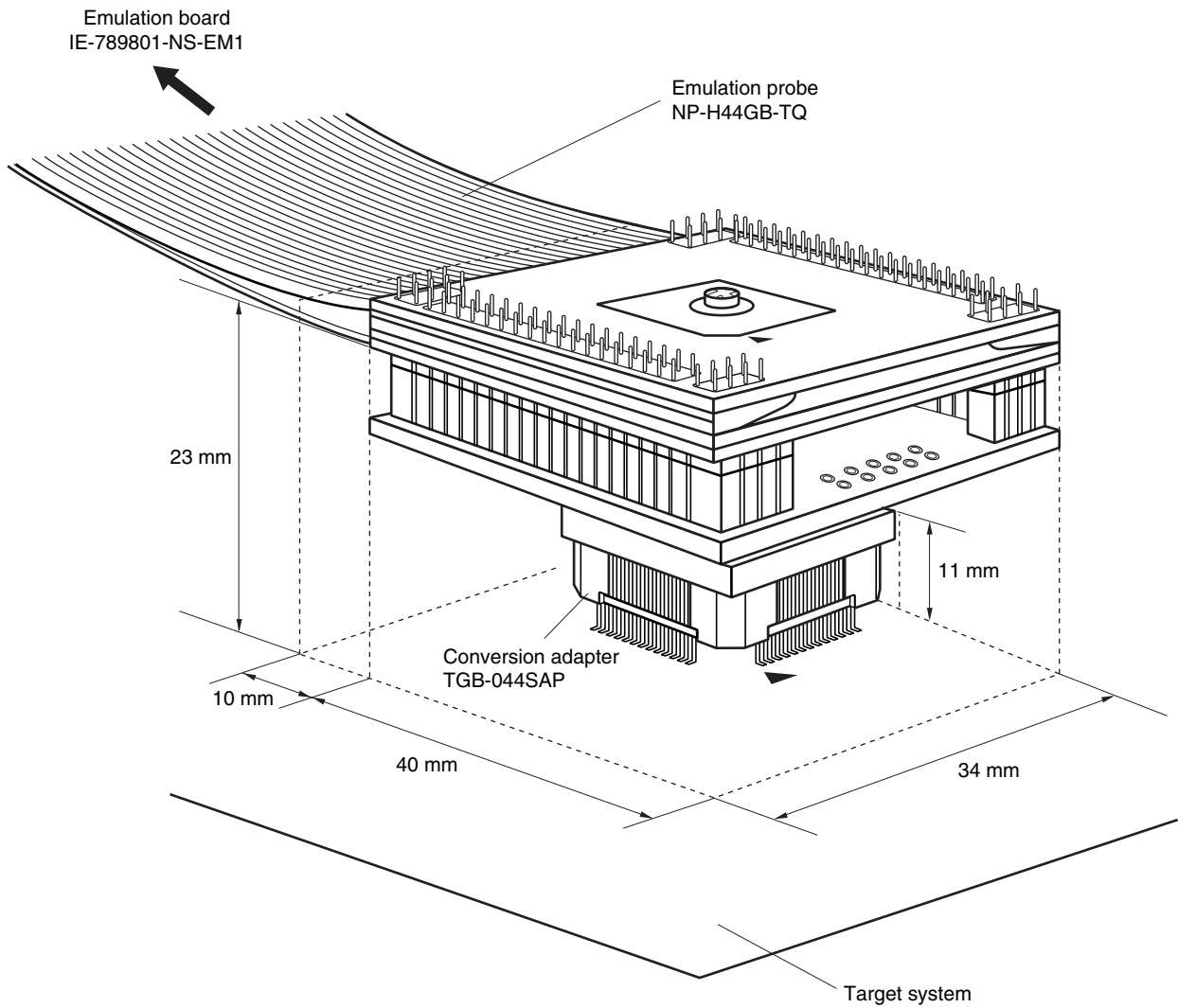
Figure B-1. Distance Between In-Circuit Emulator and Conversion Socket



Note Distance when NP-44GB and NP-44GB-TQ are used. When NP-H44GB-TQ is used, the distance is 370 mm.

Remarks 1. NP-44GB, NP-44GB-TQ, and NP-H44GB-TQ are products of Naito Densai Machida Mfg. Co., Ltd.
2. TGB-044SAP is a product of TOKYO ELETECH CORPORATION.

Figure B-2. Connection Condition of Target System (NP-H44GB-TQ)



- Remarks**
1. NP-H44GB-TQ is a product of Naito Densai Machida Mfg. Co., Ltd.
 2. TGB-044SAP is a product of TOKYO ELETECH CORPORATION.

[MEMO]

Facsimile Message

Although NEC has taken all possible steps to ensure that the documentation supplied to our customers is complete, bug free and up-to-date, we readily accept that errors may occur. Despite all the care and precautions we've taken, you may encounter problems in the documentation. Please complete this form whenever you'd like to report errors or suggest improvements to us.

From:

Name

Company

Tel.

FAX

Address

Thank you for your kind support.

North America

NEC Electronics Inc.
Corporate Communications Dept.
Fax: +1-800-729-9288
+1-408-588-6130

Hong Kong, Philippines, Oceania

NEC Electronics Hong Kong Ltd.
Fax: +852-2886-9022/9044

Taiwan

NEC Electronics Taiwan Ltd.
Fax: +886-2-2719-5951

Europe

NEC Electronics (Europe) GmbH
Market Communication Dept.
Fax: +49-211-6503-274

Korea

NEC Electronics Hong Kong Ltd.
Seoul Branch
Fax: +82-2-528-4411

Asian Nations except Philippines

NEC Electronics Singapore Pte. Ltd.
Fax: +65-250-3583

South America

NEC do Brasil S.A.
Fax: +55-11-6462-6829

P.R. China

NEC Electronics Shanghai, Ltd.
Fax: +86-21-6841-1137

Japan

NEC Semiconductor Technical Hotline
Fax: +81-44-435-9608

I would like to report the following error/make the following suggestion:

Document title: _____

Document number: _____ Page number: _____

If possible, please fax the referenced page or drawing.

Document Rating	Excellent	Good	Acceptable	Poor
Clarity	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Technical Accuracy	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
Organization	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>