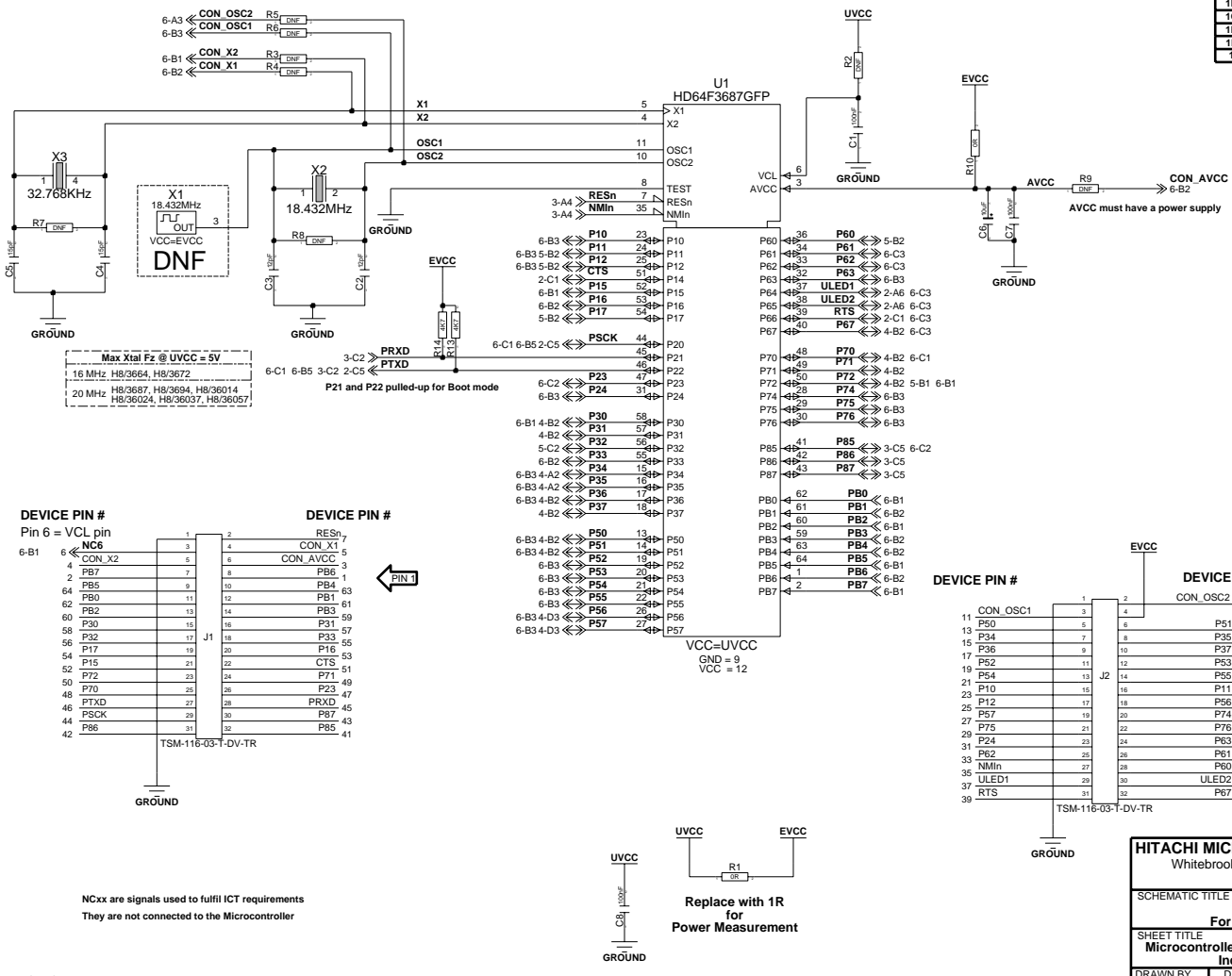


REV	REF	DATE	DRAWN BY	
1A	First Issue	25.05.02	J.Dinks	
1B	First Issue	20.06.02	M.H.C	
1C	DR	27.09.02	30.09.02	M.H.C
1D	DR	10.10.02	11.10.02	M.H.C
1E	D.Spec V2a	29.10.02	M.H.C	
1	DR4	27.11.02	MJB	



DEVICE PIN #	DEVICE PIN #
Pin 6 = VCL pin	RESn 7
6-B1	CON_X1 5
6-B1	CON_X2 5
6-B1	CON_AVCC 5
6-B1	PB7 1
6-B1	PB5 9
6-B1	PB0 11
6-B1	PB1 61
6-B1	PB2 13
6-B1	PB3 59
6-B1	P30 15
6-B1	P32 17
6-B1	P33 57
6-B1	P17 19
6-B1	P15 21
6-B1	P72 23
6-B1	P70 25
6-B1	P23 47
6-B1	PTXD 27
6-B1	PRXD 45
6-B1	PSCK 31
6-B1	P86 33
6-B1	P85 41

DEVICE PIN #	DEVICE PIN #
CON_OSC1 3	CON_OSC2 10
P50 5	P51 14
P34 7	P35 18
P36 9	P37 16
P52 11	P53 20
P54 13	P55 22
P10 15	P11 24
P12 17	P13 26
P57 19	P74 28
P75 21	P76 28
P29 23	P63 30
P62 25	P61 34
NMin 27	P60 36
ULED1 29	ULED2 38
RTS 31	P67 40

SIZE A4

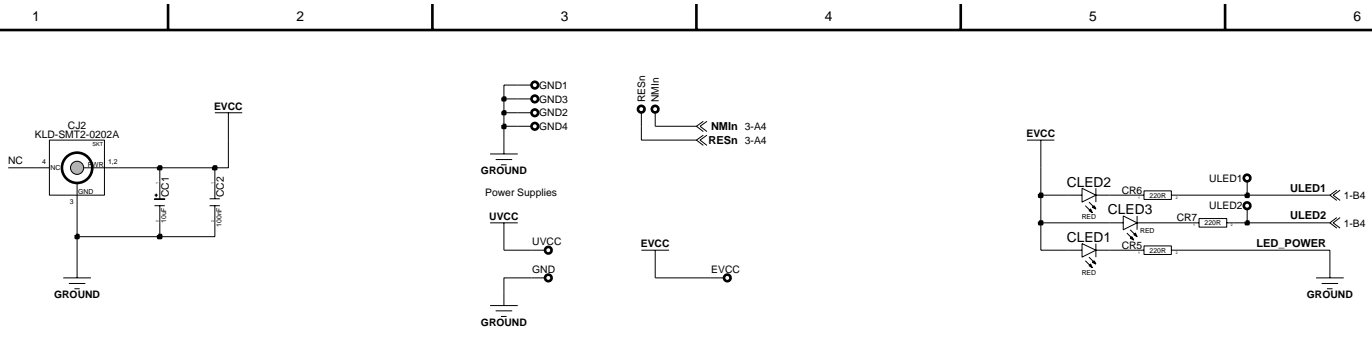
**HITACHI MICRO SYSTEMS EUROPE LTD**  
Whitebrook Park, Maidenhead, Berkshire  
UNITED KINGDOM

**SHEMATIC TITLE**  
EDK Tiny  
For Evaluation Purposes

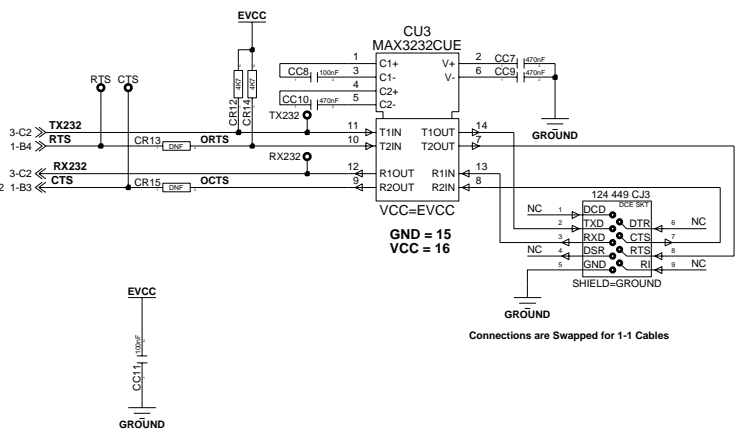
**SHEET TITLE**  
Microcontroller & Specific Supprting Circuitry  
Including Pin Headers

DRAWN BY	DATE	REV	DRAWING NUMBER	PAGE
M.H.C	27-11-02	1	D004172_04	1/6

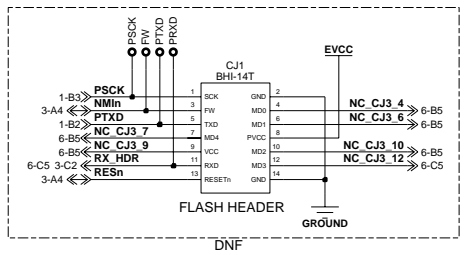
DNF = DO NOT FIT  
INDICATES USER CONFIGURATION OPTION



Testpoints for layout purposes



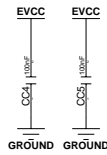
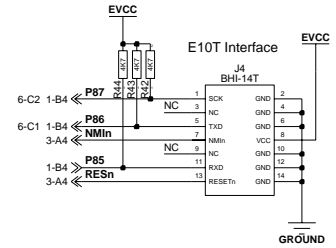
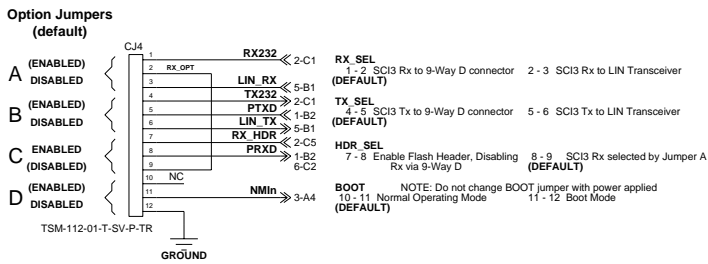
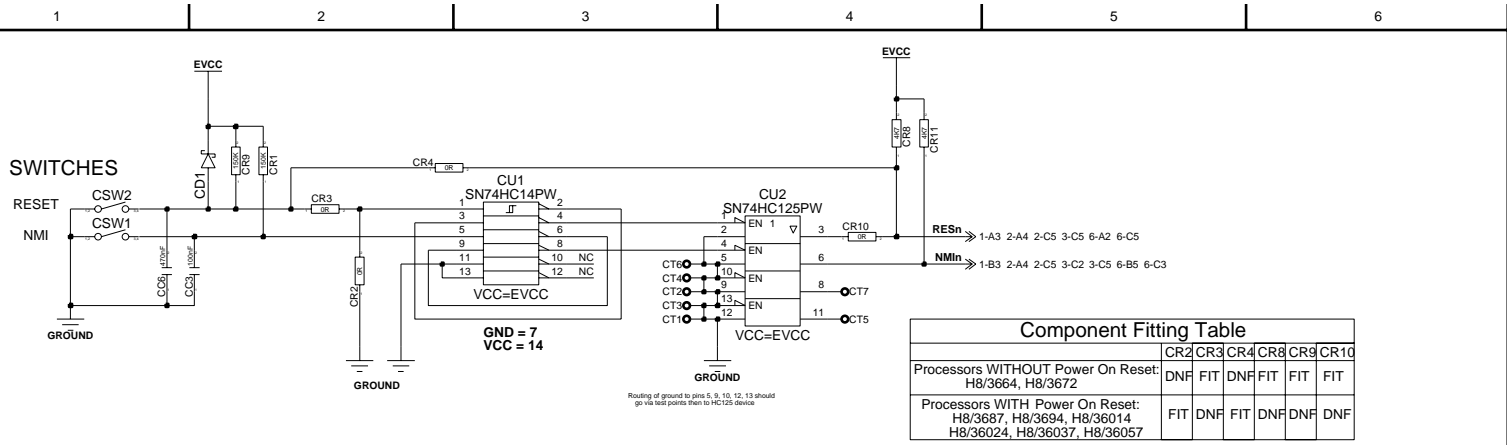
Connections are Swapped for 1-1 Cables



DNF

DNF = Do not fit component  
 These components are either option selection  
 or are not required to support this microcontroller  
 Please refer to the user manual for details

SIZE A4			
<b>HITACHI MICRO SYSTEMS EUROPE LTD</b> Whitebrook Park, Maidenhead, Berkshire UNITED KINGDOM			
SCHEMATIC TITLE		<b>EDK Tiny For Evaluation Purposes</b>	
SHEET TITLE			
<b>PSU connection, Flash Programming header LED and EIA232 Interface</b>			
DRAWN BY	DATE	REV	DRAWING NUMBER
M.H.C	27-11-02	1	D004172_04
			PAGE 3/6



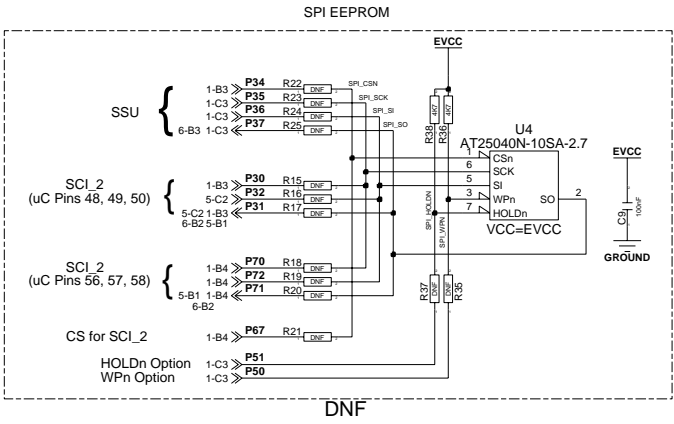
SIZE A4

**HITACHI MICRO SYSTEMS EUROPE LTD**  
Whitebrook Park, Maidenhead, Berkshire  
UNITED KINGDOM

SHEMATICAL TITLE: **EDK Tiny For Evaluation Purposes**

SHEET TITLE: **Reset & NMI signal Generation, E10T Connection and Option Links**

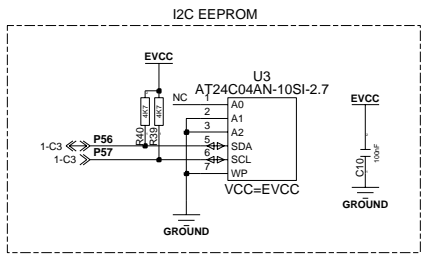
DRAWN BY	DATE	REV	DRAWING NUMBER	PAGE
M.H.C	27-11-02	1	D004172_04	3/6



DNF

SPI Port Selection Options		
Port Option	Option Valid On	Resistors to Fit
SSU	H8/36037, H8/36057	R22, R23, R24, R25
SCL_2 (Pins 48, 49, 50)	H8/3687, H8/36057	R15, R16, R17
SCL_2 (Pins 56, 57, 58)	H8/36014, H8/36024	R18, R19, R20

NOTES: No SPI Support is provided for H8/3672  
Only one of the above options should be selected.  
R36 and R38 should be included in all configurations



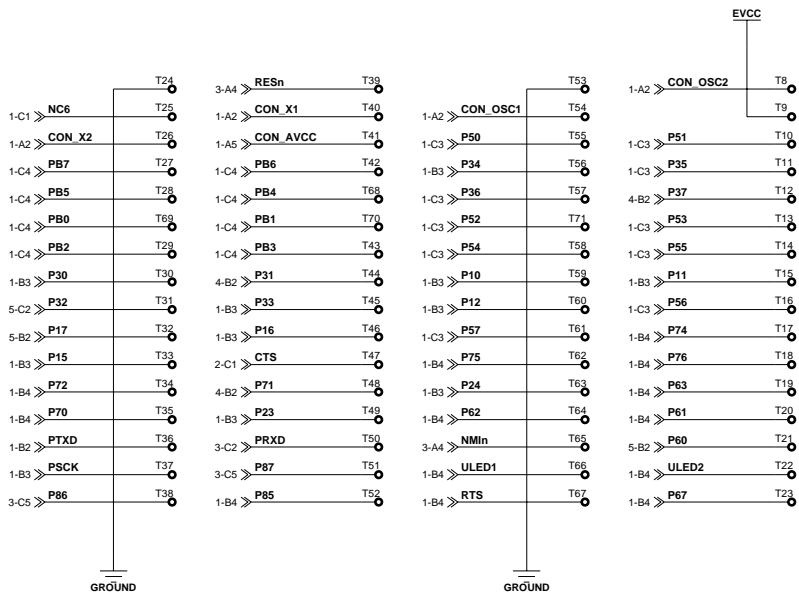
DNF

NOTE: I2C interface is only available on H8/3664, H8/3694, H8/3687

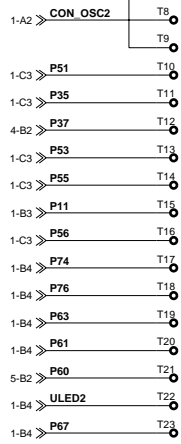
SIZE A4					
<b>HITACHI MICRO SYSTEMS EUROPE LTD</b>					
Whitebrook Park, Maidenhead, Berkshire UNITED KINGDOM					
SCHEMATIC TITLE		<b>EDK Tiny For Evaluation Purposes</b>			
SHEET TITLE		<b>I2C and SPI ROM</b>			
DRAWN BY	DATE	REV	DRAWING NUMBER	PAGE	
M.H.C	27-11-02	1	D004172_04	4/6	



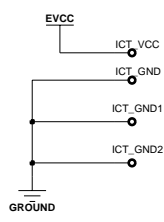
Microcontroller Expansion Connector Test Points



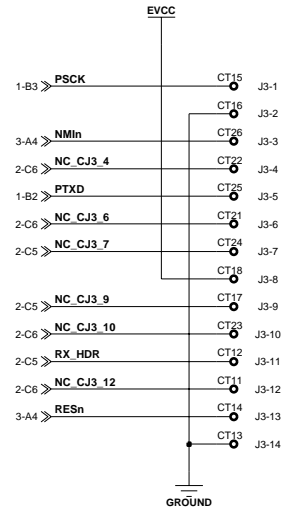
EVCC



ICT Power Injection



Programming Header Test Points



ALL TESTPOINTS ON UNDERSIDE OF PCB AND NOT MARKED FOR MANUFACTURING USE ONLY

SIZE A4				
<b>HITACHI MICRO SYSTEMS EUROPE LTD</b> Whitebrook Park, Maidenhead, Berkshire UNITED KINGDOM				
SCHEMATIC TITLE				
<b>EDK Tiny</b> <b>For Evaluation Purposes</b>				
SHEET TITLE				
<b>Testpoints for Production Test</b>				
DRAWN BY	DATE	REV	DRAWING NUMBER	PAGE
M.H.C	27-11-02	1	D004172_04	9/6