

# **AT91CAP9HA-DK Development Kit** for CAP Customizable Microcontroller

The AT91CAP9HA-DK Development Kit is the ideal platform to start developing designs on the ARM926EJ-S<sup>™</sup>-based CAP9H customizable microcontroller. The fixed portion of the CAP9H device is implemented as a microcontroller standard product, coupled to a high-density FPGA, integrating the equivalent of 2 million ASIC gates, that emulates the metal programmable block. The kit can be rapidly configured to emulate the behavior of a design under development. This saves time and reduces development costs, enabling customer designs to be fully debugged before commitment to metal programmed silicon, making right-first-time silicon easier to achieve.

The kit consists of three associated boards, the motherboard, mezzanine and memory extension, to be used jointly. The boards include a range of memories and physical interfaces/connectors representing external system components. This configuration enables parallel hardware/software testing of the application under development at close to operational speed, with no penalty for hardware modifications. Software development proceeds in parallel with hardware development, and significantly reduces the design cycle time, increasing confidence in a right-first-time system solution.

### Motherboard

- ATX power supply connector
- 2x Full-speed Host USB interfaces
- 100-base T Ethernet PHY with three status LEDs
- DBGU serial communication port
- 4x analog inputs
- AC97 interface with three 3.5 mm audio jack connectors
- I2S audio codec with two 3.5 mm audio jack connectors
- 2x SD/MMC card slots

### **Mezzanine Board**

- AT91CAP9 ARM926EJ-S-based microcontroller
- Altera<sup>®</sup> Stratix<sup>®</sup> III FPGA, equivalent to 2 million ASIC gates (lower cost version available equivalent to 1.5 million ASIC gates)

## Memory Extension Board

- 1.8V option hosting Burst Cellular RAM, NAND Flash, Mobile DDR SDRAM and a service TWI EEPROM
- 3.3V option hosting SDRAM, NAND Flash, NOR Flash and a service TWI EEPROM

- Atmel TWI serial EEPROM
- 3.5 inch QVGA display LCD with Touch Panel
- Touch Screen Controller
- Image Sensor expansion connector
- Push button keyboard and user LEDs
- CAN bus interface
- PIO expansion connectors (PIOA, PIOB, PIOC, PIOD)
- Extension connectors
- 3x USB device PHY interfaces with USB B connectors (FPGA controlled)



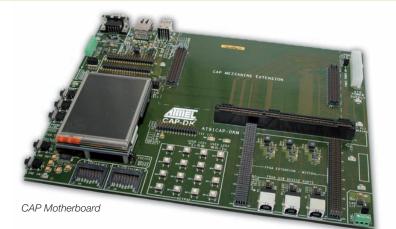


# **AT91CAP9HA-DK Development Kit** for CAP Customizable Microcontroller

## **CAP Product Family Development Kits**

The CAP family of ARM7<sup>™</sup>- and ARM9<sup>™</sup>-based products is supported by a full range of development kits. The motherboard is common to all kits. Mezzanine boards and memory extension boards are available in different variants to respond to customer needs in gate count and memory type.

Product	Gate Count	Memory Type
AT91CAP7X Dev Kit	Xilinx <sup>®</sup> Virtex <sup>®</sup> 4 FPGA (equivalent 500K ASIC gates)	1.8V Memory Board 3.3V Memory Board
AT91CAP9A Dev Kit	Altera <sup>®</sup> Stratix <sup>®</sup> II FPGA (equivalent 500K ASIC gates)	1.8V Memory Board 3.3V Memory Board
AT91CAP9HA15 Dev Kit	Altera <sup>®</sup> Stratix <sup>®</sup> III FPGA (equivalent 1.5M ASIC gates)	1.8V Memory Board 3.3V Memory Board
AT91CAP9HA20 Dev Kit	Altera <sup>®</sup> Stratix <sup>®</sup> III FPGA (equivalent 2M ASIC gates)	1.8V Memory Board 3.3V Memory Board









### Headquarters

**Atmel Corporation** 2325 Orchard Parkway San Jose, CA 95131, USA Tel.: (+1) (408) 441 0311 Fax: (+1) (408) 487 2600

#### International

Atmel Asia Unit 1-5 & 16, 19/F **BEA** Tower Millennium City 5 418 Kwun Tong Road Kwun Tong, Kowloon Hong Kong Tel.: (852) 2245 6100 Fax: (852) 2722 1369

#### Atmel Europe

Le Krebs 8, rue Jean-Pierre Timbaud B.P. 309 78054 Saint-Quentin-en-Yvelines Cedex, France Tel.: (33) 1 30 60 70 00 Fax: (33) 1 30 60 71 11

#### Atmel Japan 9F, Tonetsu Shinkawa Bldg. 1-24-8 Shinkawa

Chuo-ku, Tokyo 104-0033 Japan Tel.: (81) 3 3523 3551 Fax: (81) 3 3523 7581

#### **Product Contacts**

Technical Support www.atmel.com/products/AT91CAP/

Sales Contacts www.atmel.com/contacts

Web Site

www.atmel.com



© 2009 Atmel Corporation. All rights reserved.

Atmel<sup>®</sup>, Atmel logo and combinations thereof, and others, are registered trademarks, CAP™ and others are trademarks of Atmel Corporation or its subsidiaries. ARM®, the ARMPowered® logo, and others are registered trademarks or trademarks of ARM Ltd. Other terms and product names may be trademarks of others.

Rev.: 6460A-CAP-02/09/2M



CAP9A Mezzanine Board

Disclaimer: The information in this document is provided in connection with Athel products. No locrese, express or implied, by estoppel or otherwise, to any intellectual property right is granted by this document or in connection with the sale of Athel products. EXCEPT AS SET FORTH IN ATHEL'S TEMS AND CONDITIONS OF SALES LOCATED ON ATHEL'S WEB STE, ATHEL ASSUMES NO LABILITY WHATSOCYER AND DISCLAMS ANY EXPRESS, MPLIED OR STATUTORY WARPANTY REPORTS, INDIRECT, CONSCIUNNS, BUT NOT UNITED TO, THE WHELD WARPANTY OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PUPPOSC, OR NOV-INFINGEMENT: IN NO EVENT SHALL. TARLE LE LABLE LE LABLE CRI ANY DIRECT, NORECT, CONSCIUNNS, BUT NO LOSS OF NORMATION, PARING OUT OF THE USE OR INVESSION OF THE OSSIGN OF MORPHANING, STREPH AT TARLE LAB SELIDATE OF THE WARPANTY OF UNDERST, NORMECT, CONSCIUNNS, BUT NO LOSS OF NORMATION, PARING OUT OF THE USE OR INVESS OF NORMATION, PARING DUR T, IN NO EVENT SHALL. TARLE LE LABLE LE QUAR EN TO TRANSMENT OF UNDERST, NORMECT, CONSCIUNNT, EVENT AT TARLE LAB SEDIESS DIFFERENTIATION OF WARPANTY OF UNDERST, NORMECT, CONSCIUNNT, SUPERIT SHALL DAWAGES DIFFERENTIATION, DAWAGES AND PROFITS, BUSINESS NITERPHIPTON, OIL LOSS OF NORMATION, PARING OUT OF THE USE OR INVESTIGATION OF WARRENT OF WARRENT

CAR