

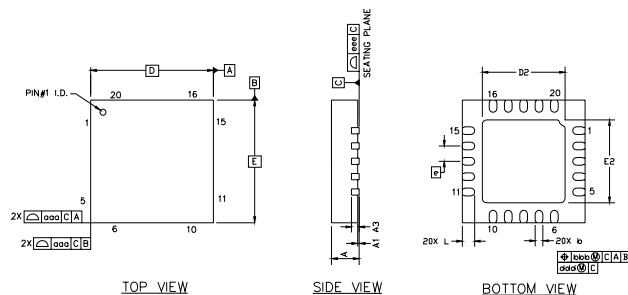


### Selected Electrical Specifications

( $T_A = -40$  to  $+85$  °C,  $V_{DD} = 2.7$  V unless otherwise specified)

Parameter	Conditions	Min	Typ	Max	Units
<b>Global Characteristics</b>					
Supply Voltage				3.6	V
Supply Current with CPU Active	Clock = 25 MHz	—	TBD	—	mA
	Clock = 1 MHz	—	TBD	—	mA
	Clock = 80 kHz; $V_{DD}$ monitor disabled	—	TBD	—	$\mu$ A
	Clock = 32 kHz; $V_{DD}$ monitor disabled	—	TBD	—	$\mu$ A
Supply Current (shutdown)	Oscillator off; $V_{DD}$ monitor disabled	—	TBD	—	$\mu$ A
Clock Frequency Range			—	25	MHz
<b>Internal Oscillators</b>					
Frequency (OSC0)			24.5	25.0	MHz
Frequency (OSC1)			80	—	kHz
<b>A/D Converter</b>					
Resolution					bits
Integral Nonlinearity			$\pm 1/2$	TBD	LSB
Differential Nonlinearity	Guaranteed monotonic	—	$\pm 1/2$	TBD	LSB
Signal-to-Noise Plus Distortion			55.5	—	dB
Throughput Rate			—	200	ksps
Input Voltage Range			—	$V_{REF}$	V
<b>D/A Converter</b>					
Resolution					bits
Integral Nonlinearity			$\pm 1/2$	—	LSB
Differential Nonlinearity	Guaranteed monotonic	—	$\pm 1/2$	TBD	LSB
Output Settling Time			5	—	$\mu$ s
<b>Comparator</b>					
Response Time Mode0	(CP+) – (CP–) = 100 mV	—	TBD	—	$\mu$ s
Current Consumption Mode0			TBD	—	$\mu$ A
Response Time Mode1	(CP+) – (CP–) = 100 mV	—	TBD	—	$\mu$ s
Current Consumption Mode1			TBD	—	$\mu$ A
Response Time Mode2	(CP+) – (CP–) = 100 mV	—	TBD	—	$\mu$ s
Current Consumption Mode2			TBD	—	$\mu$ A
Response Time Mode3	(CP+) – (CP–) = 100 mV	—	TBD	—	$\mu$ s
Current Consumption Mode3			TBD	—	$\mu$ A

### QFN-20 Package Information



Dimension	Millimeters			Dimension	Millimeters		
	Min	Nom	Max		Min	Nom	Max
A	0.80	0.90	1.00	E	4.00 BSC.		
A1	0.03	0.07	0.11	E2	2.55	2.70	2.85
A3	0.25 REF			L	0.30	0.40	0.50
b	0.18	0.25	0.30	aaa	—	—	0.15
D	4.00 BSC.			bbb	—	—	0.10
D2	2.55	2.70	2.85	ddd	—	—	0.05
e	0.50 BSC.			eee	—	—	0.08

### C8051F336DK Development Kit

