## Introduction

The ISL2819xEVAL1Z evaluation board is a design platform containing all the circuitry needed to characterize critical performance parameters of the ISL28190 and ISL28191 single operational amplifiers, using a variety of user defined test circuits.

The ISL28190 and ISL28191 amplifiers feature ultra-low noise, ultra-low distortion, and rail-to-rail output drive capability. They are designed to operate with single and dual supplies from $+5.5 \mathrm{VDC}( \pm 2.75 \mathrm{VDC})$ down to +3 VDC ( $\pm 1.5 \mathrm{VDC}$ ).

## Reference Documents

- ISL28190 Data Sheet, FN6247
- ISL28191 Data Sheet, FN6156


## Evaluation Board Key Features

The ISL2819xEVAL1Z is designed to enable the IC to operate from a single supply (+3VDC to +5.5 VDC ), or from split supplies ( $\pm 1.5 \mathrm{VDC}$ to $\pm 2.75 \mathrm{~V}$ ). The board is configured for a single op amps connected for differential input with a closed loop gain of 10. A single external reference voltage (VREF) pin and provisions for a user-selectable voltage divider (filter is included).

## Power Supplies (Figure 1)

External power connections are made through the $\mathrm{V}+$, V and Ground connections on the evaluation board. For single supply operation, the $V$ - and Ground pins are tied together to the power supply negative terminal. For split supplies $V+$ and $V$ - terminals connect to their respective power supply terminals. De-coupling capacitors $\mathrm{C}_{1}$ and $\mathrm{C}_{2}$ connect to ground through $R_{1}$ and $R_{21} 0 \Omega$ resistors. Resistors $R_{20}$ and
$\mathrm{R}_{24}$ are $0 \Omega$ but can be changed by the user to provide additional power supply filtering, or to reduce the voltage rate-of-rise to less than $\pm 1 \mathrm{~V} / \mu \mathrm{s}$. Two additional capacitors, $\mathrm{C}_{3}$ and $\mathrm{C}_{4}$ are connected close to the part to filter out high frequency noise. Anti-reverse diodes $D_{1}$ and $D_{2}$ protect the circuit in the case of accidental polarity reversal.


FIGURE 1. POWER SUPPLY CIRCUIT

## Amplifier Configuration (Figure 2)

The schematic of the op amp with the components supplied is shown in Figure 2. The circuit implements a differential input amp with a closed loop gain of 10 . The circuit can operate from a single 3 VDC to +5.5 VDC supply, or from dual supplies from $\pm 1.5 \mathrm{VDC}$ to $\pm 2.75 \mathrm{VDC}$. The VREF pin can be connected to ground to establish a ground referenced input for split supply operation, or can be externally set to any reference level for single supply operation.


FIGURE 2. BASIC AMPLIFIER CONFIGURATION

## User-Selectable Options (Figures 3 and 4)

Component pads are included to enable a variety of user-selectable circuits to be added to the amplifier inputs, the VREF input, outputs and the amplifier feedback loops.

A voltage divider and filter option (Figure 3) can be added to establish a power supply-tracking common mode reference at the VREF input. The inverting and non-inverting inputs
have additional resistor placements for adding input attenuation, or to establish input DC offsets through the VREF pin.

The output (Figure 4) has a series $50 \Omega$ back-termination resistor to drive $50 \Omega$ cables, and additional resistor and capacitor placements for loading.


FIGURE 3. INPUT STAGE


FIGURE 4. OUTPUT STAGE

## ISL2819xEVAL1Z Components Parts List

| DEVICE NUMBER | DESCRIPTION | COMMENTS |
| :---: | :---: | :---: |
| C1, C2, C5 | CAP-TANTALUM, SMD, D, 4.7 $\mu \mathrm{F}, 50 \mathrm{~V}, 10 \%$, LOW ESR, ROHS | Power supply decoupling |
| C3, C4 | CAP, SMD, 0603, 0.1 FF, 25V, 10\%, X7R, ROHS | Power supply decoupling |
| C6-C10 | CAP, SMD, 0603, DNP-PLACE HOLDER, ROHS | User selectable capacitors - not populated |
| D1, D2 | DIODE-RECTIFIER, SMD, SOD-123, 2P, 40V, 0.5A, ROHS | Reverse power protection |
| U1 (ISL28190EVAL1Z) | ISL28190FHZ-T7, IC-RAIL-TO-RAIL OP AMP, SOT-23, ROHS |  |
| U1 (ISL28191EVAL1Z) | ISL28191FHZ-T7, IC-RAIL-TO-RAIL OP AMP, SOT-23, ROHS |  |
| R2, R3, R6, R8, R9, R12, R13, R15, R16, R23, R25, R27 | RESISTOR, SMD, 0603, 0.1\%, MF, DNP-PLACE HOLDER | User selectable resistors - not populated |
| $\begin{gathered} \text { R1, R10, R11, R17, R20, R21, } \\ \text { R24, R26 } \end{gathered}$ | RES, SMD, 0603, $0 \Omega, 1 / 10 \mathrm{~W}, \mathrm{TF}, \mathrm{ROHS}$ | $0 \Omega$ user selectable resistors |
| R22 | RES, SMD, 0603, $49.9 \Omega, 1 / 10 \mathrm{~W}, 1 \%$, TF,ROHS | User selectable output resistors |
| R5, R7 | RES, SMD, 0603, 499 , 1/10W, 1\%, TF, ROHS | Gain resistors |
| R14, R19 | RES, SMD, 0603, 4.99k, 1/10W, 1\%, TF, ROHS | Gain resistors |
| R4, R18 | RES, SMD, 0603, 10k, 1/10W, 1\%, TF, ROHS | User selectable resistors |

ISL2819xEVAL1Z Top View


## ISL2819xEVAL1Z Schematic Diagram



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