# LMZ10504 4A Demo Board SIMPLE SWITCHER® Power Module Quick Start Guide

National Semiconductor Application Note 2030 Ricardo Capetillo January 22, 2010



#### Introduction

The LMZ10504 SIMPLE SWITCHER® power module is a complete, easy-to-use DC-DC solution capable of driving up to a 4A load with exceptional power conversion efficiency, output voltage accuracy, line and load regulation. The LMZ10504 is available in an innovative package that enhances thermal performance and allows for hand or machine soldering.

The LMZ10504 can accept an input voltage rail between 2.95V and 5.5V and deliver an adjustable and highly accurate output voltage as low as 0.8V. One megahertz fixed frequency PWM switching provides a predictable EMI characteristic. Two external compensation components can be adjusted to set the fastest response time, while allowing the option to use ceramic and/or electrolytic output capacitors. Externally programmable soft-start capacitor facilitates controlled startup. The LMZ10504 is a reliable and robust solution with the following features: lossless cycle-by-cycle peak current limit to protect for over current or short-circuit fault, thermal shutdown, input under-voltage lock-out, and pre-biased startup.

#### **Packaging Highlights**

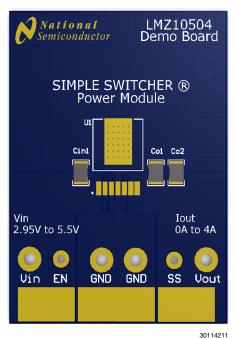
- Single exposed pad and standard pin out for easy mounting and manufacturing
- 10.2 x 13.8 x 4.6 mm (0.4 x 0.39 x 0.18 in) package
- · 20W maximum total output power
- 1.7" x 2.3" (4.3 cm x 5.8 cm) reduced size demo board form factor

#### **Demo Board Features**

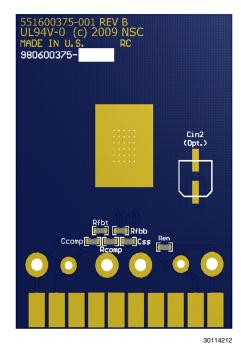
- Power input voltage range 2.95V-5.5V
- Input UVLO at 2.7V
- Adjustable output voltage range 0.8V to 5V
- Up to 4A output current
- Integrated shielded inductor in module
- Efficiency up to 96% at 1A load
- · All ceramic capacitor design
- Partial compensation allows adjustment of the control loop
- Starts into pre-biased loads
- ±1.63% maximum feedback voltage accuracy over temperature
- ±2.5% maximum feedback voltage accuracy over full line, load, and temperature conditions
- · Thermal Shutdown
- Only 9 external passives plus module
- · Two layer low cost assembly

### **Typical Applications**

- Point-of-load conversions from 3.3V and 5V rails
- Space constrained applications
- Extreme temperatures/no air flow environments
- Noise sensitive applications (i.e. transceiver, medical)
- Industrial controls
- Networking equipment



**Top Layer** 



**Bottom Layer** 

## **Absolute Maximum Module Ratings**

| VIN to GND        | -0.3V to 6V |
|-------------------|-------------|
| EN, FB, SS to GND | -0.3V to 6V |

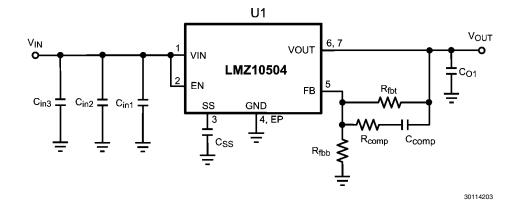
#### **Module Operating Ratings**

| - |                               |                   |  |
|---|-------------------------------|-------------------|--|
|   | VIN                           | 2.95V to 5.5V     |  |
|   | Junction Temperature Range (T | ) -40°C to +125°C |  |

# **Demo Board Operating Ratings**

|                                     | <u> </u>                    |
|-------------------------------------|-----------------------------|
| $V_{IN}$                            | 2.95V to 5.5V               |
| V <sub>OUT</sub>                    | 2.5V                        |
| I <sub>OUT</sub>                    | 0A to 4A                    |
| Softstart time                      | 4 ms                        |
| Operating Ambient                   | - 40°C to +94°C (at full 4A |
| Temperature Range (T <sub>A</sub> ) | load)                       |
| $\theta_{JA}$                       | 30°C/W                      |
| $\theta_{JC}$                       | 1.9°C/W                     |
| Board Dimensions                    | 1.7 x 2.3 (4.3 cm x 5.8 cm) |
| Number of Layers                    | 2                           |

## **Demo Board Schematic**



#### **Demo Board Bill of Materials**

| Ref Des  | Description          | Case Size | Manufacturer           | Manufacturer P/N | Quantity |
|--|----------------------|-----------|------------------------|------------------|----------|
| U1   | SIMPLE SWITCHER®     | TO-PMOD   | National Semiconductor | LMZ10504         | 1        |
| C <sub>in1</sub> , C <sub>o1</sub> , C <sub>o2</sub> | 47 μF, X5R, 6.3V     | 1210      | TDK                    | C3225X5R0J476M   | 3        |
| C <sub>in2</sub>                                     | 220 μF, 10V, AL-Elec | E         | Panasonic              | EEE-FPA221XAP    | 0*       |
| C <sub>comp</sub>                                    | 120 pF, 5%, C0G, 50V | 603       | TDK                    | C1608C0G1H121J   | 1        |
| C <sub>ss</sub>                                      | 10 nF, 5%, C0G, 25V  | 603       | TDK                    | C1608C0G1E103J   | 1        |
| R <sub>fbt</sub>                                     | 75 kO                | 603       | Vishay Dale            | CRCW060375K0FKEA | 1        |
| R <sub>fbb</sub>                                     | 34.8 kO              | 603       | Vishay Dale            | CRCW060334K8FKEA | 1        |
| R <sub>comp</sub>                                    | 1.1 kO               | 603       | Vishay Dale            | CRCW06031K10FKEA | 1        |
| R <sub>en</sub>                                      | 100 kO               | 603       | Vishay Dale            | CRCW0603100KFKEA | 1        |
| VIN, GND, VOUT                                       | Turret Terminal      | 1502-2    | Keystone               | 1502-2K-ND       | 4        |
| EN, SS   | Turret Terminal      | 1593-2    | Keystone               | 1593-2K-ND       | 2        |

<sup>\*</sup> Not installed

TABLE 1. Output Voltage Setting ( $R_{fbt} = 75 \text{ k}\Omega$ )

|                  | , o ( )   |
|------------------|-----------|
| V <sub>OUT</sub> | $R_{fbb}$ |
| 3.3 V            | 23.7 kΩ   |
| 2.5 V            | 34.8 kΩ   |
| 1.8 V            | 59 kΩ     |
| 1.5 V            | 84.5 kΩ   |
| 1.2 V            | 150 kΩ    |
| 0.9 V            | 590 kΩ    |

### **Demo Board Hookup**

**VOUT** Connect the load to VOUT and one of the GND posts. The module can source up to a 4A load current.

**VIN** Connect VIN to a positive voltage in the 2.95 to 5.5V range. Connect the negative terminal of the source supply to one of the posts labeled GND.

**EN** The enable is configured with a 100 k $\Omega$  pull up resistor, which enables the LMZ10504, but also allows the user to disable the LMZ10504, by pulling low on the EN post.

**Quiescent current** If the enable post is pulled low, the module will be disabled and about 260  $\mu$ A of supply current will flow into VIN (pin 1). To enable the device, leave the enable

post open, there will be approximately 12 mA of no-load quiescent current into the VIN post. (VIN to ground voltage at the post set to 3.3V).

#### **Demo Board Passive Components**

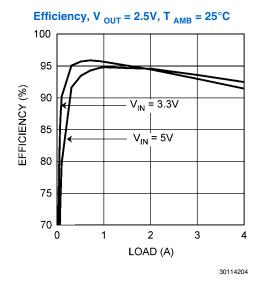
**Soft-start capacitor** The soft-start capacitor controls the rise time of the output voltage when power is first applied and following the clearing of a fault mode.

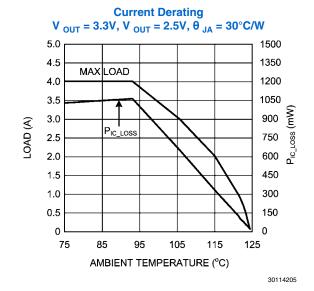
**Feedback divider** Regulator output voltage is programmed though the selection of the two resistors,  $R_{\text{fbb}}$  and  $R_{\text{fbt}}$ .  $C_{\text{comp}} R_{\text{comp}}$  is located in parallel with the upper feedback divider resistor,  $R_{\text{fbt}}$ . These components adjust the control loop and will improve the step response to abrupt changes in load current and input voltage.

**Output Capacitor** Parallel connections of two 47  $\mu$ F 6.3V multilayer ceramic are used for the output capacitor.

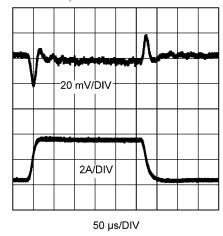
Input Capacitor A 47  $\mu$ F 6.3V multilayer ceramic ( $C_{in1}$ ) supplies the fast switching current generated from the quick rise time of the internal MOSFET.  $C_{in1}$  is also the energy reservoir for the input line. Additionally,  $C_{in2}$  is not installed, but is a placeholder for an aluminum capacitor to reduce the resonance of the input line produced by the inductance and resistance in the cables connecting the bench power supply to the evaluation board and the input capacitors.

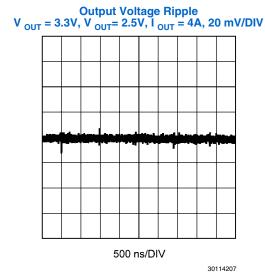
#### **Performance Characteristics**





 $\begin{array}{c} \text{Load Transient Response} \\ \text{V}_{\text{OUT}} = 5\text{V}, \, \text{V}_{\text{OUT}} = 2.5\text{V}, \, \text{I}_{\text{OUT}} = 0.4\text{A to } 3.6\text{A step} \\ \text{20 mV/DIV}, \, \text{20 MHz Bandwidth Limited} \end{array}$ 

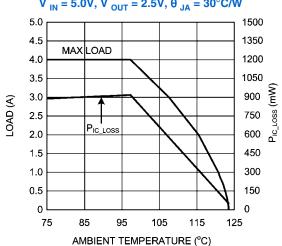


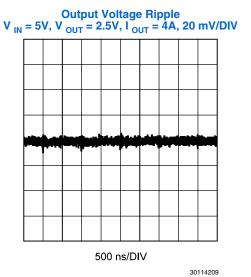




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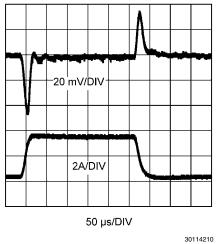
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