Quick Start Guide for a 0.5 Amp Buck Regulator Using The LM5574 and LM25574

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The LM5574 and LM25574 switching regulators feature all of the functions necessary to implement an efficient high voltage buck regulator using a minimum of external components. These easy to use regulators include either a 42V (LM25574) or a 75V (LM5574) N-Channel buck switch with an output current capability of 0.5 Amps. The operating frequency is programmable from 50kHz up to 1MHz. Protection features include: current limit, thermal shutdown and remote shutdown capability. The device is available in a TSSOP-16 package. This step-by-step guide provides an easy to use process to quickly select the external components necessary to com-

plete a design. More detailed information including theory of operation, design trade-offs and additional application guidance is available in the device datasheet. Shown below in Figure 1 is a complete schematic for a 0.5 Amp step-down DC-DC converter. Several external component values can be standardized for most applications. The input voltage range, output voltage and desired operating frequency dictate the remaining component values.

An Excel based spreadsheet derived from the guide is available on the National Semiconductor website.

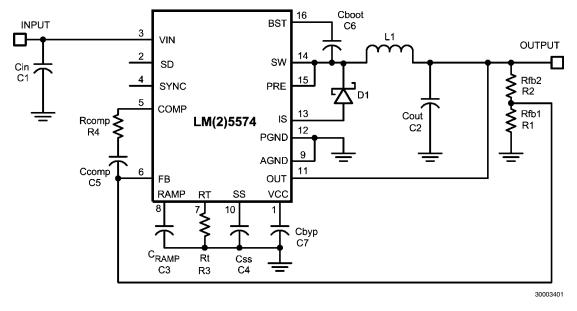


FIGURE 1. Application Schematic

Quick Guide Design Worksheet

Step 1. List the basic requirements:

Output Current: 0 to 0.5 Amp

Output Voltage: V [1]
Input Voltage Min Spec: V [2]
Input Voltage Max Spec: V [3]

Step 2. If the Input Voltage Max Spec [3] is less than 42V use the LM25574. If the Input Voltage Max Spec [3] is greater than 42V but less than 75V use the LM5574. Both devices require the Input Voltage Min Spec [2] to be greater than 6V.

Selected Regulator: [4]

Step 3. Selection of the operating frequency is a trade-off between the conversion efficiency and solution size. Operating at a high frequency, with a relatively high input voltage will severely impact the efficiency and consequently generate a lot of heat. In some applications, the selection of a high op-

erating frequency will limit the input voltage range. The recommended maximum operating frequency for applications using LM5574 is 500 KHz. When using the LM25574 the operating frequency may be set as high as 1MHz. Select a target operating frequency from 50 kHz to 1MHz (500kHz for the LM5574). Check to see if the Vin(min) limits the selected operating frequency:

Fsw(max) =
$$\frac{Vin(min)[2] - (Vout[1] + 0.6)}{Vin(min)[2] \times 5.5 \times 10^{-7}}$$

The selected operating frequency must be less than Fsw (max) calculated above, if not reduce the operating frequency. Check to see if the Vin(max) limits the selected operating frequency:

Fsw(max) =
$$\frac{\text{(Vout[1] + 0.6)}}{\text{Vin(max)[3] x 8 x 10}^{-8}}$$

The selected operating frequency must be less than Fsw (max) calculated above, if not reduce the operating frequency.	Selected diode part number:	[11]	
Selected operating frequency Fsw: Hz [5]	Step 9. A good quality input capacitor(s) is necessary to limit the ripple voltage at the VIN pin while supplying most of the switch current during the on-time. The minimum RMS ripple current rating for the input capacitor(s) is 0.25 Amp. A quality ceramic capacitor with a low ESR is recommended. The input capacitor voltage rating should be greater than the Input Voltage Max Spec [3], plus some safety margin. A guide to select		
Step 4. Calculate the value of Rt for the selected operating frequency.			
$Rt = \frac{\frac{1}{Fsw[5]} - 580 \times 10^{-9}}{135 \times 10^{-12}}$	the input capacitor(s) value in propor quency is:		
135 x 10 ⁻¹²			
	Cin = $\frac{0.25}{\text{Fsw}[5]}$		
Selected value for R1: Ohms [6]	FSW[5	P]	
Step 5. Calculate the value of L1.	Selected value for Cin:	Farads [12]	
L1 = $\frac{\text{Vout}[1] \times (\text{Vin}(\text{max})[3] - \text{Vout}[1])}{0.2 \times \text{Fsw}[6] \times \text{Vin}(\text{max})[3]}$	Step 10. The output capacitor smoothes the inductor ripple current and provide a source of charge for transient loading		
0.2 x Fsw[6] x Vin(max)[3]	conditions. A good choice for the output capacitor is to u		
Select the nearest standard inductor value. During an overload condition the peak inductor current is limited to 0.7A nominal (0.8A maximum). The selected inductor must be rated for peak current of at least 0.8 Amps.	ceramic capacitor (2.2 µF to 47 µF). Ceramic capacitors provides ultra low ESR necessary to reduce the output ripple voltage and noise spikes. The output capacitor voltage rating should be greater than the Output Voltage Spec [1], plus some safety margin. An approximation for the output ripple		
Selected value of L1: Henrys [7]	voltage is:		
Step 6. Calculate the value of C _{RAMP} :	$\Delta Vout = 0.2 x \left(ESR + \frac{1}{8 x} \right)$	1 Fsw[5] x Cout	
$C_{RAMP} = L1[7] \times 5 \times 10^{-6}$			
Selected value of C _{RAMP} :Farads [8]	Selected value for Cout:	Farads [13]	
Step 7. Set Rfb2 to 5kOhms if Vout[1] is less than or equal to 5 Volts. If Vout[1] is greater than 5V set Rfb2 to 10K Ohms.	Step 11. Ccomp and Rcomp configure the error amplifier gain characteristics to accomplish a stable overall loop gain. One		
Selected value of Rfb2: Ohms [9]	advantage of current mode control i loop with only two feedback compone of Rcomp:		
Calculate the value of Rfb1:	•		
	Rcomp = 2.5 x 10 ⁵ x Rfb1[9] x 0	$\frac{1}{2}$ $\frac{13}{2}$ + $\frac{\frac{1}{2}}{2}$	
$Rfb1 = \frac{1.225 \times Rfb2[9]}{(Vout[1] - 1.225)}$	100mp - 2.0 x 10 x 10 x 10 [0] x 1	³ Vout[1] /	
(Vout[1] – 1.225)	Colocted value of Doomn	Ohma [14]	
Selected value of Rfb1: Ohms [10]	Selected value of Rcomp:	Ohms [14]	
	Calculate the value of Ccomp		
Step 8. Select the re-circulating diode, D1. A Schottky type diode is required for all applications. Ultra-fast diodes are not recommended and may result in damage to the IC due to reverse recovery current transients. The reverse breakdown	$Ccomp = \frac{1}{8 \times 10^3 \times F}$	Rcomp[14]	
rating should be greater than the Input Voltage Max Spec[3].			

rating should be greater than the Input Voltage Max Spec[3], plus some safety margin. For worst case design, assume a short circuit load condition. In this case the diode will carry the output current almost continuously. This current can be as high as 0.8A. Assuming a 0.6V drop across the diode, the maximum diode power dissipation can be as high as 0.5W.

Selected value of Ccomp:

Step 12. Shown in the following table is for your design. Transcribe each value.

Step 12. Shown in the following table is the Bill of Materials for your design. Transcribe each value [#] from worksheet above into the following table. Congratulations, you're done.

Farads [15]

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An SMA case or similar package is recommended.

SCH REF	PART NUMBER	DESCRIPTION	VALUE	WORK SHEET REF
C1 (Cin)		INPUT CAPACITOR		[12]
C2 (Cout)		OUTPUT CAPACITOR		[13]
C3 (CRAMP)		RAMP CAPACITOR		[8]
C4 (Css)	C2012X7R2A103K	CAPACITOR, TDK	0.01µ	
C5 (Ccomp)		COMPENSATION CAP		[15]
C6 (Cboot)	C2012X7R2A223K	CAPACITOR, TDK	0.022µ	
C7 (Cbyp)	C2012X7R1C474M	CAPACITOR, TDK	0.47μ, 16V	
D1		SCHOTTKY DIODE		[11]
L1		INDUCTOR		[7]
R1 (Rfb1)		Feedback RESISTOR		[10]
R2 (Rfb2)		Feedback RESISTOR		[9]
R3 (Rt)		TIMING RESISTOR		[6]
R4 (Rcomp)		COMPENSATION		[14]
		RESISTOR		
U1		REGULATOR,		[4]
		NATIONAL		

Notes

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