

Type	Revised demoboard	Description
ADC12125 series	ADC12125A05-08	ADC12125A05 demo board, both CMOS and UDS
	ADC12125A05-10	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-12	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-14	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-16	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-18	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-20	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-22	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-24	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-26	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-28	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-30	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-32	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12125A05-34	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
ADC12125A05-36	ADC12125A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board	
ADC12130 series	ADC12130A05-08	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-10	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-12	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-14	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-16	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-18	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-20	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-22	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-24	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-26	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-28	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-30	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-32	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
	ADC12130A05-34	ADC12130A05 demo board, CMOS version, SPI, Regulators and CMOS buffer on board
ADC12132 series	ADC12132A05-08	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-10	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-12	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-14	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-16	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-18	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-20	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-22	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-24	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-26	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-28	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-30	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-32	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12132A05-34	ADC12132A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
ADC12135 series	ADC12135A05-08	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-10	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-12	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-14	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-16	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-18	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-20	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-22	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-24	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-26	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-28	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-30	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-32	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12135A05-34	ADC12135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
ADC12138 series	ADC12138A05-08	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-10	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-12	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-14	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-16	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-18	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-20	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-22	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-24	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-26	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-28	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-30	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-32	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC12138A05-34	ADC12138A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors

Type	Revised demoboard	Description
ADC14135 series	ADC14135A05-08	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-10	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-12	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-14	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-16	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-18	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-20	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-22	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-24	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-26	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-28	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-30	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-32	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
	ADC14135A05-34	ADC14135A05 demo board, compliant with Lattis, Altera, Xilinx FPGAs through specific connectors
DAC Demo Boards		



NXP high-speed ADC/DAC selection guide

High-speed ADC/DAC solutions for wideband communication and industrial applications

Available with three different data interfaces (including JESD204A), our high-speed ADC/DAC solutions deliver best-in-class speed, size, and integration.

- High-speed single/dual ADCs**
- Resolution: 8 to 16 bits
 - Sampling rates: 20 to 250 Msps
 - Supply voltages: 1.8 / 3.3 / 5.0 V
 - Serial interface, input buffer, internal V_{ref}
 - JESD204A and other digital interfaces
 - Low power dissipation
 - Excellent SFDR and SNR ratings
 - Packages: HVQFN, QFP, SSOP, LQFP, HTQFN

- High-speed dual DACs**
- Resolution: 10 to 14 bits
 - Sampling rates: 125 to 750 Msps
 - Supply voltages: 1.8 / 3.3 V
 - Low power dissipation
 - Excellent SFDR ratings
 - Interpolation: 2x, 4x, 8x
 - High-speed dual DACs
 - JESD204A and other digital interfaces
 - Packages: HVQFN, HTQFP, LQFP

Many of the world's most creative innovators have benefited from our best-in-class data converters. We now offer that same industry-leading performance to the general market.

These highly competitive ADCs and DACs build on NXP's long heritage of innovation in High-Performance Analog, and join NXP's other leading portfolios, including RF power management, and signal-processing technologies, for consumer and industrial applications.

The ADC family uses either a folding or pipeline architecture to provide best-in-class dynamic performance at the lowest possible power dissipation. There are options that support the high speeds and high bandwidth needed for Flash architecture, versions that provide the low bandwidth/high resolution combination required for Sigma-Delta architectures, and general-purpose options that meet the needs of Success Approximation Register architectures.

Our new single- and dual-channel DACs portfolio comprises some fifty models with resolutions of 10, 11, 12, 14 and 16 bits, optional input buffer, input sample rates of 65, 80, 105, 125 Msps, and low-voltage CMOS, LVDS DDR and JEDEC JESD204A compliant CCV™ digital outputs. Typical performance ranges from 64 dBc SFDR at Fin = 170 MHz and Fclk = 125 Msps input sample rate.



www.nxp.com
 © 2016 NXP
 All rights reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.
 Information presented in this document does not form part of any contract or agreement. A trademark is hereby acknowledged and may be changed without notice. No liability will be accepted by the publisher for any consequences of its use. Publication thereof does not constitute an implied or explicit warranty or representation of any kind.



