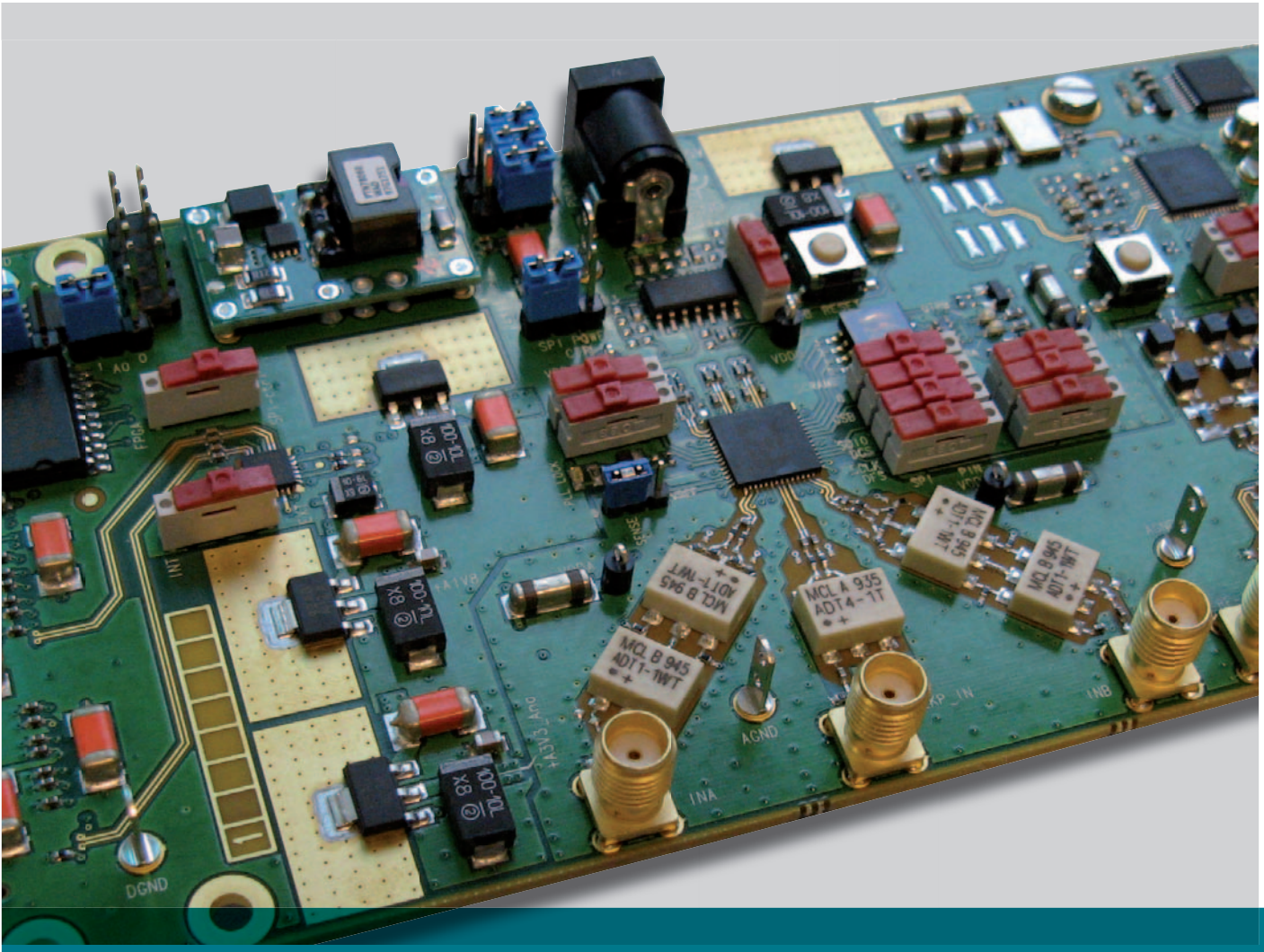
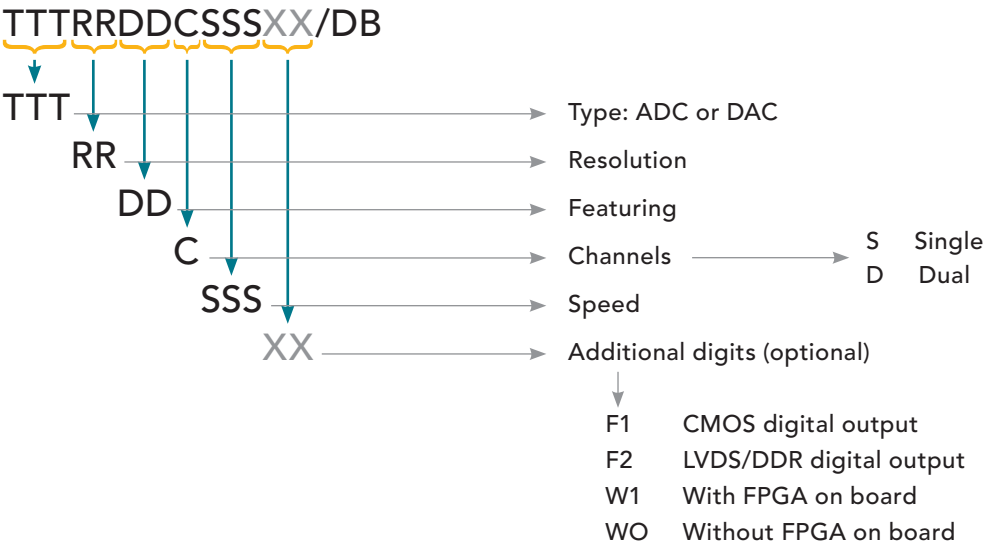


Demoboards for high-speed converters

Naming convention for demoboards

- Root name follows the ADC/DAC naming convention
- Demoboard name ends with /DB
- Additional digits are used to differentiate multiple boards for a single IC



Example:

Standard IC: ADC1413D125
ADC, 14 bits, dual channel, 125 Msps

Demoboard: ADC1413D125W1/DB
ADC1413D125 demoboard, with FPGA

Accessories naming convention

Selection guide: ADC demoboards

ADC product	CMOS I/O	LVDS/ DDR I/0	CGV	FPGA	Optional accessories	Optional extension module	Page
ADC1410S series	•	•			HSDC-ACC09/DB	•	6
	•					•	7
		•			HSDC-ACC05/DB	•	7
ADC1210S series	•	•			HSDC-ACC09/DB	•	6
	•					•	7
		•			HSDC-ACC05/DB	•	7
ADC1010S series	•	•			HSDC-ACC09/DB	•	6
	•					•	7
		•			HSDC-ACC05/DB	•	7
ADC1610S series	•					•	8
		•			HSDC-ACC05/DB	•	8
ADC1415S series	•	•			HSDC-ACC09/DB	•	9
	•					•	10
		•			HSDC-ACC05/DB	•	10
ADC1215S series	•	•			HSDC-ACC09/DB	•	9
	•					•	10
		•			HSDC-ACC05/DB	•	10
ADC1115S125	•	•			HSDC-ACC09/DB	•	9
	•					•	10
		•			HSDC-ACC05/DB	•	10
ADC1015S series	•	•			HSDC-ACC09/DB	•	9
	•					•	10
		•			HSDC-ACC05/DB	•	10
ADC1412D series	•				HSDC-ACC09/DB	•	11
		•			HSDC-ACC05/DB HSDC-ACC09/DB	•	11
ADC1212D series	•				HSDC-ACC09/DB	•	11
		•			HSDC-ACC05/DB HSDC-ACC09/DB	•	11
ADC1112D125	•				HSDC-ACC09/DB	•	11
		•			HSDC-ACC05/DB HSDC-ACC09/DB	•	11
ADC1613D series			•	•			12
			•		HSDC-ACC01/DB HSDC-ACC02/DB		13
ADC1413D series			•	•			12
			•		HSDC-ACC01/DB HSDC-ACC02/DB		13
ADC1213D series			•	•			12
			•		HSDC-ACC01/DB HSDC-ACC02/DB		13
ADC1113D125			•	•			12
			•		HSDC-ACC01/DB HSDC-ACC02/DB		13
ADC1207S080	•				HSDC-ACC08/DB	•	14
ADC1206S series	•				HSDC-ACC07/DB	•	15
ADC1006S series	•				HSDC-ACC07/DB	•	15
ADC1005S060	•				HSDC-ACC06/DB	•	16
ADC1004S series	•				HSDC-ACC06/DB	•	17
ADC1003S series	•				HSDC-ACC06/DB	•	18
ADC1002S020	•				HSDC-ACC07/DB	•	19
ADC0808S series	•				HSDC-ACC06/DB	•	20
ADC0804S series	•						21
ADC0801S040	•						22

Selection guide: DAC demoboards

DAC product	CMOS I/O	LVDS/ DDR I/0	CGV	FPGA	Optional accessories	Optional extension module	Page
DAC1408D series			•	•			23
			•		HSDC-ACC10/DB HSDC-ACC11/DB		24
DAC1208D series			•	•			23
			•		HSDC-ACC10/DB HSDC-ACC11/DB		24
DAC1008D series			•	•			23
			•		HSDC-ACC10/DB HSDC-ACC11/DB		24
DAC1405D650 DAC1205D650 DAC1005D650	•					•	25
DAC1405D750 DAC1205D750 DAC1005D750	•					•	26
DAC1403D160 DAC1203D160 DAC1003D160	•					•	27
DAC1401D125 DAC1201D125 DAC1001D125	•					•	28

Modules and accessories

Product	Page
Promotional demoboard, ADC1413D + Lattice ECP3	29
Extension module	30
Accessories	31

Additional information can be found on **www.nxp.com/dataconverters** and in the Quickstart guide.

Customer support: **dataconverter-support@nxp.com**

JESD204A CGV

Fast track your design

CGV™ (Convertisseur Grande Vitesse) designates NXP's compliant, superset implementation of the JEDEC JESD204A interface standard, with enhanced rate (4.0 Gbps typical), enhanced reach (100 cm typical), enhanced features (multiple DAC synchronization) and assured FPGA interoperability. Specifically, NXP offers enhancements in terms of transceiver rate (up to 4.0 Gbps versus the standard rate of 3.125 Gbps, a 28% increase), and transmitter reach (up to 100 cm versus the standard reach of 20 cm, a 400% increase). The enhanced CGV features include Multi Device Synchronization (MDS) for the DAC1408D series of D/A Converters, which is not specified, but discussed in the JEDEC specification. NXP has implemented this optional feature to enable LTE MIMO basestations and other advanced multi-channel applications. NXP's implementation of MDS enables up to sixteen DAC data streams to be sample synchronized and phase coherent.

ADC1410S series
ADC1210S series
ADC1010S series

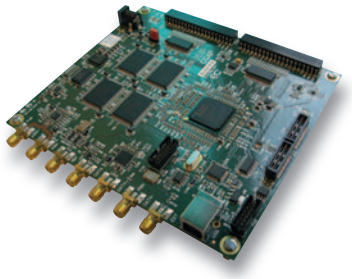
Demoboards

Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals
- CMOS (up to 10 pF load) or LVDS DDR Outputs

Demo box contents

USB to SPI adaptor, USB cable, supply connectors, CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)



Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

Supply adaptor 5 V to 1.8/3/5 V (**HSDC-ACC09/DB**)

Ordering information

Contact us at dataconverter-support@nxp.com

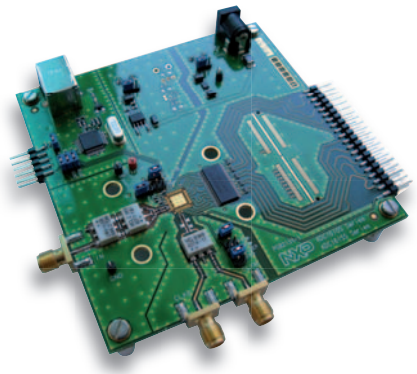
Product names

ADC1410S125/DB 12NC: 935289449598	ADC1210S125/DB 12NC: 935291275598	ADC1010S125/DB 12NC: 935291343598
ADC1410S105/DB 12NC: 935289448598	ADC1210S105/DB 12NC: 935291274598	ADC1010S105/DB 12NC: 935291342598
ADC1410S080/DB 12NC: 935289447598	ADC1210S080/DB 12NC: 935291272598	ADC1010S080/DB 12NC: 935291341598
ADC1410S065/DB 12NC: 935289446598	ADC1210S065/DB 12NC: 935291271598	ADC1010S065/DB 12NC: 935291339598

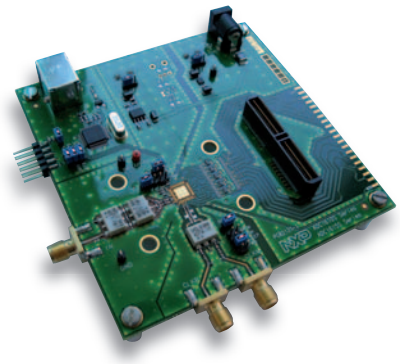
ADC1410S series
ADC1210S series
ADC1010S series

Demoboards

New version: Q3 2010



CMOS digital outputs (F1/DB)



LVDS/DDR digital outputs (F2/DB)

Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS or LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals
- CMOS (buffered outputs) - F1/DB reference
- LVDS/DDR outputs - F2/DB reference
- Single power supply (on-board regulators)

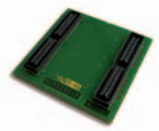
Demo box contents

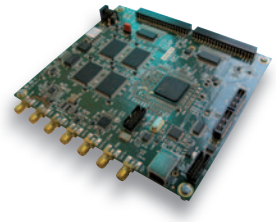
USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Data-acquisition for ADC (HSDC-EXTMOD01/DB)

- With CMOS output mode
- With LVDS/DDR digital outputs
  and bridge accessory (**HSDC-ACC05/DB**)



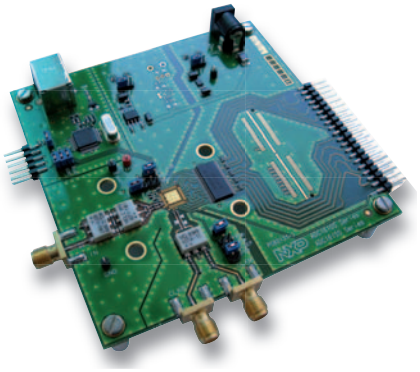


Ordering information

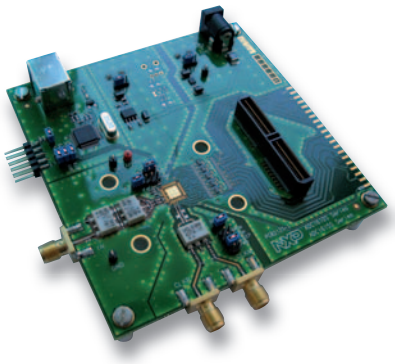
Contact us at dataconverter-support@nxp.com

Product names

ADC1410S125F1/DB	ADC1410S080F2/DB	ADC1210S125F2/DB	ADC1010S080F1/DB
ADC1410S105F1/DB	ADC1410S065F2/DB	ADC1210S105F2/DB	ADC1010S065F1/DB
ADC1410S080F1/DB	ADC1210S125F1/DB	ADC1210S080F2/DB	ADC1010S125F2/DB
ADC1410S065F1/DB	ADC1210S105F1/DB	ADC1210S065F2/DB	ADC1010S105F2/DB
ADC1410S125F2/DB	ADC1210S080F1/DB	ADC1010S125F1/DB	ADC1010S080F2/DB
ADC1410S105F2/DB	ADC1210S065F1/DB	ADC1010S105F1/DB	ADC1010S065F2/DB



CMOS digital outputs (F1/DB)



LVDS/DDR digital outputs (F2/DB)

Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS or LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals
- CMOS (buffered outputs) - F1/DB reference
- LVDS/DDR outputs - F2/DB reference
- Single power supply (on-board regulators)

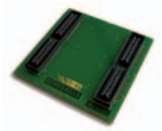
Demo box contents

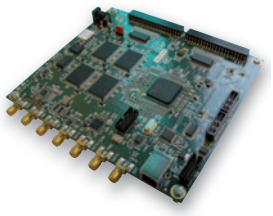
USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Data-acquisition for ADC (HSDC-EXTMOD01/DB)

- With CMOS output mode
- With LVDS/DDR digital outputs
  and bridge accessory (**HSDC-ACC05/DB**)





Ordering information

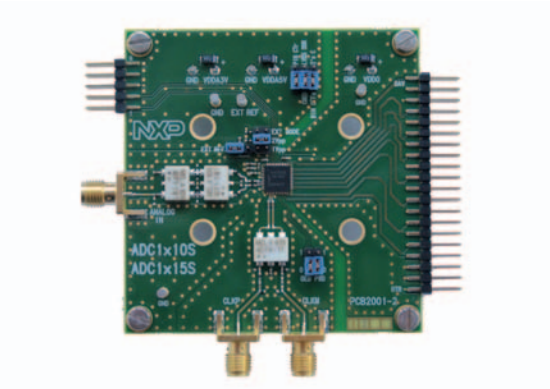
Contact us at dataconverter-support@nxp.com

Product names

ADC1610S125F1/DB 12NC: 935292527598	ADC1610S080F1/DB 12NC: 935292525598	ADC1610S125F2/DB 12NC: 935292528598	ADC1610S080F2/DB 12NC: 935292531598
ADC1610S105F1/DB 12NC: 935292526598	ADC1610S065F1/DB 12NC: 935292524598	ADC1610S105F2/DB 12NC: 935292529598	ADC1610S065F2/DB 12NC: 935292532598

ADC1415S series
ADC1215S series
ADC1015S series
ADC1115S125

Demoboards

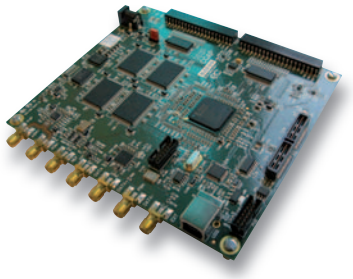


Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals
- CMOS (up to 10 pF load) or LVDS DDR Outputs

Demo box contents

USB to SPI adaptor, USB cable, supply connectors, CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)



Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

Supply adaptor 5 V to 1.8/3/5 V (**HSDC-ACC09/DB**)

Ordering information

Contact us at dataconverter-support@nxp.com

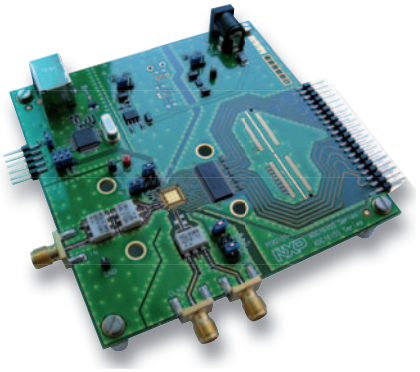
Product names

ADC1415S125/DB 12NC: 935289445598	ADC1215S125/DB 12NC: 935291347598	ADC1015S125/DB 12NC: 935291194598	ADC1115S125/DB 12NC: 935291267598
ADC1415S105/DB 12NC: 935289444598	ADC1215S105/DB 12NC: 935291346598	ADC1015S105/DB 12NC: 935291193598	
ADC1415S080/DB 12NC: 935289442598	ADC1215S080/DB 12NC: 935291345598	ADC1015S080/DB 12NC: 935291192598	
ADC1415S065/DB 12NC: 935289441598	ADC1215S065/DB 12NC: 935291344598	ADC1015S065/DB 12NC: 935291191598	

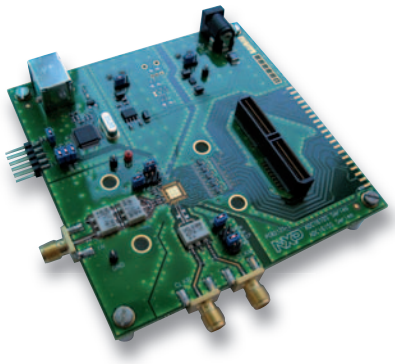
ADC1415S series
ADC1215S series
ADC1015S series

Demoboards

New version: Q3 2010



CMOS digital outputs (F1/DB)



LVDS/DDR digital outputs (F2/DB)

Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS or LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals
- CMOS (buffered outputs) - F1/DB reference
- LVDS/DDR outputs - F2/DB reference
- Single power supply (on-board regulators)

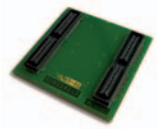
Demo box contents

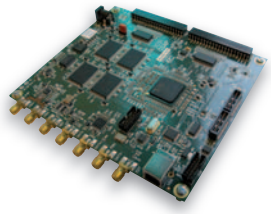
USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Data-acquisition for ADC (HSDC-EXTMOD01/DB)

- With CMOS output mode
- With LVDS/DDR digital outputs
  and bridge accessory (**HSDC-ACC05/DB**)





Ordering information

Contact us at dataconverter-support@nxp.com

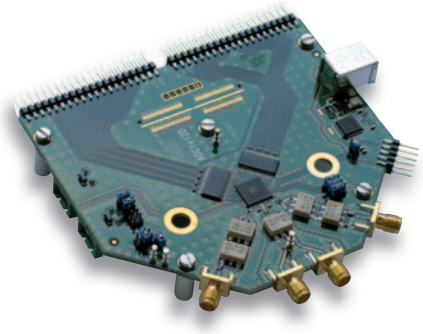
Product names

ADC1415S125F1/DB	ADC1415S080F2/DB	ADC1215S125F2/DB	ADC1015S080F1/DB
ADC1415S105F1/DB	ADC1415S065F2/DB	ADC1215S105F2/DB	ADC1015S065F1/DB
ADC1415S080F1/DB	ADC1215S125F1/DB	ADC1215S080F2/DB	ADC1015S125F2/DB
ADC1415S065F1/DB	ADC1215S105F1/DB	ADC1215S065F2/DB	ADC1015S105F2/DB
ADC1415S125F2/DB	ADC1215S080F1/DB	ADC1015S125F1/DB	ADC1015S080F2/DB
ADC1415S105F2/DB	ADC1215S065F1/DB	ADC1015S105F1/DB	ADC1015S065F2/DB

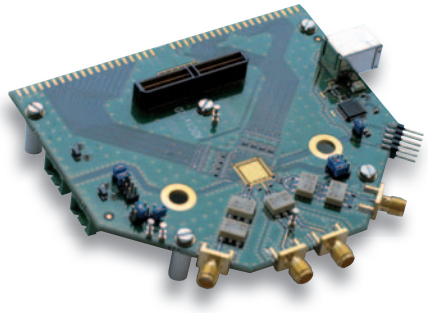
ADC1412D series
ADC1212D series
ADC1112D125

Demoboards

Q3 2010



CMOS digital outputs (F1/DB)



LVDS/DDR digital outputs (F2/DB)

Our dual-channel ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration with LVCMOS or LVDS DDR output variants. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals
- CMOS (buffered outputs) - F1/DB reference
- LVDS/DDR outputs - F2/DB reference

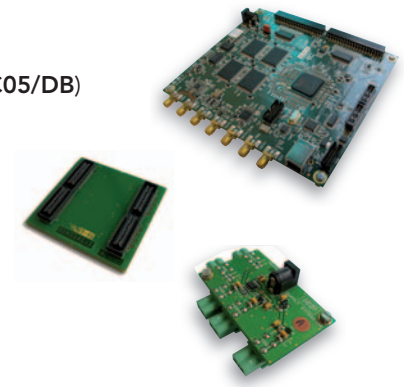
Demo box contents

USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Data-acquisition for ADC (HSDC-EXTMOD01/DB)

- With CMOS output mode
- With LVDS/DDR digital outputs and bridge accessory (**HSDC-ACC05/DB**)



Optional accessories

Supply adaptor 5 V to 1.8/3/5 V (**HSDC-ACC09/DB**)

Ordering information

Contact us at dataconverter-support@nxp.com

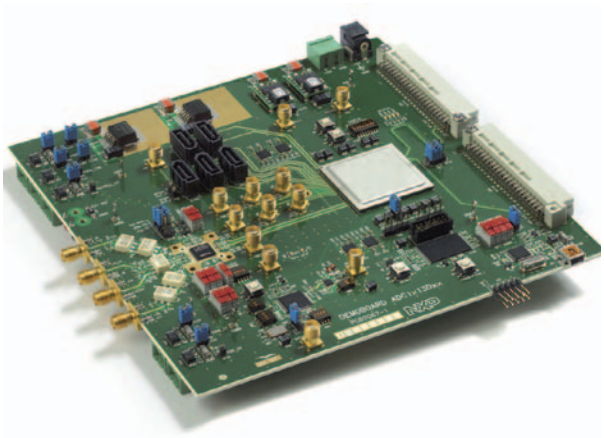
Product names

ADC1412D125F1/DB 12NC: 935292533598	ADC1412D105F2/DB 12NC: 935292544598	ADC1212D080F1/DB 12NC: 935292538598	ADC1212D065F2/DB 12NC: 935292547598
ADC1412D105F1/DB 12NC: 935292534598	ADC1412D080F2/DB 12NC: 935292545598	ADC1212D065F1/DB 12NC: 935292537598	ADC1112D125F1/DB 12NC: 935292542598
ADC1412D080F1/DB 12NC: 935292535598	ADC1412D065F2/DB 12NC: 935292546598	ADC1212D125F2/DB 12NC: 935292551598	ADC1112D125F2/DB 12NC: 935292552598
ADC1412D065F1/DB 12NC: 935292536598	ADC1212D125F1/DB 12NC: 935292541598	ADC1212D105F2/DB 12NC: 935292549598	
ADC1412D125F2/DB 12NC: 935292543598	ADC1212D105F1/DB 12NC: 935292539598	ADC1212D080F2/DB 12NC: 935292548598	

ADC1613D series
ADC1413D series
ADC1213D series
ADC1113D125

Demoboards

With FPGA



Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204A feature set.

- SMA connector for clock and analog input signals
- On-board Data clock generator for ADC and FPGA
- On-board Virtex 5 FPGA for data-acquisition
- Single power supply (on-board regulators)

Demo box contents

USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)

Ordering information

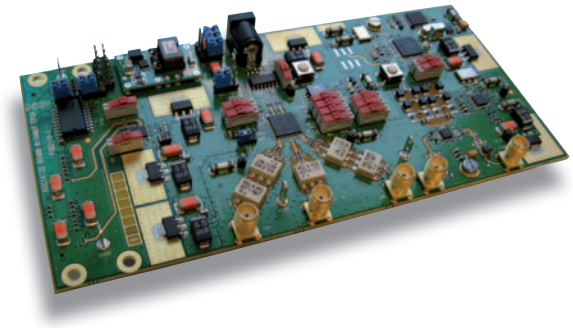
Contact us at dataconverter-support@nxp.com

Product names

ADC1613D125W1/DB	ADC1413D125W1/DB 12NC: 935291352598	ADC1213D125W1/DB 12NC: 935291286598	ADC1113D125W1/DB 12NC: 935291266598
ADC1613D105W1/DB	ADC1413D105W1/DB 12NC: 935291351598	ADC1213D105W1/DB 12NC: 935291285598	
ADC1613D080W1/DB	ADC1413D080W1/DB 12NC: 935291349598	ADC1213D080W1/DB 12NC: 935291284598	
ADC1613D065W1/DB	ADC1413D065W1/DB 12NC: 935291348598	ADC1213D065W1/DB 12NC: 935291283598	

ADC1613D series
ADC1413D series
ADC1213D series
ADC1113D125

Demoboards



Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. An FPGA mother board (Xilinx, Altera, or Lattice) can be connected to ease the evaluation and analysis of the ADC dynamic and enable use of the full JESD204A features set.

- SMA connector for clock and analog input signals
- On-board Data clock generator
- On-board demodulator + LO synthesizer
- Single power supply (on-board regulators)

Demo box contents

USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, Quickstart guide)

Optional accessories

Connection adaptor (FMC connector) to Xilinx FPGA development boards (HSDC-ACC01/DB)
Connection adaptor (SMA connectors) to Lattice FPGA development boards (HSDC-ACC02/DB)

Note: connection to Altera FPGA development board (HSMC connector) is default

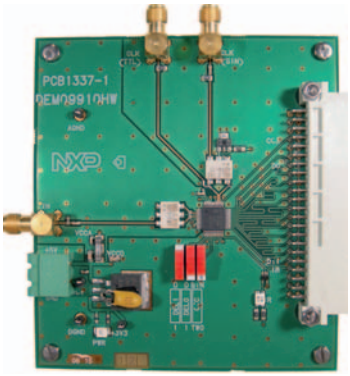
Ordering information

Contact us at dataconverter-support@nxp.com

Product names

ADC1613D125WO/DB	ADC1413D125WO/DB 12NC: 935290417598	ADC1213D125WO/DB 12NC: 935290425598	ADC1113D125WO/DB 12NC: 935291265598
ADC1613D105WO/DB	ADC1413D105WO/DB 12NC: 935290421598	ADC1213D105WO/DB 12NC: 935290424598	
ADC1613D080WO/DB	ADC1413D080WO/DB 12NC: 935290419598	ADC1213D080WO/DB 12NC: 935290423598	
ADC1613D065WO/DB	ADC1413D065WO/DB 12NC: 935290418598	ADC1213D065WO/DB 12NC: 935290422598	

ADC1207S080 Demonstration board

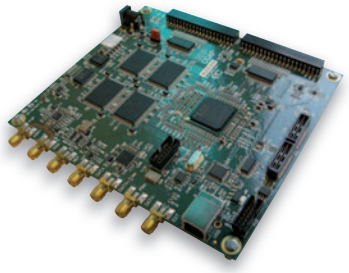


Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)

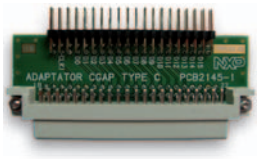


Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

Adaptor Type C for HSDC EXTMOD01/DB and ADC1207S080/DB (**HSDC-ACC08/DB**)



Ordering information

Contact us at dataconverter-support@nxp.com

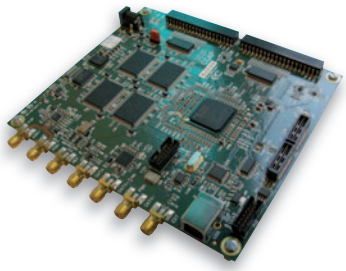
Product name

ADC1207S080/DB
12NC: 935288918598

- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)

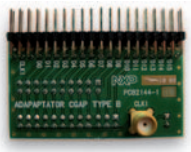


Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

Adaptor Type B for HSDC EXTMOD01/DB and ADC1206S070/DB, ADC1206S055/DB, ADC1206S040/DB, ADC1006S055/DB, ADC1206S070/DB (**HSDC-ACC07/DB**)



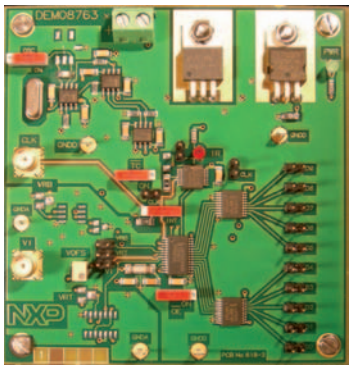
Ordering information

Contact us at dataconverter-support@nxp.com

Product names

ADC1206S070/DB 12NC: 935288917598	ADC1206S055/DB 12NC: 935288916598	ADC1206S040/DB 12NC: 935288915598
ADC1006S070/DB 12NC: 935288914598	ADC1006S055/DB 12NC: 935288913598	

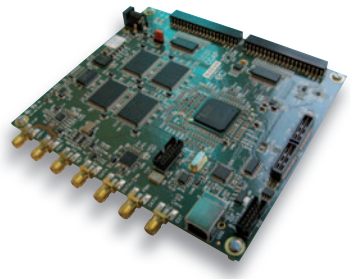
ADC1005S060 Demonstration board



- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)



Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

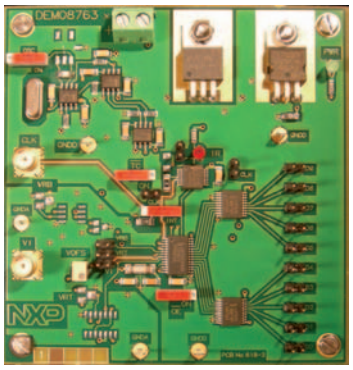
Adaptor Type A for HSDC EXTMOD01/DB and ADC1005S060/DB (**HSDC-ACC06/DB**)

Ordering information

Contact us at dataconverter-support@nxp.com

Product name

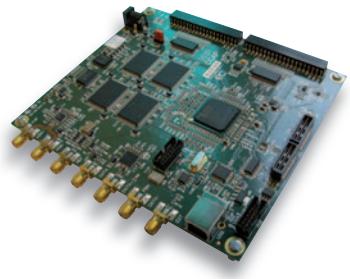
ADC1005S060/DB
12NC: 935288912598



- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)



Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

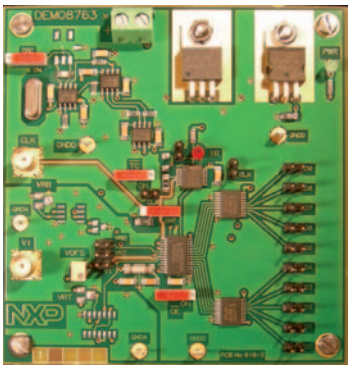
Adaptor Type A for HSDC EXTMOD01/DB and ADC1004S050/DB, ADC1004S040/DB, ADC1004S030/DB (**HSDC-ACC06/DB**)

Ordering information

Contact us at dataconverter-support@nxp.com

Product names

ADC1004S050/DB 12NC: 935288911598	ADC1004S040/DB 12NC: 935288909598	ADC1004S030/DB 12NC: 935288908598



- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

Adaptor Type A for HSDC EXTMOD01/DB and ADC1003S050/DB, ADC1003S040/DB, ADC1003S030/DB (**HSDC-ACC06/DB**)

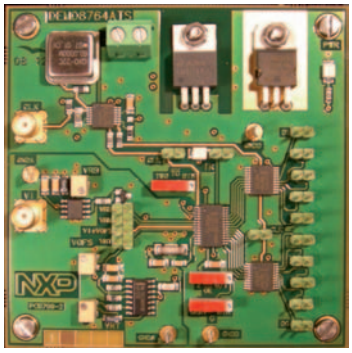
Ordering information

Contact us at dataconverter-support@nxp.com

Product names

ADC1003S050/DB 12NC: 935288907598	ADC1003S040/DB 12NC: 935288906598	ADC1003S030/DB 12NC: 935288905598

ADC1002S020 Demonstration board



- SMA connector for clock and analog input signals

Demo box contents

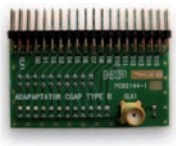
CDROM (datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

Adaptor Type B for HSDC EXTMOD01/DB and ADC1002S020/DB (**HSDC-ACC07/DB**)



Ordering information

Contact us at dataconverter-support@nxp.com

Product name

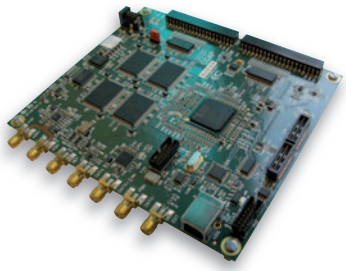
ADC1002S020/DB
12NC: 935288904598

Our ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. A data-acquisition board can be used during design and prototype to analyze ADC performance.

- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)



Extension module

Data-acquisition for ADC with CMOS output mode (HSDC-EXTMOD01/DB)

Optional accessories

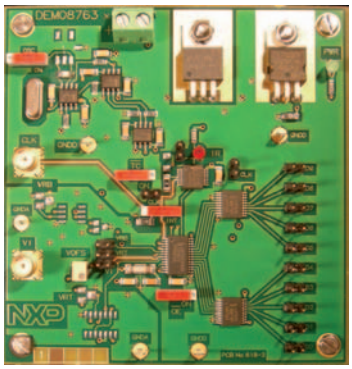
Adaptor Type A for HSDC EXTMOD01/DB and ADC0808S125/DB, ADC0808S250/DB (**HSDC-ACC06/DB**)

Ordering information

Contact us at dataconverter-support@nxp.com

Product names

ADC0808S125/DB 12NC: 935288902598	ADC0808S250/DB 12NC: 935288903598



- SMA connector for clock and analog input signals

Demo box contents

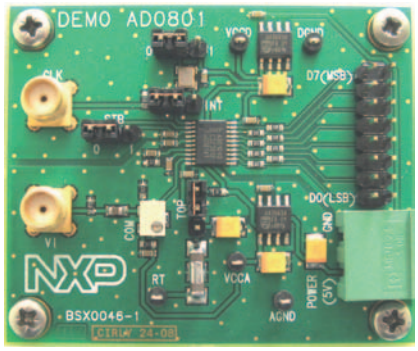
CDROM (datasheets, PCB layout/schematics, Quickstart guide)

Ordering information

Contact us at dataconverter-support@nxp.com

Product names

ADC0804S050/DB 12NC: 935288901598	ADC0804S040/DB 12NC: 935288899598	ADC0804S030/DB 12NC: 935288898598



- SMA connector for clock and analog input signals

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)

Ordering information

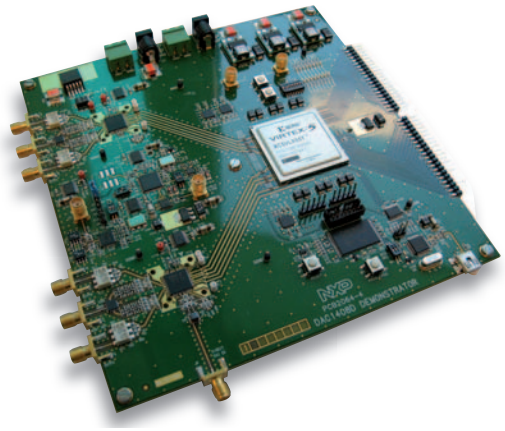
Contact us at dataconvereter-support@nxp.com

Product name

ADC0801S040/DB
12NC: 935288897598

DAC1408D series
DAC1208D series
DAC1008D series

Demoboards
Q3 2010
With FPGA



Our DAC demoboard is suitable for dynamic performance evaluations from low to high outputs frequency configuration. The FPGA makes it easy to evaluate and analyze the DAC's dynamic performance and enables use of the full JESD204A feature set (e.g. Multiple DAC Synchronization)

- SW interface to easily program DAC parameters (NCO, offset, interpolation factor, PLL, etc.)
- On-board AQM + LO synthesizer
- SMA connector for DAC and AQM external clock input signals
- On-board clock generator for DAC and FPGA
- On-board Virtex 5 FPGA for data generation:
  - Single/ multi-tone for SFDR evaluation
  - Test pattern generation for ACLR measurements (2/4 carriers WCDMA, TDSCDMA, GSM-MC, etc.)
  - Various clocks for DAC & AQM
- 2 DACs on same PCB to easily evaluate Multi-DAC synchronization
- Single power supply (on-board regulators)

Demo box contents

USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, drivers, Quickstart guide)

Ordering information

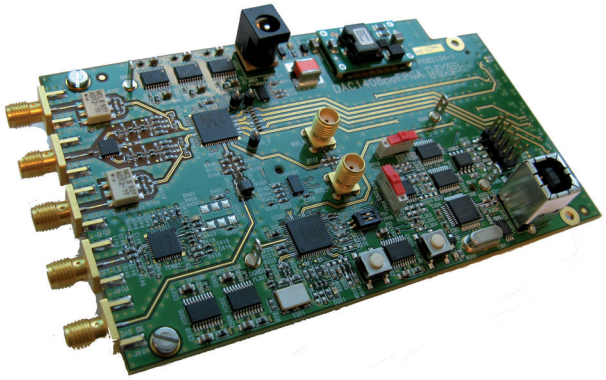
Contact us at dataconverter-support@nxp.com

Product names

DAC1408D650W1/DB 12NC: 935292559598	DAC1208D650W1/DB	DAC1008D650W1/DB
DAC1408D750W1/DB 12NC: 935292561598	DAC1208D750W1/DB	DAC1008D750W1/DB

DAC1408D series
DAC1208D series
DAC1008D series

Demoboards
Q3 2010

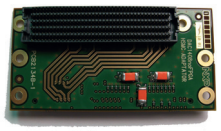


Our DAC demoboard is suitable for dynamic performance evaluations from low to high outputs frequency configuration. An FPGA mother board (Xilinx, Altera, or Lattice) can be connected to ease the evaluation and analysis of the DAC dynamic and enable use of the full JESD204A full feature set.

- SW interface to easily program DAC parameters (NCO, offset, interpolation factor, PLL, etc.)
- On-board AQM TRF3703-17+ LO synthesizer
- On-board clock generator
- Single power supply (on-board regulators)

Demo box contents

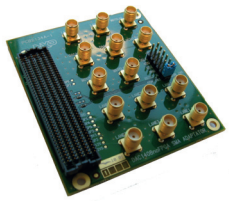
USB cable, power supply adaptor,
CDROM (SPI software, datasheets, PCB layout/schematics, drivers, Quickstart guide)



Optional accessories

Connection adaptor (HSMC connector) to Altera FPGA development boards (**HSDC-ACC10/DB**)
Connection adaptor (SMA connectors) to Lattice FPGA development boards (**HSDC-ACC11/DB**)

Note: connection to Xilinx FPGA development boards (FMC connector) is default



Ordering information

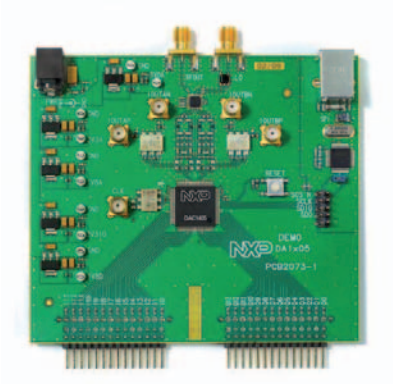
Contact us at dataconverter-support@nxp.com

Product names

DAC1408D650WO/DB 12NC: 935292563598	DAC1208D650WO/DB	DAC1008D650WO/DB
DAC1408D750WO/DB 12NC: 935292562598	DAC1208D750WO/DB	DAC1008D750WO/DB

DAC1405D650
DAC1205D650
DAC1005D650

Demoboards

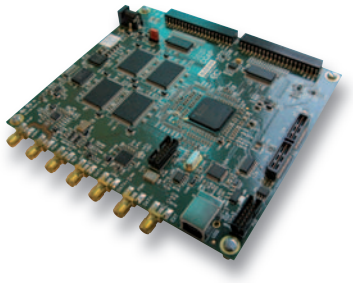


Our DAC demoboard is suitable for dynamic performance evaluations from low to high output frequency configurations. A data-generation board can be used during design and prototype to generate various input test patterns and ease DAC performance analysis.

- SW interface for easy programming of DAC parameters (NCO, offset, interpolation factor, etc.)
- 3rd order DAC output filters
- On-board AQM ADL5372
- SMA connector for DAC and AQM external clock input signalss
- LVCMOS 1.8 or 3.3 V digital inputs
- Single power supply

Demo box contents

USB cable, power supply adaptor, CDROM (SPI software, datasheets, PCB layout/schematics, drivers, Quickstart guide)



Extension module

Clock generator; pattern generator for DAC
(HSDC-EXTMOD01/DB)

Ordering information

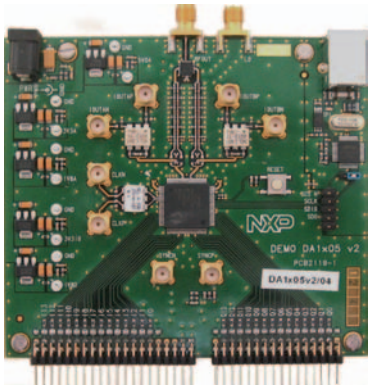
Contact us at dataconverter-support@nxp.com

Product names

DAC1405D650/DB 12NC: 935288928598	DAC1205D650/DB 12NC: 935288927598	DAC1005D650/DB 12NC: 935288926598

DAC1405D750
DAC1205D750
DAC1005D750

Demoboards

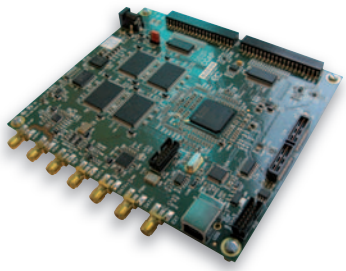


Our DAC demoboard is suitable for dynamic performance evaluations from low to high output frequency configurations. A data-generation board can be used during design and prototype to generate various input test patterns and ease DAC performance analysis.

- SW interface for easy programming of DAC parameters (NCO, offset, interpolation factor, etc.)
- 3rd order DAC output filters
- On-board AQM ADL5375 (or TRF3703 on request)
- SMA connector for DAC and AQM external clock input signals
- LVCMOS 1.8 or 3.3 V digital inputs
- Single power supply

Demo box contents

USB cable, power supply adaptor, CDROM (SPI software, datasheets, PCB layout/schematics, drivers, Quickstart guide)



Extension module

Clock generator; pattern generator for DAC (HSDC-EXTMOD01/DB)

Ordering information

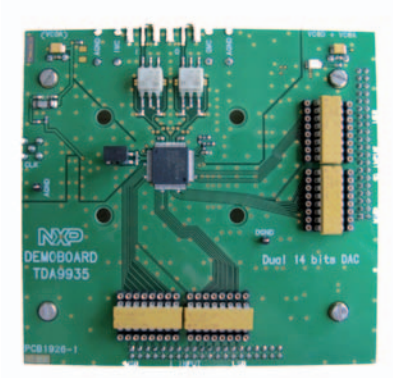
Contact us at dataconverter-support@nxp.com

Product names

DAC1405D750/DB	DAC1205D750/DB	DAC1005D750/DB

DAC1403D160
DAC1203D160
DAC1003D160

Demoboards



Our DAC demoboard is suitable for dynamic performance evaluations from low to high output frequency configurations. A data-generation board can be used during design and prototype to generate various input test patterns and ease DAC performance analysis.

- SMA connector for DAC analog outputs and clock input signals
- LVCMOS 1.8 or 3.3 V digital inputs

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)

Extension module

Clock generator; pattern generator for DAC
(HSDC-EXTMOD01/DB)

Ordering information

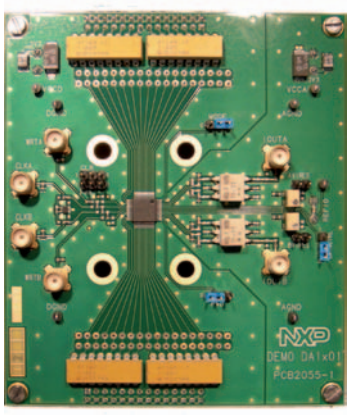
Contact us at dataconverter-support@nxp.com

Product names

DAC1403D160/DB 12NC: 935288925598	DAC1203D160/DB 12NC: 935288923598	DAC1003D160/DB 12NC: 935288921598

DAC1401D125
DAC1201D125
DAC1001D125

Demoboards

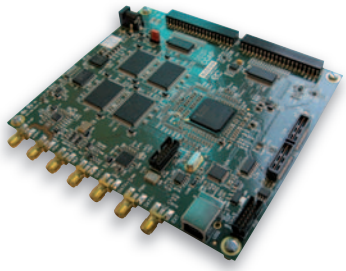


Our DAC demoboard is suitable for dynamic performance evaluations from low to high output frequency configurations. A data-generation board can be used during design and prototype to generate various input test patterns and ease DAC performance analysis.

- SMA connector for DAC analog outputs and clock input signalss
- LVCMOS 1.8 or 3.3 V digital inputs
- Single power supply

Demo box contents

CDROM (datasheets, PCB layout/schematics, Quickstart guide)



Extension module

Clock generator; pattern generator for DAC
(HSDC-EXTMOD01/DB)

Ordering information

Contact us at dataconverter-support@nxp.com

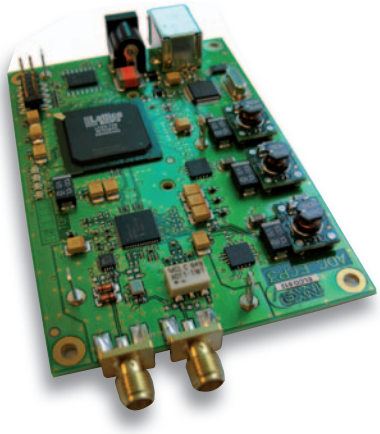
Product names

DAC1401D125/DB 12NC: 935288924598	DAC1201D125/DB 12NC: 935288922598	DAC1001D125/DB 12NC: 935288919598

ADC1413D series plus Lattice ECP3

Promotional board

Q3 2010



Our ADC promotional board, equipped with an on-board Lattice ECP3-70 FPGA, enables use of the full JESD204A feature set. It also enables dynamic performance evaluations of a one-channel ADC with analog inputs up to 30 MHz.

- USB-powered demoboard
- 66.66 MHz on-board oscillator for ADC sample clock
- Optional external ADC sample clock (via SMA connector)
- Access to one ADC channel

Demo box contents

USB cable, CDROM (SPI software, datasheets, PCB layout/schematics, drivers, Quickstart guide)

Optional accessories

Any DAC demoboard for analog input frequency generation

Ordering information

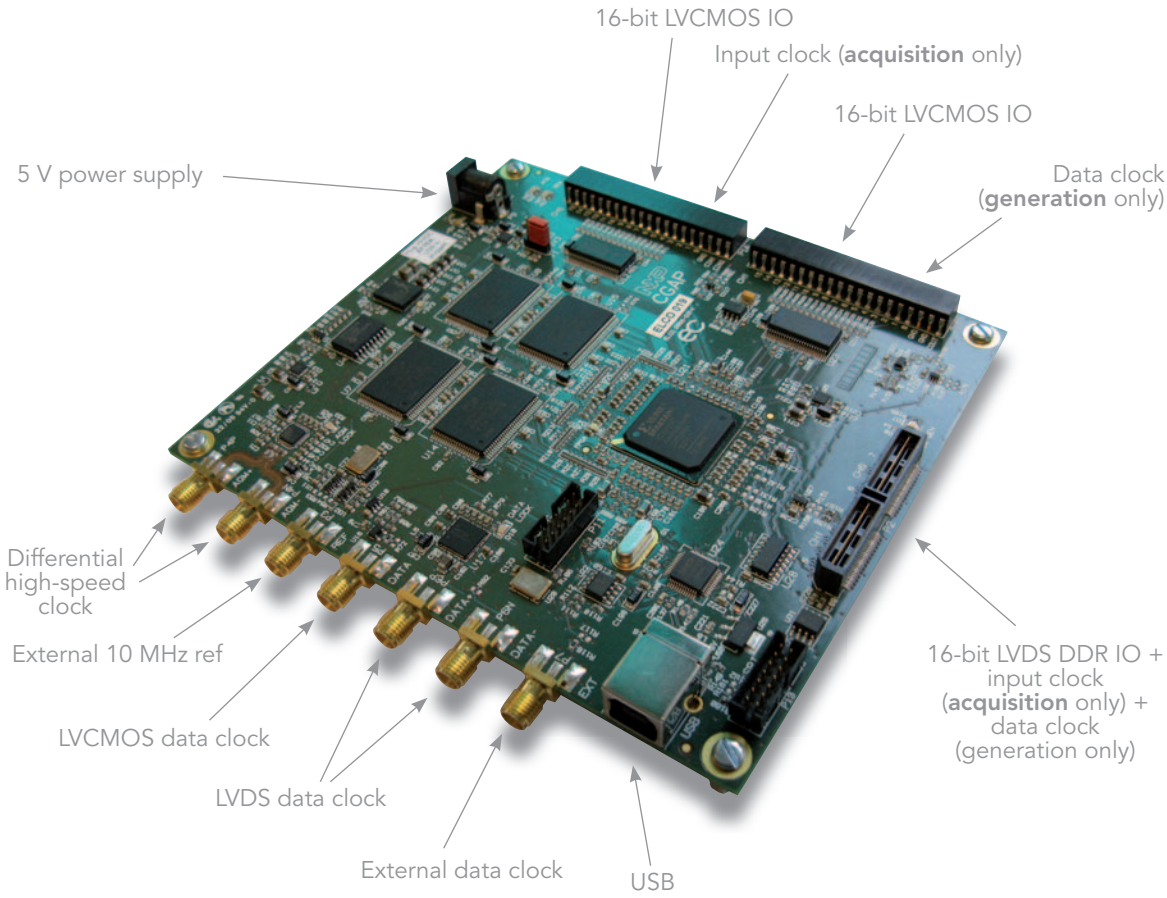
Contact us at dataconverter-support@nxp.com

Product names

ADC1413D080W2/DB 12NC: 935291942598	ADC1213D080W2/DB 12NC: 935291941598

Clock generator
Pattern generator for DAC
Ddata-acquisition for ADC

Extension module



Demo box contents

USB cable, power supply adaptor
CDROM (SPI software, datasheets, PCB layout/schematics, drivers, Quickstart guide)

Ordering information

Contact us at dataconverter-support@nxp.com

Product name

HSDC-EXTMOD01/DB
12NC: 935291353598

HSDC-ACC01/DB

Connection adaptor (Xilinx FMC) for ADC JESD204A product demo boards (with WO references only) with Xilinx development boards (Spartan and Virtex)

HSDC-ACC02/DB

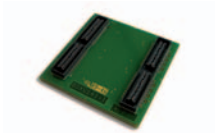
Connection adaptor (SMA connectors) for ADC JESD204A product demo boards (with WO references only) with Lattice development boards (ECP2M and EPC3)

HSDC-ACC04/DB

USB to SPI adaptor for ADC1410S, ADC1210S, ADC1010S series demo boards and ADC1415S, ADC1215S, ADC1115S, ADC1015S series demo boards
Included in related Demoboxes

HSDC-ACC05/DB

Bridge adaptor for HSDC EXTMOD01/DB and ADC demoboards with LVDS output



HSDC-ACC06/DB

Adaptor Type A for HSDC EXTMOD01/DB and ADC0808S125/DB, ADC0808S250/DB, ADC1005S060/DB, ADC1004S050/DB, ADC1004S040/DB, ADC1004S030/DB, ADC1003S050/DB, ADC1003S040/DB, ADC1003S030/DB

HSDC-ACC07/DB

Adaptor Type B for HSDC EXTMOD01/DB and ADC1002S020/DB, ADC1206S070/DB, ADC1206S055/DB, ADC1206S040/DB, ADC1006S055/DB, ADC1206S070/DB, ADC1002S020/DB

HSDC-ACC08/DB

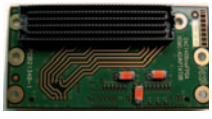
Adaptor Type C for HSDC EXTMOD01/DB and ADC1207S080/DB
(**HSDC-ACC08/DB**)

HSDC-ACC09/DB

Supply adaptor for ADC1410S, ADC1210S, ADC1010S series demo boards and ADC1415S, ADC1215S, ADC1115S, ADC1015S series demo boards and ADC1412D, ADC1212D, ADC1112D series demo boards

HSDC-ACC10/DB

Connection adaptor (Altera HSMC) for DAC JESD204A product demo boards (with WO references only) with Altera development boards (Arria and Stratix)



HSDC-ACC11/DB

Connection adaptor (SMA connectors) for DAC JESD204A product demo boards (with WO references only) with Lattice development boards (ECP2M and EPC3)

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