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**DESCRIPTION:** chassis mount dc-dc converter

# description

PART NUMBER: VHK50W

The VHK50W series of rugged DC-DC converters are designed for high reliability applications, featuring integral heat sink, over-temperature protection, over-current protection and input transient voltage protection. With an external input fuse, the converter can protect accidental input polarity reversal. The wide 4:1 input range (9-36V or 18-75V) is very useful to stabilize an input source like batteries in various discharging and charging conditions. Additionally, high efficiency, fast response, tight regulations, remote sense and remote On/Off control make these converters very useful in many industrial and communications applications.

#### features

- ·25-50W isolated output
- ·Efficiency to 83%
- ·RoHS Compliant
- ·4:1 input range
- Regulated output
- -Continuous short circuit protection
- Metal enclosure with integrated heatsink
- ·Rugged design
- ·Over-temperature shutdown



MODEL	input	voltage	output	output	input	efficiency	
	nominal (V dc)	range (V dc)	voltage (V dc)	current <sup>1</sup> (A)	no load <sup>2</sup> (mA)	full load <sup>2</sup> (mA)	typ. <sup>3</sup> (%)
VHK50W-Q24-S3R3	24	9.0~36.0	3.3	10	50	1833	75
VHK50W-Q24-S5	24	9.0~36.0	5	10	50	2637	79
VHK50W-Q24-S12	24	9.0~36.0	12	4.16	50	2537	82
VHK50W-Q24-S15	24	9.0~36.0	15	3.33	50	2538	82
VHK50W-Q24-S24	24	9.0~36.0	24	2.08	50	2537	82
VHK50W-Q24-S28	24	9.0~36.0	28	1.78	50	2537	82
VHK50W-Q24-S48	24	9.0~36.0	48	1.04	50	2537	82
VHK50W-Q48-S3R3	48	18.0~75.0	3.3	10	50	905	76
VHK50W-Q48-S5	48	18.0~75.0	5	10	50	1302	80
VHK50W-Q48-S12	48	18.0~75.0	12	4.16	50	1267	83
VHK50W-Q48-S15	48	18.0~75.0	15	3.33	50	1265	83
VHK50W-Q48-S24	48	18.0~75.0	24	2.08	50	1264	83
VHK50W-Q48-S28	48	18.0~75.0	28	1.78	50	1264	83
VHK50W-Q48-S48	48	18.0~75.0	48	1.04	50	1253	83

notes:

- 1. see output derating curves (page 4)
- 2. input currents are measured at nominal input voltage
- 3. efficiency is measured at nominal line, full load

### INPUT

parameter	conditions/description	min	nom	max	units
input voltage range		9	24	36	V dc
		18	48	75	V dc
under voltage lockout	24 Vin power up		8.8		V dc
	24 Vin power down		8		V dc
	48 Vin power up		17		V dc
	48 Vin power down		16		V dc
remote on/off control <sup>4</sup>	section 15 in the application notes				
input filter	PI type				

notes

4. add suffix "N" to the model number for negative logic on/off control

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# **OUTPUT**

			±1	%
25% step load change			500	μ sec
			±10	%
3.3V, 5V			40	mV RMS
			100	mV p-p
12V& 15V			60	mV RMS
			150	mV p-p
24V			100	mV RMS
			240	mV p-p
28V			100	mV RMS
			280	mV p-p
48V			200	mV RMS
			480	mV p-p
		±0.03		%/°C
continuous				
			±0.2	%
			±1	%
ge, % Vo nom.	115		140	%
% nominal output current	110		160	%
	3.3V, 5V  12V& 15V  24V  28V  48V  continuous	3.3V, 5V  12V& 15V  24V  28V  48V  continuous  ge, % Vo nom. 115	3.3V, 5V  12V& 15V  24V  28V  48V  ±0.03  continuous	#10 3.3V, 5V 40 100 12V& 15V 60 150 24V 100 240 28V 100 280 48V 200 480  ±0.03  continuous ±1 ge, % Vo nom. 115 140

# **GENERAL SPECIFICATIONS**

parameter	conditions/description	min	nom	max	units	
switching frequency			300		KHz	
operating ambient temp. <sup>7</sup>	derated from 60 to 85°C	-40		85	°C	
storage temperature		-55		105	°C	
thermal shutdown case temp			85		°C	
case material	aluminum / steel					

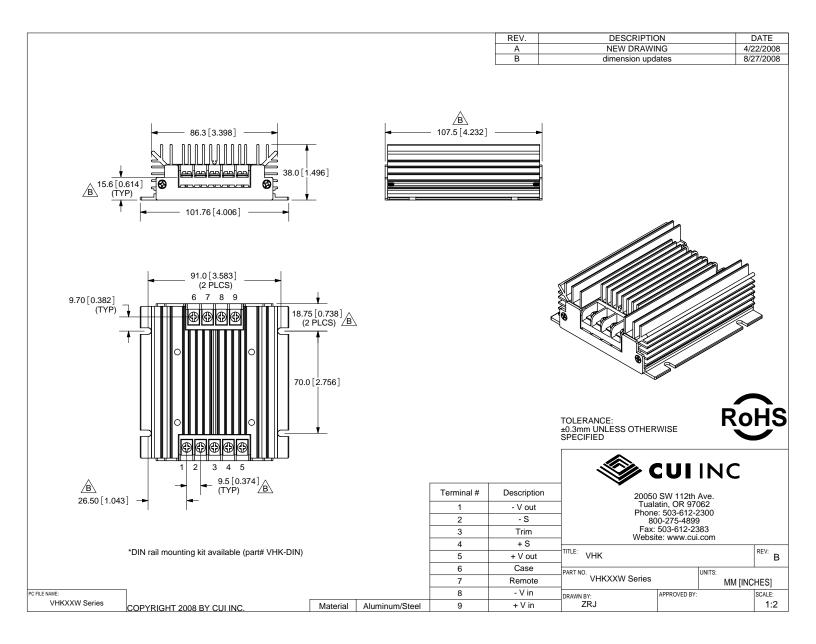
# **ISOLATION SPECIFICATIONS**

parameter	conditions/description	min	nom	max	units
isolation voltage	input/output	1500			V dc
	input/case	1500			V dc
	output/case	1500			V dc
isolation resistance		100			$M\Omega$

notes:

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measured from high line to low line at full load
 measured from full load to zero load at nominal input
 see output derating curves (page 4)





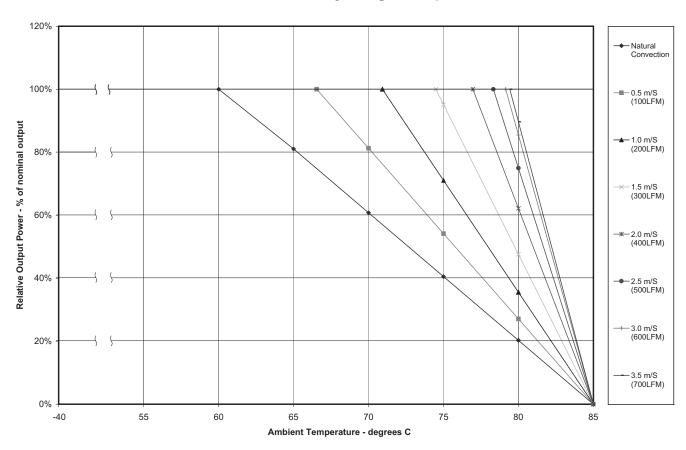
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# APPLICATION NOTES

#### 1. OUTPUT DE-RATING

The operating ambient temperature range for VHK50W is -40 ~ +85°C, with the actual output power subject to the following de-rating curves at free-air convection and at forced air conditions. To ensure long-term reliability, it is important to ensure proper cooling at the worst operating conditions. Please note the derating curves may improve if the converter is mounted on a metal surface to allow heat conduction.



VHK50W Power Derating Curves @ Nominal Input

FIGURE 1. OUTPUT DERATING

#### 2. INPUT VOLTAGE RANGE

It is important to ensure the input voltage measured at the converter input pins is within the range for that converter. Make sure wire losses and voltage ripples are accounted for. One possible problem is driving the converter with a linear unregulated power supply. For example, if the average voltage measured by a DMM is 9V, with a voltage ripple of 3Vpp, the actual input can swing from 7.5V to 10.5V. This will be outside the specified input range of 9-36V and the converter may not function properly. On the other end, make sure the actual input voltage does not exceed the highest voltage of 36V or75V.

#### 3. LEAD WIRES

Make sure the input and output wires are of adequate AWG size to minimize voltage drop, and ensure the voltage across the input terminals is above the converter's rated minimum voltage at all times. It is recommended to have the wire pairs twisted, respectively for the input pair and the output pair, so as to minimize noise pickup.

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#### 4. INPUT CURRENT

The input voltage source must be able to provide enough current to the converter, otherwise it may not start up or operate properly. A typical symptom is not starting or unusually low output voltage. In general, it is recommended to be able to provide at least: **Ipeak = 150%\*Pout/(\eta\*Vmin)** where Pout is the maximum output power, Vmin is the minimum input voltage and  $\eta$  is the converter's efficiency. As an example, for VHK50W-Q24-S5 to operate with 9~36V input, 50W output and an efficiency of 81%, the minimum source current is recommended to be: **Ipeak = 150% \* 50 / (81% \* 9) = 10.29A**.

#### 5. INPUT FUSE

To limit the input current and to facilitate input reversal protection and input OVP protection, a fast-acting input fuse is recommended for the input line. The fuse rating will depend on the input range and should allow for the maximum current at the lowest input voltage, as shown in this equation: **Ipeak = 150%\*Pout/(n\*Vmin)**.

In the previous example of VHK50W-Q24-S5, the peak input current at 9V was calculated to be 10.29A. A 10A or a 15A fuse may be suitable for this application. Make sure the fuse voltage rating is higher than the maximum input voltage.

#### 6. INPUT OVP

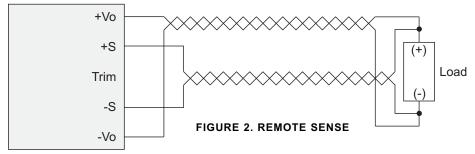
It is important to ensure the input voltage does not exceed the maximum rated input voltage for that model. To suppress voltage transients of short durations, the converter includes a transient voltage suppression device (TVS) at the input. The built-in TVS has a rated breakdown voltage of 39V for the VHK50W-Q24-XX models and 82V for the VHK50W-Q48 models. With small transients of short-durations, the TVS will limit the input voltage without interrupting the converters operations. For large transients, the TVS may conduct a large amount of current that may trip the input fuse. Without the input fuse, the converter may suffer permanent damages. When the fuse is open, replace it with one of same type and ratings.

#### 7. INPUT REVERSAL PROTECTION

If the input voltage is reversed for any reason, the built-in protection circuits in the converter will limit the reverse voltage to one diode drop which is no more than 1V. With an external input fuse connected, the fuse will open and thus remove power from the converter. Check the wiring and make corrections as needed. The input fuse will need to be replaced. Make sure the new fuse is of the same type and rating. Without the input fuse, the converter may suffer permanent damages in a reversal situation.

# 8. REMOTE SENSE

The converter provides regulated outputs at the output terminals. When there is a large current and/or the output cable is of some length, the voltage at the end of the output cable may be noticeably lower than at the terminals. The converter can compensate up to 0.5V of voltage drop through remote sense terminals. To ensure accurate regulation, run two separate wires (twisted) from the desired regulation points to the remote sense terminals, as shown below. Even if the load current is low, still connect +Vo to +S and -Vo to -S.



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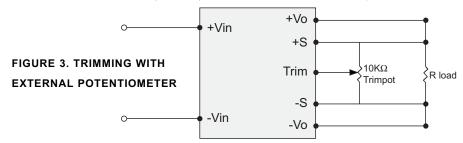
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#### 9. OUTPUT TRIMMING (OPTIONAL)

The output voltages are preset to nominal values as indicated by the models table at the factory. If desired, the output voltage may optionally be trimmed to a different value (+/- 10%) with external resistors and/or potentiometer as shown below.



To trim the output voltage with fixed resistors, the output voltage can be calculated as follows.

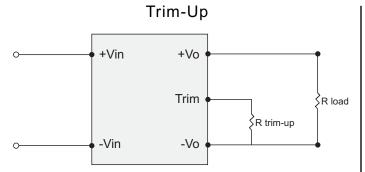


FIGURE 4: TRIM-UP VOLTAGE SETUP

The value of  $R_{trim-up}$  is defined as:

$$R_{trim-up} = \frac{R1 - R2 x (Vo-Vo, nom)}{Vo-Vo, nom} (K\Omega)$$

Where:  $R_{trim-up}$  is the external resistor in  $K\Omega$ .  $V_{o,nom}$  is the nominal output voltage.  $V_o$  is the desired output voltage.  $R_1$ ,  $R_2$ ,  $R_3$ ,  $V_r$ , and  $V_f$  are internal to the unit and are defined in Table 1. For example, to trim-up the output voltage of 5.0V module (VHK50W-Q48-S5) by 8% to 5.4V,  $R_{trim-up}$  is calculated

as follows:  

$$Vo - Vo, nom = 5.4 - 5.0 = 0.4 \text{ V}$$
  
 $RI = 5.8 \text{ K}\Omega$   
 $R2 = 3.3 \text{ K}\Omega$   
 $Rtrim-up = \frac{5.8 - 3.3 \times 0.4}{0.4} = 11.2 \text{ (K}\Omega)$ 

Output Voltage(V) | R1(K $\Omega$ ) | R2(K $\Omega$ )

3.3V 3.168 7.2 5V 5.8 3.3 12V 18.945 4.636 15V 25.189 7.191 Table 1 24V 25.189 6.977 28V 608.6 173.7 48V 74.25 13.3

Trim-Down

+Vin +Vo
R trim-down

-Vin -Vo

FIGURE 5: TRIM-DOWN VOLTAGE SETUP

The value of  $R_{trim-down}$  is defined as:

$$R_{trim-down} = \frac{R1 - R2 \times (Vo, nom - Vo)}{Vo, nom - Vo} (K\Omega)$$

Where:  $R_{trim-down}$  is the external resistor in  $K\Omega$ .  $V_{o,nom}$  is the nominal output voltage.  $V_o$  is the desired output voltage.  $R_1$ ,

 $R_2$ , and  $V_r$  are internal to the unit and are defined in Table 1. For example, to trim-up the output voltage of 5.0V module (VHK50W-Q48-S5) by 8% to 4.6V,  $R_{trim-down}$  is calculated

as follows:  
Vo, nom - Vo = 5.0 - 4.6 = 0.4 V  

$$RI = 5.8 \text{ K}\Omega$$
  
 $R2 = 5.32 \text{ K}\Omega$   
 $Rtrim-down = \frac{5.8 - 5.32 \times 0.4}{0.4} = 9.18 \text{ (K}\Omega)$ 

Output Voltage(V) R1(KΩ)  $R2(K\Omega)$ 3.3V 6.18 15 5V 5.8 5.32 12V 86.45 60.1 15V 150 Table 2 68 24V 430 120 28V 608.6 173.7 1638 306

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#### 10. OUTPUT OCP AND SHORT-CIRCUIT PROTECTION

Output overload and short circuit conditional will cause the output voltage to decline or shutdown altogether. If the case temperature is not over 85°C, the output recovers automatically when the short or OCP conditions are removed. In the case of slight overloading, the output voltage may not shut down, but the converter may build up heat over time, causing over-temperature shutdown.

### 11. OVER-TEMPERATURE PROTECTION

When the case temperature reaches about 85°C, the converter's built-in protection circuit will shut down the output. When the temperature is reduced enough to a safe operating level, the converter will recover to normal operations automatically.

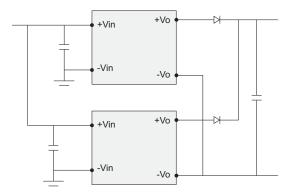
#### 12. OUTPUT OVP

In case the output voltage exceeds the OVP threshold, the converter shuts down.

#### 13. OUTPUT PARALLEL CONNECTIONS

The converter is not designed for load share on the output. One may be inclined to use this circuit to force current sharing by trimming the output voltages for each converter. However, this circuit me not reliably or accurately divide the load current, as the device characteristics of the converters or the diodes may not be balanced over a range of operating conditions. We generally do not recommend this circuit to increase power output over a single converter. Instead, we recommend this setup for redundancy only, having one converter as a backup in case of a failure. Make sure the OR-ing diodes can handle the voltage and full load current.

FIGURE 6. OUTPUT
PARALLEL CONNECTIONS





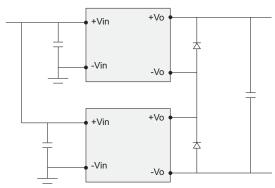
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#### 14. OUTPUT SERIES CONNECTIONS

Two or more converters can be connected in series to obtain a higher output voltage. To prevent output reverse biasing each other in case of a short, add a Schottky diode on each output in reverse polarity, as shown in the diagram. In the event of a short, the converters will forward-bias the diodes and the output reversal will be limited to one diode drop (about 0.5V) only, so as not to damage the converters. The forward current will cause each converter to go into short-circuit protection. For proper diode selection, make sure that: 1) the diodes voltage rating is higher than each converter output; 2) the rated diode current can carry the short-circuit current; 3) the diodes do not overheat before the short is removed.

FIGURE 7. OUTPUT SERIES CONNECTIONS



#### 15. REMOTE OUTPUT ON/OFF CONTROL

The converter output can be enabled or disabled through the On/Off pin. The control logic is shown in this table. A common control circuit is shown below. The standard version defaults to positive logic. For negative logic, indicate the selection when ordering.

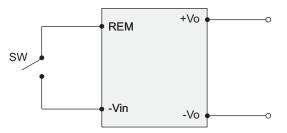


FIGURE 8. REMOTE ON/OFF CONTROL

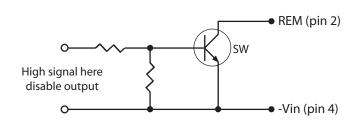


FIGURE 9. REMOTE ON/OFF CONTROL WITH TRANSISTOR SWITCH

Logic Table	Negative logic	Positive logic
SW Closed (VREM < 0.8 V)	Output on	Output off
SW Open (VREM >2.4 V)	Output off	Output on

#### 16. ISOLATION

The input and output of the converter are electrically isolated. If needed, an output terminal can be connected to an input terminal, resulting in the converter non-isolated.



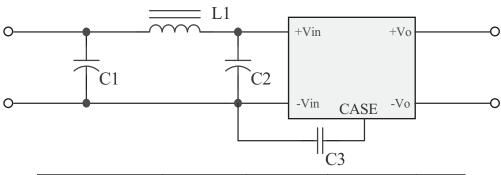
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### 17. INPUT FILTERING AND EMI INTERFERENCE

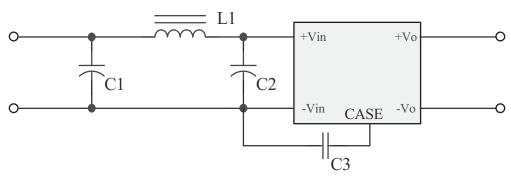
The VHK50W converters have input capacitors that control input current ripple and the associated EMI interference. However, it has not been tested to a formal standard for conducted emissions. Additional filtering may be needed to ensure compliance to an EMI standard. Refer to FIGURE 10.1 and 10.2 for reference circuits.

FIGURE 10.1. EMI FILTER FOR VHK50W, 3.3 ~ 24 V



VHK50W Series	C2	C3	C4	L1
EMI to meet class A	47µF/50V	47µF/50V	2200pF/2KV	2.5µH
	ESR<0.60	ESR<0.60		
EMI to meet class B	220µF/50V	220µF/50V	2200pF/2KV	1.5uH
LIVII to meet class b	ESR<0.120	ESR<0.120		

FIGURE 10.2A. EMI FILTER FOR VHK50W, 28 V (CLASS A)



Model No.	C1	C2	C3	L1
VHK50W-Q24-S28	100uF/50V KY	100uF/50V KY	2200pF/2KV	3.4uH
VHK50W-Q48-S28	100uF/100V KY	100uF/100V KY	2200pF/2KV	3.4uH

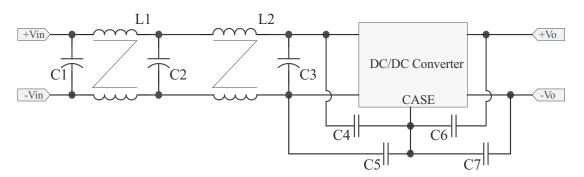
note: The recommended components are a starting point <u>but not guaranteed</u> for meeting these EMI requirements. More or less filtering may be required for a specific application or to meet other EMI standards, e.g.MIL-STD-461



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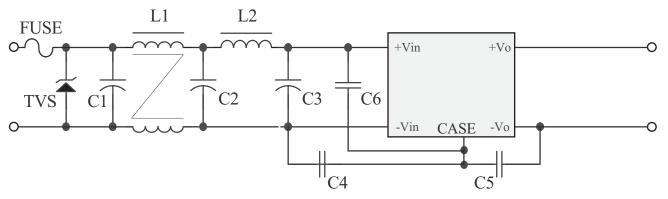
# FIGURE 10.2B. EMI FILTER FOR VHK50W, 28 V (CLASS B)



## Note:

Model No.	C1	C2	C3	C4	C5	C6	C7	L1	L2
VHK50W-Q24-S28	100uF/50V KY	100uF/50V KY	NC	NC	1000pF/2KV	NC	NC	0.12mH	0.34mH
VHK50W-Q48-S28	100uF/100V KY	100uF/100V KY	NC	NC	1000pF/2KV	NC	NC	0.12mH	0.34mH

# FIGURE 10.3. EMI FILTER FOR VHK50W, 48 V



EMI to meet class A	C1	C2	C3	C4	C5	C6	L1	L2
VHK50W-Q24-S48	NC	100uF/50V ESR<0.33Ω	100uF/50V ESR<0.33Ω	I NIC	NC	NC	Short	3.5uH
VHK50W-Q48-S48	NC	47uF/100V KMF	47uF/100V KMF	2200pF/2KV	NC	NC	Short	3.5uH
EMI to meet class B	C1	C2	C3	C4	C5	C6	L1	L2
VHK50W-Q24-S48	10uF/50V X7R,2220 Ceramic	100uF/50V ESR<0.33Ω	100uF/50V ESR<0.33Ω	4700pF/2KV	2200pF/2KV	1000pF/2KV	1.5mH	3.4uH
VHK50W-Q48-S48	47uF/100V KMF	47uF/100V KMF	47uF/100V KMF	4700pF/2KV	2200pF/2KV	1000pF/2KV	1.5mH	3.4uH

note: The recommended components are a starting point <u>but not guaranteed</u> for meeting these EMI requirements. More or less filtering may be required for a specific application or to meet other EMI standards, e.g.MIL-STD-461



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### 18. OUTPUT FILTERING

The converter has built-in capacitors at the output to control ripple and noise. If desired, additional output capacitance can be introduced at the input to the powered equipment. Recommended starting point: one 10µF tantalum and one 1µF ceramic capacitor in parallel at the output.

#### 19. OUTPUT NOISE MEASUREMENT SCHEMATIC

For proper output ripple and noise measurement, connect a  $10\mu F$  tantalum and a  $1\mu F$  ceramic capacitor across the output. Set the scope bandwidth to 20MHz. Probe directly off of one of the capacitors, using a small ground clip to minimize measurement error.

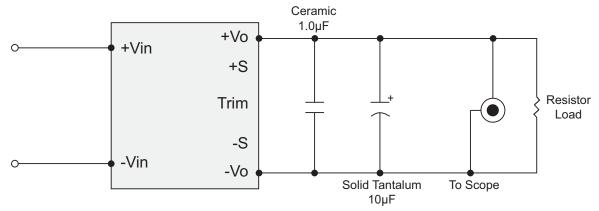


FIGURE 11. OUTPUT NOISE MEASUREMENT CIRCUIT