

NON-ISOLATED DC/DC CONVERTERS

4.5 Vdc - 14 Vdc Input

0.75 Vdc - 3.63 Vdc/10 A Output

bel
POWER PRODUCTS

VRBC-10E2Ax

RoHS Compliant

Rev.A

- Non-Isolated
- High Efficiency
- High Power Density
- Excellent Thermal Performance
- Low Cost
- Flexible Output Voltage Sequencing (Option)
- Remote Sense
- Wide Input
- Wide Trim
- OCP/SCP
- Remote On/Off
- Active Low/High (Option)
- Over Temperature Protection
- Under-voltage Lockout (UVLO)
- Industrial Temperature Range



Description

The Bel VRBC-10E2Ax is part of the non-isolate dc/dc power converter series. The modules use a SIP package. These converters are available in a range of output voltages from 0.75 Vdc to 3.63 Vdc over a wide range of input voltage ($V_{in} = 4.5 \text{ Vdc} - 14 \text{ Vdc}$). The Bel VRBC-10E2Ax has a sequencing feature that enables designers to implement various types of output voltage sequencing when powering. The efficiency is typically 94.3% at 3.3 V output at 5.0 Vdc input at full load.

Part Selection

Output Voltage	Input Voltage	Max. Output Current	Max. Output Power	Typical Efficiency	Model Number Active Low	Model Number Active High
0.75 V- 3.63 V	4.5 V - 14 V	10 A	36.3 W	94.3%	VRBC-10E2AL	VRBC-10E2A0

- Notes:** 1. Add "G" suffix at the end of the model number to indicate Tray Packaging.
2. All part numbers above indicate RoHS 6. Change the second letter "R" to "7" for RoHS 5 part numbers.

Absolute Maximum Ratings

Parameter	Min	Typ	Max	Notes
Input Voltage (continuous)	-0.3 V	-	15 V	
Output Enable Terminal Voltage	-0.3 V	-	15 V	
Sequencing Voltage ¹	-0.3 V	-	V_{in}	
Ambient Temperature	-40 °C	-	85 °C	
Storage Temperature	-55 °C	-	125 °C	

- Notes:** All specifications are typical at 25 °C unless otherwise stated.
1. VRBC-10E2Ax series of modules include a sequencing feature that enables users to implement various types of output voltage sequencing in their applications. This is accomplished via an additional sequencing pin. When not using the sequencing feature, either tie the SEQ pin to V_{in} or leave it unconnected.

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Input Specifications

Parameter	Min	Typ	Max	Notes
Input Voltage				
$V_{o,set} < 3.0\text{ V}$	4.5 V	-	14 V	
$V_{o,set} \geq 3.0\text{ V}$	$V_{o,set} + 1.5\text{ V}$	-	14 V	
Input Current (full load)	-	-	8.6 A	An input line fuse must always be used.
Input Current (no load)	-	40 mA	-	
Remote Off Input Current	-	2 mA	-	
Input Reflected Ripple Current (pk-pk)	-	-	400 mA	Tested with one 1000 $\mu\text{F}/25\text{ V}$ AL input capacitor with ESR=0.03 ohm max and 4 \times 47 $\mu\text{F}/16\text{ V}$ Tantalum capacitors with ESR=0.013 ohm max at 100 kHz, & simulated source impedance of 1000 nH, 5 Hz to 20 MHz.
Input Reflected Ripple Current (rms)	-	-	150 mA	
I^2t Inrush Current Transient	-	0.2 A^2s	0.4 A^2s	
Turn-on Voltage Threshold	-	4.3 V	-	
Turn-off Voltage Threshold	-	4.0 V	-	

Note: All specifications are typical at nominal input, full load at 25 °C unless otherwise stated.

Output Specifications

Parameter	Min	Typ	Max	Notes
Output Voltage Set Point	-2% $V_{o,set}$	-	2% $V_{o,set}$	$V_{in}=5\text{ V}$ & 12 V, full load
Load Regulation	-	0.1% $V_{o,set}$	-	
Line Regulation	-	0.1% $V_{o,set}$	-	
Regulation Over Temperature (-40°C to +85°C)	-	0.3 $V_{o,set}$	-	$T_{ref}=T_{amin}$ to T_{amax}
Output Current	0 A	-	10 A	
Current Limit Threshold	-	200% I_{out}	-	
Short Circuit Surge Transient	-	1 A^2s	3 A^2s	
Ripple and Noise (pk-pk)	-	30 mV	80 mV	Tested with 0-20 MHz, with 10 μF tantalum capacitor & 1 μF ceramic capacitor at the output
Ripple and Noise (rms)	-	12 mV	35 mV	
Turn on Time	-	8 mS	20 mS	
Overshoot at Turn on	-	0%	1%	
Output Capacitance	0 μF	-	5600 μF	
Transient Response				
50% ~ 100% Max Load	$V_O = 0.75\text{ V} - 3.63\text{ V}$	-	160 mV	di/dt=2.5 A/ μS ; $V_{in}=5\text{ V}$ & 12 V; and with 470 μF Tantalum capacitor at the output
Settling Time		-	50 μS	
100% ~ 50% Max Load		-	160 mV	
Settling Time		-	50 μS	

Note: All specifications are typical at nominal input, full load at 25 °C unless otherwise stated.

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General Specifications

Parameter	Min	Typ	Max	Notes
Efficiency Vo=3.3 V Vo=2.5 V Vo=1.8 V Vo=1.5 V Vo=1.2 V Vo=0.75 V	- - - - - -	94.3% 93% 91.5% 90.8% 89.3% 83%	- - - - - -	Measured at Vin=5 V, full load
Efficiency Vo=3.3 V Vo=2.5 V Vo=1.8 V Vo=1.5 V Vo=1.2 V Vo=0.75 V	- - - - - -	93% 92% 90% 89% 87.5% 81%	- - - - - -	Measured at Vin=12 V, full load
Switching Frequency	265 kHz	300 kHz	335 kHz	
Over Temperature Shutdown	-	130°C	-	
Output Voltage Trim Range	0.7525 V	-	3.63 V	
Remote Sense Compensation	-	-	0.5 V	
MTBF	5,114,191 hours			Calculated Per Bell Core TR-332 (Io = 80%Iomax; Vo=3.3 V; Vin=12 V; Ta = 25 °C)
Dimensions Inches (L x W x H) Millimeters (L x W x H)	2.0 x 0.5 x 0.32 50.8 x 12.7 x 8.13			
Weight	-	7.1 g	-	

Note: All specifications are typical at 25 °C unless otherwise stated.

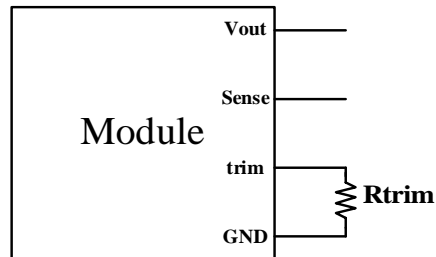
Control Specifications

Parameter	Min	Typ	Max	Notes
Remote On/Off				
Signal Low (Unit Off)	-0.2 V	-	0.3 V	VRBC-10E2A0; Remote On/Off pin open, Unit on.
Signal High (Unit On)	-	-	Vin, max	
Signal Low (Unit On)	-0.2 V	-	0.3 V	VRBC-10E2AL; Remote On/Off pin open, Unit on.
Signal High (Unit Off)	2.5 V	-	Vin, max	
Voltage Sequencing				
Sequencing Delay Time	25 mS	-	-	Delay from Vinmin to application of voltage on SEQ pin
Sequencing Slew Rate Capability	-	-	2 V/mS	Vinmin to Vinmax; Iomin to Iomax; Vseq<Vo
Tracking Accuracy Power-Up Power-Down	- -	100 mV 300 mV	200 mV 500 mV	

Output Trim Equations

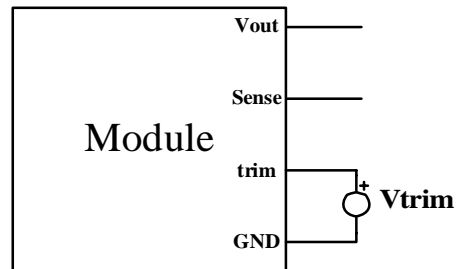
Equation for calculating the trim resistor (in Ω) given the desired output voltage (V_o) is shown below. The Trim Up resistor should be connected between the Trim pin and Ground.

$$R_{trim} = \frac{10500}{V_o - 0.7525} - 1000$$



Equation for calculating the trim voltage (in V) given the desired output voltage (V_o) is shown below. The Trim Up voltage should be connected between the Trim pin and Ground.

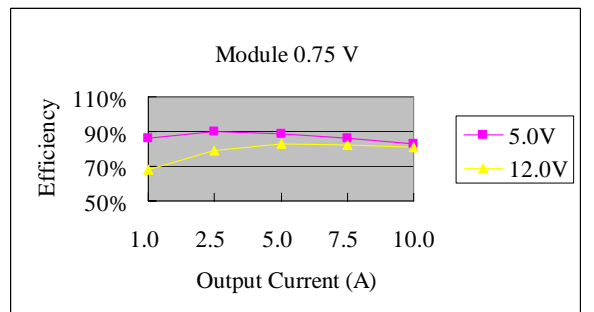
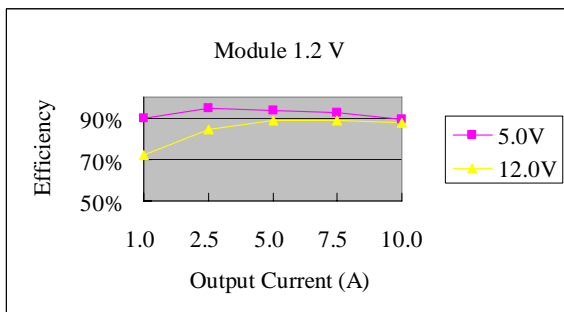
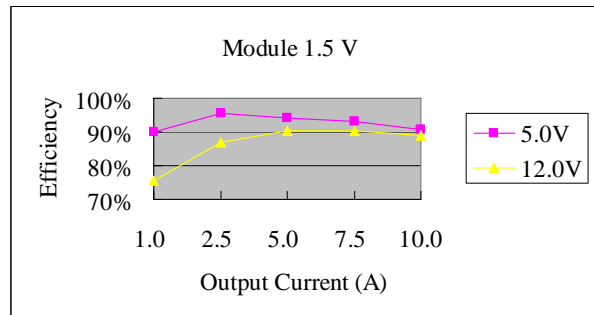
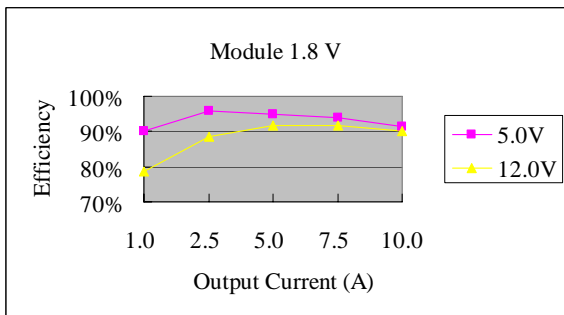
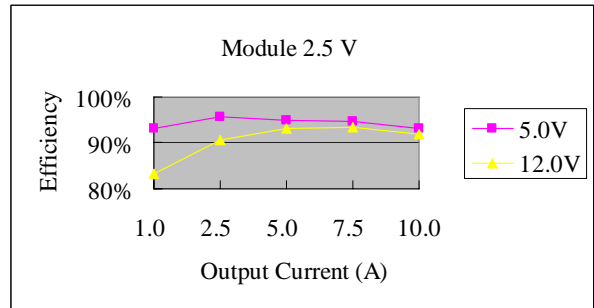
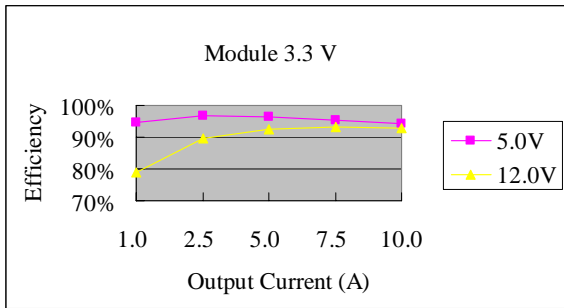
$$V_{trim} = 0.7 - 0.0667 \times (V_o - 0.7525)$$



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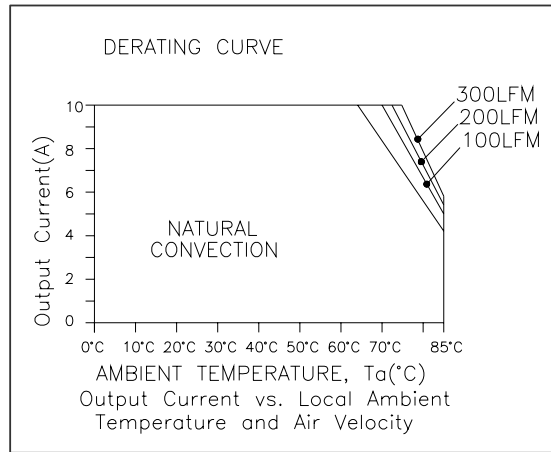
Efficiency Data



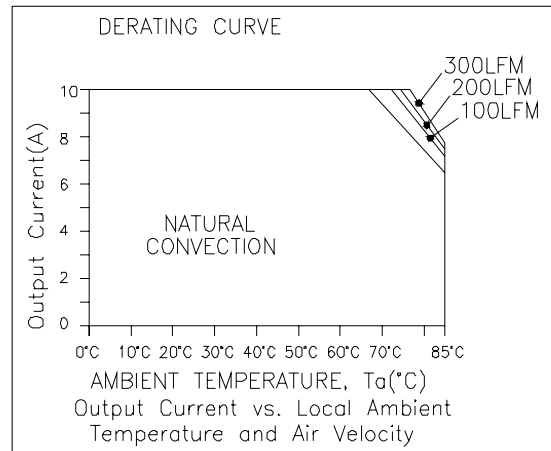
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Thermal Derating Curves



$V_{in}=12\text{ V}, V_o = 3.3\text{ V}$

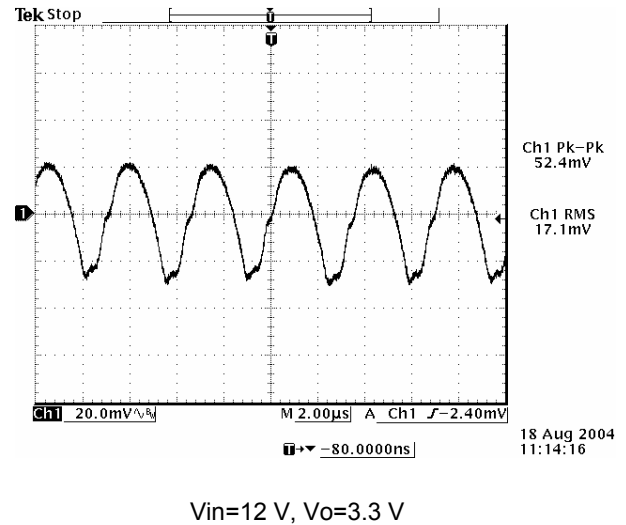
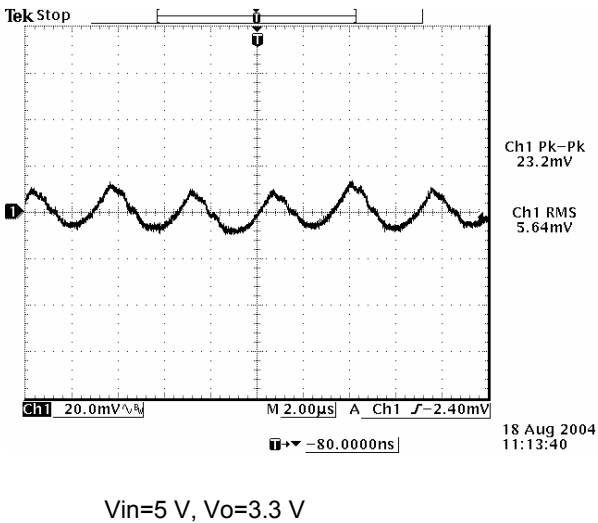
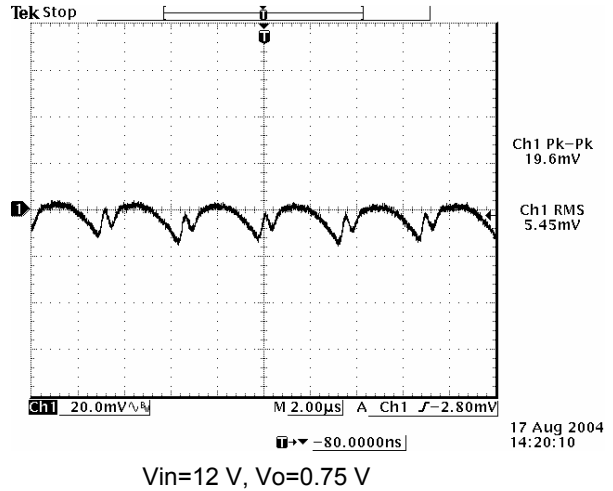
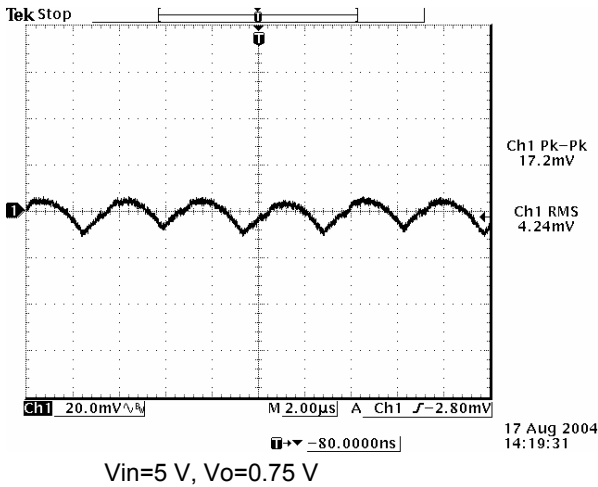


$V_{in}=5\text{ V}, V_o = 3.3\text{ V}$

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Ripple and Noise Waveforms



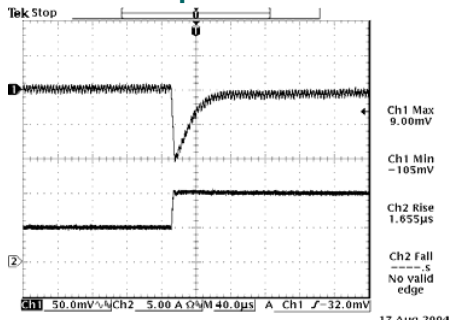
Note: Ripple and noise at full load, external load with 10 µF tantalum capacitor and 1 µF ceramic at the output, and Ta=25 deg C.

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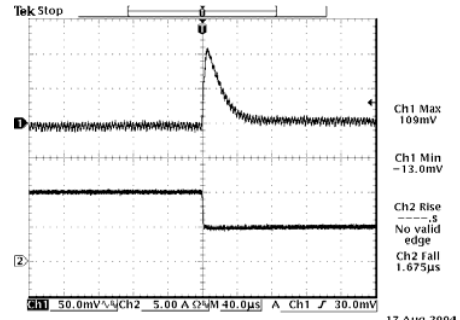
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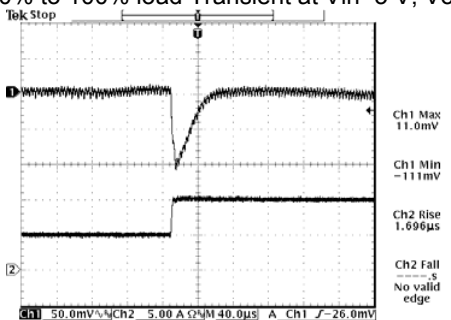
Transient Response Waveforms



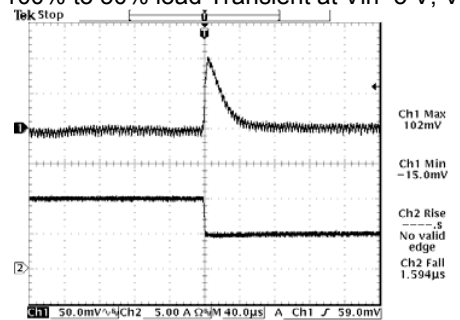
50% to 100% load Transient at Vin=5 V, Vo=0.75 V



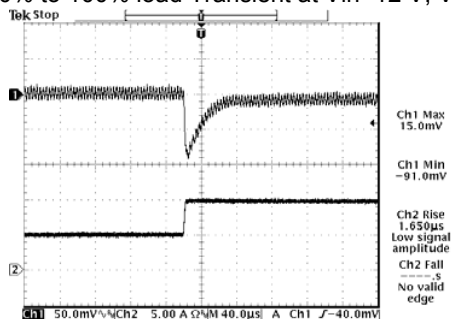
100% to 50% load Transient at Vin=5 V, Vo=0.75 V



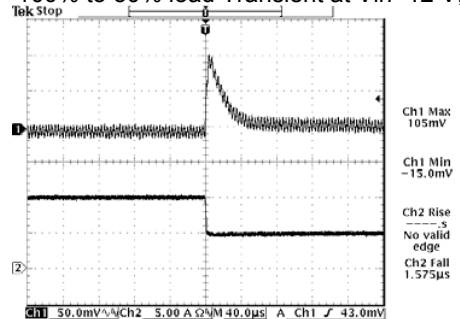
50% to 100% load Transient at Vin=12 V, Vo=0.75 V



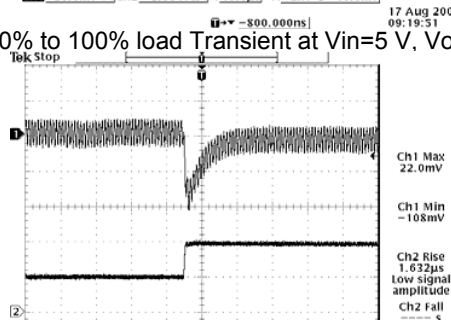
100% to 50% load Transient at Vin=12 V, Vo=0.75 V



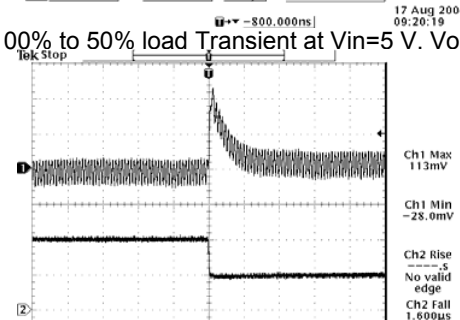
50% to 100% load Transient at Vin=5 V, Vo=3.3 V



100% to 50% load Transient at Vin=5 V, Vo=3.3 V



50% to 100% load Transient at Vin=12 V, Vo=3.3 V



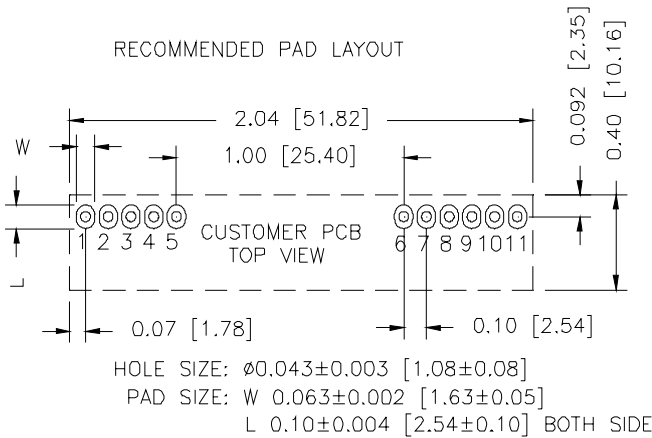
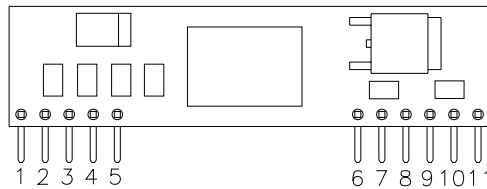
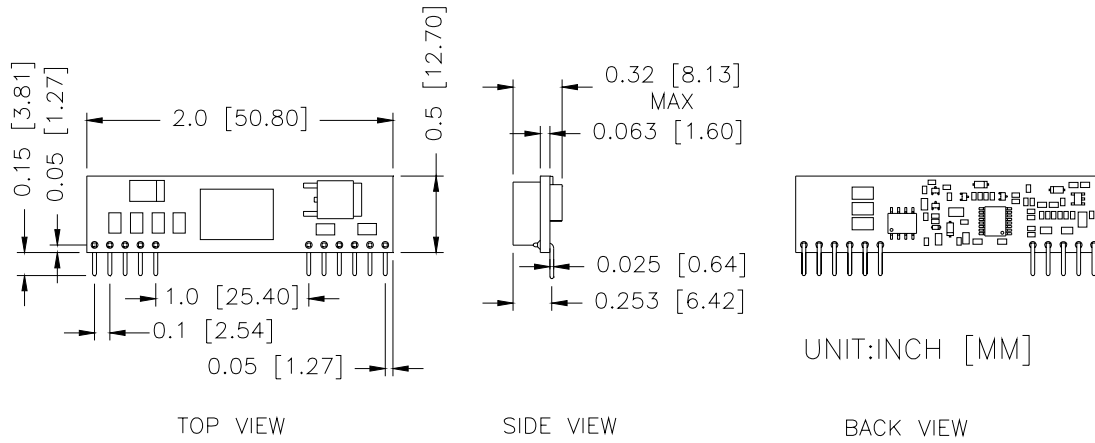
100% to 50% load Transient at Vin=12 V, Vo=3.3 V

Note: Transient response at di/dt=2.5 A/µs, external load with 470 µF tantalum capacitor at the output.

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Mechanical Outline



Pin Connections

Pin	Function
1	Vout
2	Vout
3	Remote Sense
4	Vout
5	Ground
6	Ground
7	Vin
8	Vin
9	SEQ
10	Trim
11	Remote On/Off

RoHS Compliance

Complies with the European Directive 2002/95/EC, calling for the elimination of lead and other hazardous substances from electronic products.



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CORPORATE

Bel Fuse Inc.
 206 Van Vorst Street
 Jersey City, NJ 07302
 Tel 201-432-0463
 Fax 201-432-9542
www.belfuse.com

FAR EAST

Bel Fuse Ltd.
 8F/ 8 Luk Hop Street
 San Po Kong
 Kowloon, Hong Kong
 Tel 852-2328-5515
 Fax 852-2352-3706
www.belfuse.com

EUROPE

Bel Fuse Europe Ltd.
 Preston Technology Management Centre
 Marsh Lane, Suite G7, Preston
 Lancashire, PR1 8UD, U.K.
 Tel 44-1772-556601
 Fax 44-1772-888366
www.belfuse.com