1

DC-DC Converter Short Form

MPDRX312S (Ultra High Speed Response POL)

FEATURES

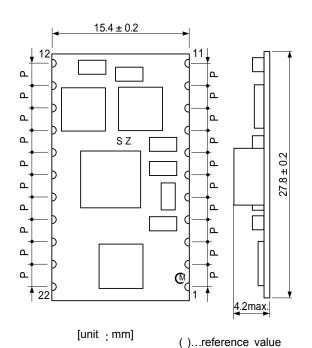
- · Ultra High Speed Response
- Wide input range (3.0V to 5.5V), Output range (0.8V to 1.8V)
- 16A Output Current
- Wide operational temperature (-40°C to +85°C)
- ON/OFF / Output voltage sense / Over current function / P-good function / Variable Start-up Speed function (by external capacitor)



GENERAL SPECIFICATIONS (Ta=25°C)

| Item | Symbol | Condition | | MIN. | TYP. | MAX. | UNIT |
|------------------------------------|--------|---|---------------|------|------|------|------|
| Input Voltage | Vin | | | 3.0 | - | 5.5 | V |
| Output Voltage Adjustable Range | Vout | F.T. pin = open | | 0.8 | - | 0.9 | V |
| | | F.T. pin = short to GND | | 0.9 | - | 1.8 | |
| Output Voltage tolerance | Vo tol | Over Io, Temperature range Vin=3.0~5.5V | Vo=0.8 ~ 0.9V | -3.0 | ı | +3.0 | . % |
| | | | Vo=0.9 ~ 1.8V | -2.5 | - | +2.5 | |
| Output Current | lout | | | 0 | - | 16 | Α |
| Ripple Voltage | Vrpl | Vin=3.3V, BW=20MHz, Cout=100μF | | - | 20 | - | mVpp |
| Efficiency | EFF | Vin=3.3V, Vout=1.8V, lout=16A | | - | 86.5 | - | % |

DIMENSIONS



| Pin No. | Symbol | Function |
|----------------------|---------|------------------------------|
| 2 | SENŚE | Output voltage sense |
| 3,4,5 | Vout | Output |
| 6,7,8,9, 16,17,18 | GND | GND |
| 10 | FT | Output trim |
| 11 | VAR | Output voltage adjustment |
| 12,13,14, 15 | Vin | Input |
| 1 | OVPOUT | Output Over-voltage Alarm |
| 19 | SS | Soft start |
| 20 | N.C. | Non connection |
| 21 | PWRGOOD | Power Good |
| 22 | ON/OFF | Remote ON/OFF |

⚠ Note:

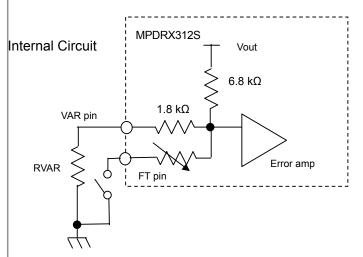
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2009.9.3

P=2.54+ 0.2mm

Tolerance is not accumulated

OUTPUT VOLTAGE ADJUSTMENT



(a)
$$0.8V \text{ Vout} < 0.9V (F.T. : open)$$

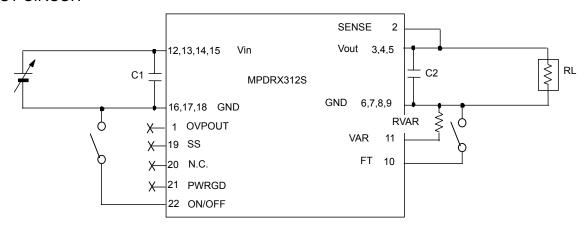
$$RVAR = \frac{5440}{Voadj[V] - 0.8[V]} - 1800[O]$$

$$RVAR = \frac{5440}{Voadj[V] - 0.9[V]} - 1800[O]$$

<RVAR CALCULATION EXAMPLE>

| Voadj [V] | Calculated RVAR | RVAR Example | F.T. pin | |
|-----------|-----------------|--------------|--------------|--|
| | [] | [] | | |
| 1.8 | 4240 | 3.9k+330 | Short to GND | |
| 1.5 | 7270 | 6.8k+470 | Short to GND | |
| 1.2 | 16330 | 16k+330 | Short to GND | |
| 1.0 | 52600 | 47k+5.6k | Short to GND | |
| 0.9 | | Open | Short to GND | |
| 0.8 | | Open | Open | |

TEST CIRCUIT



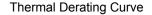
 $\begin{array}{ll} \text{C1}: 100 \mu \text{F} \, / \, 10 \text{V} & \text{Ceramic Capacitor} \\ \text{C2}: 100 \mu \text{F} \, / \, 6.3 \text{V} & \text{Ceramic Capacitor} \end{array}$

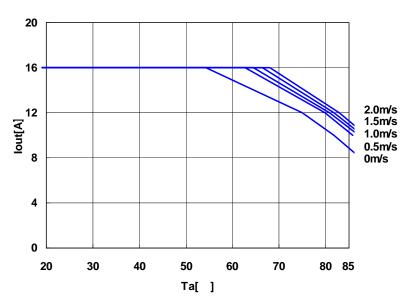
Please make sure to place C1 and C2 nearby input and output terminal of DC-DC converter.

⚠ Note:

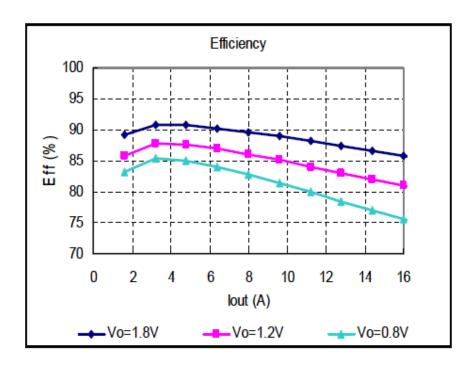
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THERMAL DERATING





EFFICIENCY



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