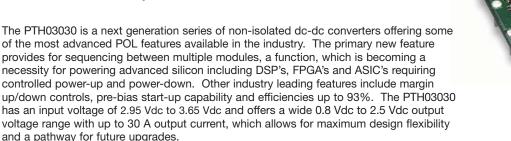






### **DC-DC CONVERTERS** POLA Non-isolated

- 30 A output current
- 3.3 V input voltage
- Wide-output voltage adjust (0.8 Vdc to 2.5 Vdc)
- Auto-track<sup>™</sup> sequencing<sup>\*</sup>
- Margin up/down controls
- Pre-bias start-up capability
- Efficiencies up to 93%
- Output ON/OFF inhibit
- Output voltage sense
- Point-of-Load-Alliance (POLA) compatible
- Available RoHS compliant



All specifications are typical at nominal input, full load at 25 °C unless otherwise stated  $C_{in}$  = 1500 µF,  $C_{out}$  = 0 µF

## **OUTPUT SPECIFICATIONS** 0.8-2.5 Vdc Voltage adjustability (See Note 4) Setpoint accuracy ±2.0% Vo Line regulation ±10 mV typ. Load regulation ±12 mV typ. ±3.0% Vo Total regulation Minimum load 0 A Ripple and noise 20 MHz bandwidth 30 mV pk-pk Temperature co-efficient -40 °C to +85 °C ±0.5% Vo Transient response 70 us recoverv time (See Note 5) Overshoot/undershoot 100 mV Margin adjustment ±5.0% Vo

### **INPUT SPECIFICATIONS** (See Note 3) 2.95-3.65 Vdc Input voltage range Input current No load 10 mA typ. Remote ON/OFF (See Note 1) Positive logic 1 V/ms Start-up time Undervoltage lockout 2.8-2.95 V typ. Track input voltage Pin 11 (See Note 6, 7) ±0.3 Vin

## International Safety Standard Approvals



UL/cUL CAN/CSA-C22.2 No. 60950-1-03/UL 60950-1, File No. E174104

TÜV Product Service (EN60950) Certificate No. B 04 06 38572 044 CB Report and Certificate to IEC60950, Certificate No. US/8292/UI







# **EMC CHARACTERISTICS**

Electrostatic discharge	EN61000-4-2, IEC801-2
Conducted immunity	EN61000-4-6
Radiated immunity	EN61000-4-3

# EN61000-4-6 EN61000-4-3

GENERAL SPECIFICAT	IONS		
Efficiency	(See Efficiency	Table)	93% max.
Insulation voltage			Non-isolated
Switching frequency		275	6 kHz to 325 kHz
Approvals and standards			EN60950 UL/cUL60950
Material flammability			UL94V-0
Dimensions	$(L \times W \times H)$		28.45 x 9.00 mm 1.120 x 0.354 in
Weight			10 g (0.35 oz)
MTBF	Telcordia SR-3	32	2,821,000 hours

## **ENVIRONMENTAL SPECIFICATIONS** Thermal performance Operating ambient.

(See Note 2)	temperature Non-operating	-40 °C to +125 °C
MSL ('Z' suffix only)	JEDEC J-STD-020C	Level 3
PROTECTION		
Short-circuit	Auto reset	45 A typ.
Thermal		Yes

\*Auto-track<sup>™</sup> is a trade mark of **Texas Instruments** 

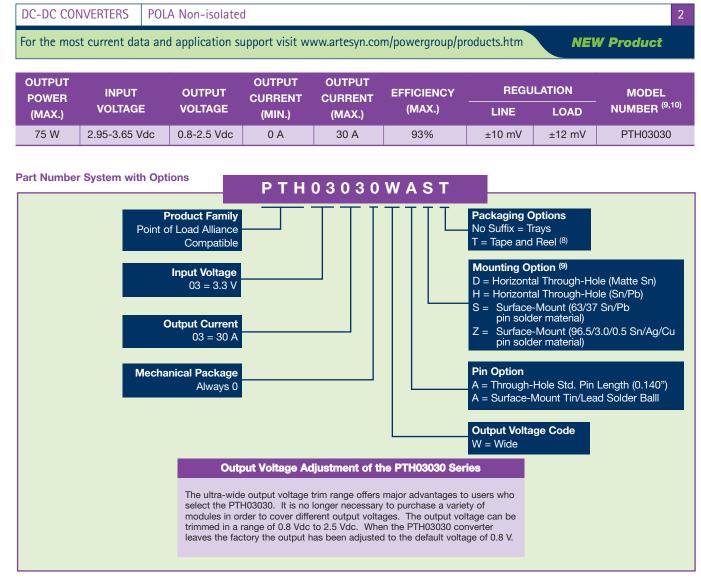
-40 °C to +85 °C







# 3.3 Vin single output



## Notes

- Remote ON/OFF. Positive Logic 1 Pin 3 open; or V > Vin - 0.5 V ON:
- Pin 3 GND; or V < 0.8 V (min 0.2 V) OFE
- See Figure 1 for safe operating curve.
- A 1,500 µF electrolytic input capacitor is required for proper operation. The capacitor must be rated for a minimum of 900 mA rms of ripple current.
- An external output capacitor is not required for basic operation. Adding 4 330 µF of distributed capacitance at the load will improve the transient response.
- 5
- I A/µs load step, 50 to 100%  $I_{omax}$ ,  $C_{out} = 330 \,\mu$ F. If utilized Vout will track applied voltage by ±0.3 V (up to Vo set point). 6 The pre-bias start-up feature is not compatible with Auto-Track<sup>™</sup>. This is because when the module is under Auto-Track<sup>™</sup> control, it is fully active ". This is 7 and will sink current if the output voltage is below that of a back-feeding source. Therefore to ensure a pre-bias hold-off, one of the following two techniques must be followed when input power is first applied to the module. The Auto-Track<sup>™</sup> function must either be disabled, or the module's output held off using the Inhibit pin. Refer to Application Note 152 for more details.
- Tape and reel packaging only available on the surface-mount versions. 8 To order Pb-free (RoHS compatible) surface-mount parts replace the 9 mounting option 'S' with 'Z', e.g. PTH03030WAZ. To order Pb-free (RoHS compatible) through-hole parts replace the mounting option 'H' with 'D', e.g. PTH03030WAD.
- 10 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.

EFFICIENCY TABLE (I <sub>O</sub> = 20 A)		
OUTPUT VOLTAGE	EFFICIENCY	
Vo = 1.0 V	85%	
Vo = 1.2 V	87%	
Vo = 1.5 V	89%	
Vo = 1.8 V	91%	
Vo = 2.0 V	92%	
Vo = 2.5 V	93%	







# DC-DC CONVERTERS POLA Non-isolated

For the most current data and application support visit www.artesyn.com/powergroup/products.htm

**NEW Product** 

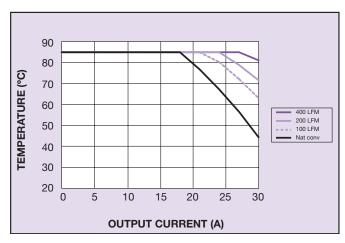


Figure 1 - Safe Operating Area Vin = 3.3 V, Output Voltage = 2.5 V (See Note A)

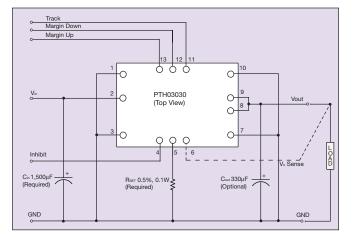


Figure 3 - Standard Application

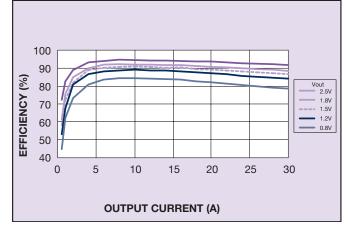


Figure 2 - Efficiency vs Load Current Vin = 3.3 V (See Note B)

## Notes

- A SOA curves represent the conditions at which internal components are within the Artesyn derating guidelines.
- B Characteristic data has been developed from actual products tested at 25 °C. This data is considered typical data for the converter.







DC-DC CONVERTERS POLA Non-isolated

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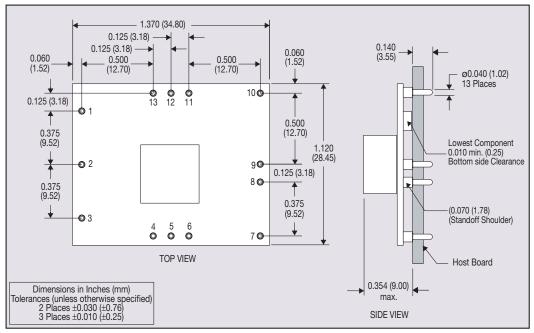
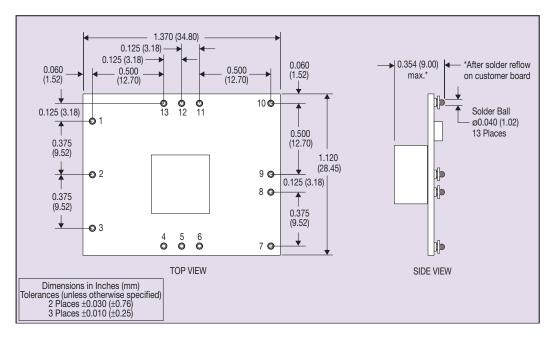


Figure 4 - Plated Through-Hole Mechanical Drawing



PIN CONNECTIONS		
PIN NO.	FUNCTION	
1	Ground	
2	Vin	
3	Ground	
4	Inhibit*	
5	Vo adjust	
6	Vo sense	
7	Ground	
8	Vout	
9	Vout	
10	Ground	
11	Track	
12	Margin down*	
13	Margin up*	

\*Denotes negative logic: Open = Normal operation Ground = Function active

Figure 5 - Surface-Mount Mechanical Drawing

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Please consult our website for the following items: ✓ Application Note

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