

# 10 Watts

## AEE01-Dual

Total Power: 10 Watts  
Input Voltages: 48V, 24V  
No. of Outputs: Dual



### Special Features

- 10 W, Dual output
- 2:1 Input range
- 1.0" x 2.0" x 0.33 case
- Industry standard package
- Low Profile
- Low Cost

### Environmental

Operating base temperature range:  
-25°C to +95°C  
Storage temperature: -40°C to +105°C  
MTBF: 2.7 Million Hours (Belcore TR332)

### Safety

UL UL1950 Recognition  
CSA CSA22.2-950 Recognition  
TUV EN60950 Recognition

### Electrical Specs

#### Input

Input range 18 to 36 VDC  
36 to 72 VDC  
Efficiency 79% typical ( $\pm 5V$ )

#### Output

Voltage tolerance  $\pm 1.0\%$   
Overall regulation  $\pm 1\%$  max  
Noise/ripple 1% (mV P-P)  
Transient response 200 usec typical (50% step load change to within 5%Vo)

Temperature Regulation  $\pm 0.02\% V_o / ^\circ C$

Switching frequency 330kHz

#### Isolation

I/O isolation 500 VDC  
Isolation Resistance 300 Mohms

AMERICAS

5810 Van Allen Way  
 Carlsbad, CA 92008  
 Telephone: 760-930-4600  
 Facsimile: 760-930-0698

EUROPE

Astec House, Waterfront Business Park  
 Merry Hill, Dudley  
 West Midlands, DY5 1LX, UK  
 Telephone: 44 (1384) 842-211  
 Facsimile: 44 (1384) 843-355

ASIA

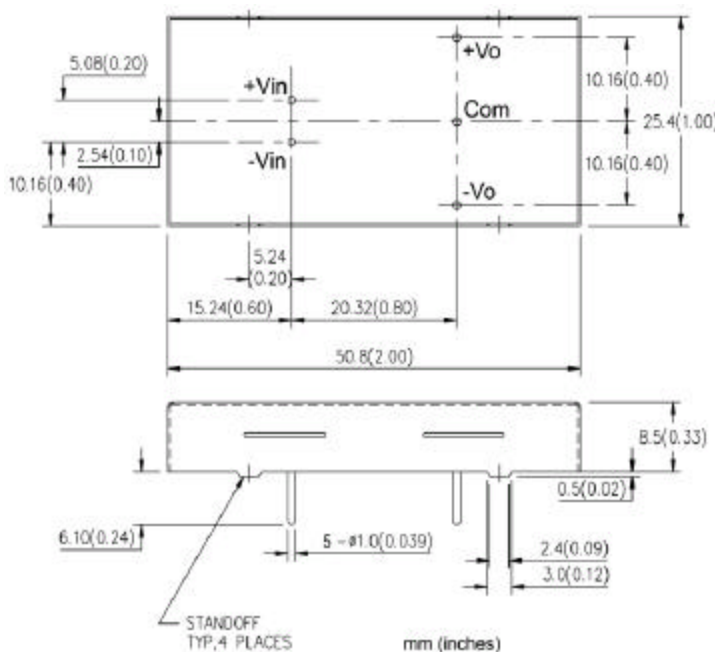
Units 2111-2116, Level 21  
 Tower1, Metroplaza  
 223, Hing Fong Road  
 Fwai Fong, New Territories  
 Hong Kong  
 Telephone: 852-2437-9662  
 Facsimile: 852-2402-4426



## Ordering Information

Input Voltage	Output Voltage	Efficiency	Model Number
18-36 V	±5V @ ±1.0 A	79%	AEE01AA24
18-36 V	±12V @ ±0.42 A	83%	AEE00BB24
18-36 V	±15V @ ±0.33 A	83%	AEE00CC24
36-72 V	±5V @ ±1.0 A	79%	AEE01AA48
36-72 V	±12V @ ±0.42 A	83%	AEE00BB48
36-72 V	±15V @ ±0.33 A	83%	AEE00CC48

## Mechanical Dimensions and Pin Assignments



Notes:

1. 20 MHz bandwidth. An external 0.1 uf ceramic capacitor is recommended to be placed from +V out to comm.
2. All specifications are typical at nominal line, full load, and 25°C unless otherwise noted.
3. All specifications subject to change without notice.
4. Mechanical drawings are for reference only.