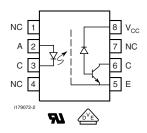


## High Speed Optocoupler, 1 Mbd, 15 kV/µs CMR, Transistor Output





#### **DESCRIPTION**

The SFH6345 is an optocoupler with a GaAlAs infrared emitting diode, optically coupled to an integrated photo detector consisting of a photo diode and a high speed transistor in a DIP-8 plastic package. The device is similar to the 6N135 but has an additional faraday shield on the detector which enhances the input-output dV/dt immunity. Signals can be transmitted between two electrically separated circuits up to frequencies of 2 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-5 (VDE 0884)

### **FEATURES**

- Direct replacement for HCPL 4503
- High-speed optocoupler without base connection
- Isolation test voltage: 5300 V<sub>RMS</sub>
- GaAlAs emitter
- Integrated detector with photo diode and transistor
- High data transmission rate: 1 MBit/s
- TTL compatible
- Open collector output
- Good CTR linearity relative to forward current
- Field effect stable
- · Low coupling capacitance
- Very high common mode transient immunity dV/dt:  $\geq$  15 kV/ $\mu$ s at V<sub>CM</sub> = 1500 V
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### **APPLICATIONS**

- Data communications
- IGBT drivers
- Programmable controllers

PACKAGE OPTION TAPE AND REEL  DIP-8 Option 6  10.16 mm Option 9  > 0.7 mm
SFH6345
SFH6345-X007T (1)
SFH6345-X009T (1)
SFH6345-X001
SFH6345-X016
SFH6345-X017T <sup>(1)</sup>
SFH6345-X019T (1)

#### Note

(1) Also available in tubes; do not add T to end

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V <sub>R</sub>	3	V			
DC forward current		I <sub>F</sub>	25	mA			
Surge forward current	t <sub>p</sub> = 1 μs, 300 pulses/s	I <sub>FSM</sub>	1	А			
Power dissipation		P <sub>diss</sub>	45	mW			
OUTPUT							
Supply voltage		Vs	- 0.5 to 30	V			
Output voltage		V <sub>O</sub>	- 0.5 to 25	V			
Output current		Io	8	mA			
Power dissipation		P <sub>diss</sub>	100	mW			
COUPLER							
Isolation test voltage between emitter and detector		$V_{ISO}$	5300	$V_{RMS}$			
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C			
Ambient temperature range		T <sub>amb</sub>	- 55 to + 100	°C			
Junction temperature		T <sub>j</sub>	100	°C			
Soldering temperature (1)	max. 10 s, max. dip soldering: distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C			

#### **Notes**

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP)

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT (1)				•	•			
Forward voltage	I <sub>F</sub> = 16 mA	V <sub>F</sub>		1.6	1.9	V		
Reverse current	V <sub>R</sub> = 3 V	I <sub>R</sub>		0.5	10	μΑ		
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	Co		75		pF		
Thermal resistance		R <sub>thja</sub>		700		K/W		
OUTPUT								
Supply ourrent logic high	$I_F = 0 \text{ V}, V_O = \text{open}, V_{CC} = 15 \text{ V}$	Іссн		0.01	1	μΑ		
Supply current, logic high		I <sub>CCH</sub>		0.01	2	μΑ		
	$I_F = 0 \text{ V}, V_O = V_{CC} = 5.5 \text{ V}$	l <sub>OH</sub>		0.003	0.5	μΑ		
Output current, output high	I <sub>F</sub> = 0 V, V <sub>O</sub> = V <sub>CC</sub> = 15 V	Іон		0.01	1	μΑ		
		I <sub>OH</sub>			50	μΑ		
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz	C <sub>CE</sub>		3		pF		
Thermal resistance		R <sub>thja</sub>		300		K/W		
COUPLER								
Coupling capacitance		C <sub>C</sub>		0.6		pF		
Collector emitter saturation voltage	$I_F = 16 \text{ mA}, I_O = 2.4 \text{ mA}, V_{CC} = 4.5 \text{ V}$	V <sub>OL</sub>		0.1	0.4	V		
Logic low supply current	$I_F = 16 \text{ mA}, V_O = \text{open}, V_{CC} = 15 \text{ V}$	I <sub>CCL</sub>		80	200	μΑ		

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>•</sup> Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<sup>&</sup>lt;sup>(1)</sup>  $T_{amb} = 0$  °C to 70 °C, unless otherwise specified, typical values  $T_{amb} = 25$  °C.



# High Speed Optocoupler, 1 Mbd, Vishay Semiconductors 15 kV/µs CMR, Transistor Output

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	$I_F = 16 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	I <sub>C</sub> /I <sub>F</sub>	19	30		%
Current transfer ratio	$I_F = 16 \text{ mA}, V_O = 0.5 \text{ V}, V_{CC} = 4.5 \text{ V},$ $T_{amb} = 0 \text{ °C to } 70 \text{ °C}$	I <sub>C</sub> /I <sub>F</sub>	15			%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Propagation delay time (high to low), see fig. 1	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	t <sub>PHL</sub>		0.3	0.8	μs	
Propagation delay time (low to high), see fig. 1	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	t <sub>PLH</sub>		0.3	0.8	μs	

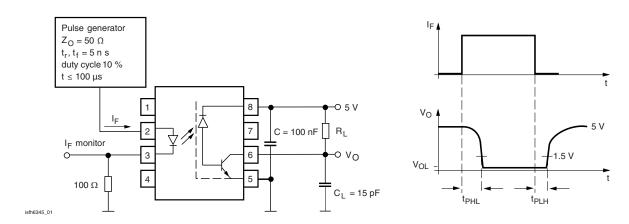


Fig. 1 - Switching Times (Typ.)

COMMON MODE TRANSIENT IMMUNITY							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Common mode transient immunity (high), see fig. 2	$I_{O} = 0$ mA, $V_{CM} = 1500 V_{P-P}$ , $R_{L} = 1.9 \text{ k}\Omega$ , $V_{CC} = 5 \text{ V}$	CM <sub>H</sub>	15	30		kV/μs	
Common mode transient immunity (low), see fig. 2	$I_{O} = 16$ mA, $V_{CM} = 1500 V_{P-P}$ , $R_{L} = 1.9 \text{ k}\Omega$ , $V_{CC} = 5 \text{ V}$	CM <sub>L</sub>	15	30		kV/μs	

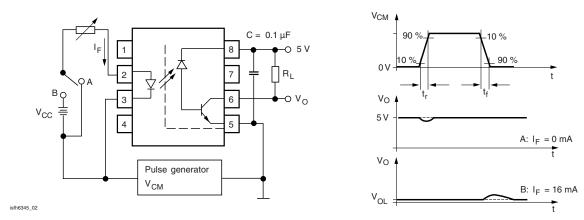


Fig. 2 - Common Mode Transient Immunity

## High Speed Optocoupler, 1 Mbd, 15 kV/µs CMR, Transistor Output



SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification	IEC 68 part 1			55/100/21			
Comparative tracking index		CTI	175		399		
V <sub>IOTM</sub>			8000			V	
V <sub>IORM</sub>			890			V	
P <sub>SO</sub>					500	mW	
I <sub>SI</sub>					300	mA	
T <sub>SI</sub>					175	°C	
Creepage distance	Standard DIP-8		7			mm	
Clearance distance	Standard DIP-8		7			mm	
Creepage distance	400 mil DIP-8		8			mm	
Clearance distance	400 mil DIP-8		8			mm	

### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

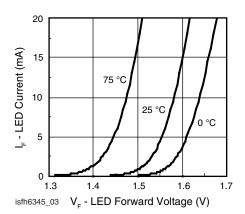


Fig. 3 - Logic High Output Current vs. Temperature

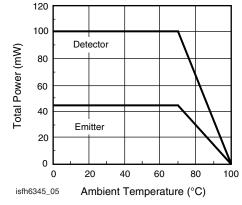


Fig. 5 - Permissible Power Dissipation vs. Temperature

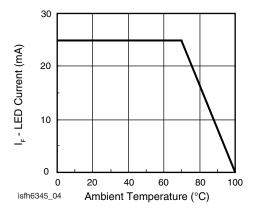


Fig. 4 - Permissible Forward LED Current vs. Temperature

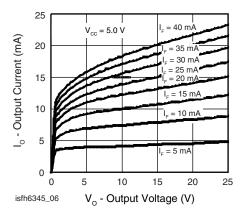


Fig. 6 - Output Current vs. Output Voltage

<sup>•</sup> As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



# High Speed Optocoupler, 1 Mbd, Vishay Semiconductors 15 kV/µs CMR, Transistor Output

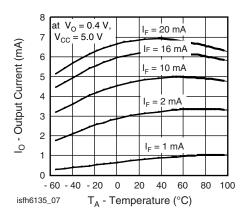


Fig. 1 - Output Current vs. Temperature

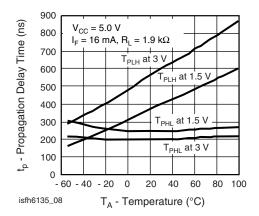


Fig. 7 - Propagation Delay vs. Ambient Temperature

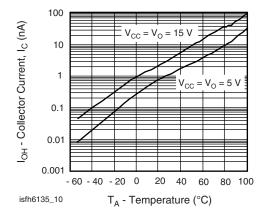


Fig. 8 - Logic High Output Current vs. Temperature

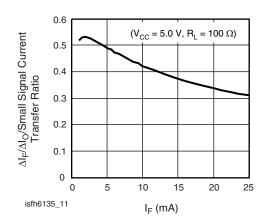
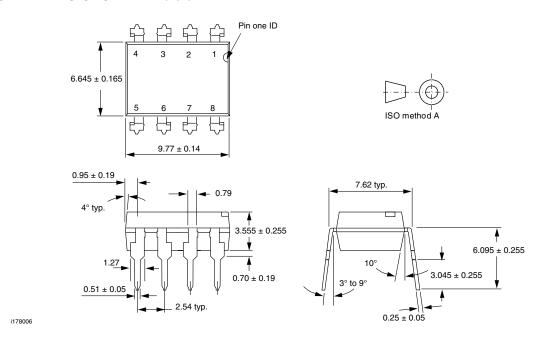


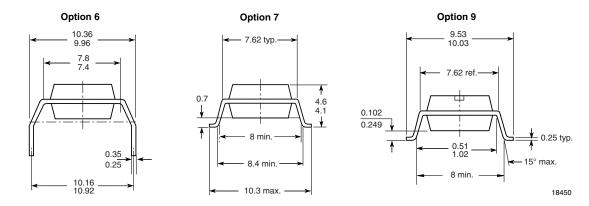
Fig. 9 - Small Signal Current Transfer Ratio vs. Input Current

## High Speed Optocoupler, 1 Mbd, 15 kV/µs CMR, Transistor Output



### **PACKAGE DIMENSIONS** in millimeters





### **PACKAGE MARKING**



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Vishay

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