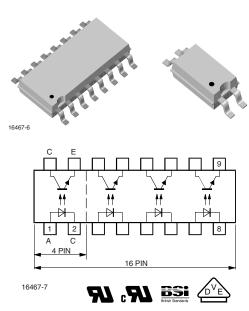


Vishay Semiconductors

COMPLIANT

Optocoupler, Phototransistor Output, Single/Quad Channel, Half Pitch Mini-Flat Package



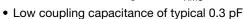
DESCRIPTION

The TCMT1100 series consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin (single channel) up to 16 pin (quad channel) package.

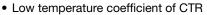
The elements are mounted on one leadframe providing a fixed distance between input and output for highest safety requirements.

FEATURES

- Low profile package (half pitch)
- AC isolation test voltage 3750 V_{RMS}







- Wide ambient temperature range
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96EC

APPLICATIONS

- Programmable logic controllers
- Modems
- Answering machines
- General applications

AGENCY APPROVALS

- UL1577, file no. E76222 system code M, double protection
- cUL CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-2 (VDE 0884)

ORDERING INFO	RMATIO	N								
ТС	М] [1		#	1	0	#		SOP-4 SOP-16	h
		P	ART NUMB	ER					7 mm	—
AGENCY CERTIFIED/ PACKAGE	CTR (%)									
UL, cUL, BSI, VDE	50 to 600	40 to 80	63 to 125	100 to 200	160 to 320	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400
SOP-4	TCMT1100	TCMT1101	TCMT1102	TCMT1103	TCMT1101	TCMT1104	TCMT1106	TCMT1107	TCMT1108	TCMT1109
SOP-16, quad channel	TCMT4100	-	-	-	-	-	TCMT4106	-	-	=



Vishay Semiconductors Optocoupler, Phototransistor Output, Single/Quad Channel, Half Pitch Mini-Flat Package

ABSOLUTE MAXIMUM RATINGS ⁽¹⁾ (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
INPUT							
Reverse voltage		V _R	6	V			
Forward current		I _F	60	mA			
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	Α			
Power dissipation		P _{diss}	100	mW			
Junction temperature		T _j	125	°C			
OUTPUT							
Collector emitter voltage		V_{CEO}	70	V			
Emitter collector voltage		V _{ECO}	7	V			
Collector current		I _C	50	mA			
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA			
Power dissipation		P _{diss}	150	mW			
Junction temperature		T _j	125	°C			
COUPLER							
AC isolation test voltage (RMS)	Related to standard climate 23/50 DIN 50014	V _{ISO}	3750	V_{RMS}			
Total power dissipation		P _{tot}	250	mW			
Operating ambient temperature range		T _{amb}	- 40 to + 100	°C			
Storage temperature range		T _{stg}	- 40 to + 100	°C			
Soldering temperature (2)		T _{sld}	260	°C			

Notes

⁽²⁾ Refer to reflow profile soldering conditions for surface mounted devices.

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V		
Junction capacitance	V _R = 0, f = 1 MHz	Cj		50		pF		
OUTPUT								
Collector emitter voltage	I _C = 100 μA	V_{CEO}	70			V		
Emitter collector voltage	$I_E = 100 \mu A$	V_{ECO}	7			V		
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}			100	nA		
COUPLER								
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}			0.3	V		
Cut-off frequency	V_{CE} = 5 V, I_F = 10 mA, R_L = 100 Ω	f _c		100		kHz		
Coupling capacitance	f = 1 MHz	C _k		0.3		pF		

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

⁽¹⁾ Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.



Optocoupler, Phototransistor Output, Vishay Semiconductors Single/Quad Channel, Half Pitch Mini-Flat Package

CURRENT TRANSFER RATIO									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	TCMT1100	CTR	50		600	%		
		TCMT1102	CTR	63		125	%		
	$V_{CE} = 5 \text{ V}, I_{F} = 10 \text{ mA}$	TCMT1103	CTR	100		200	%		
		TCMT1104	CTR	160		320	%		
	V _{CE} = 5 V, I _F = 5 mA	TCMT1105	CTR	50		150	%		
I _C /I _F		TCMT1106	CTR	100		300	%		
		TCMT1107	CTR	80		160	%		
		TCMT1108	CTR	130		260	%		
		TCMT1109	CTR	200		400	%		
		TCMT4100	CTR	50		600	%		
		TCMT4106	CTR	100		300	%		

SWITCHING CHARAC	TERISTICS					
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _d		3		μs
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _r		3		μs
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _f		4.7		μs
Storage time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω , (see figure 1)	t _s		0		μs
Turn-on time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω , (see figure 1)	t _{on}		6		μs
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega,$ (see figure 1)	t _{off}		5		μs
Turn-on time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 2)	t _{on}		9		μs
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega,$ (see figure 2)	t _{off}		18		μs

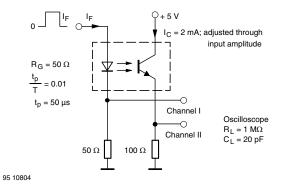


Fig. 1 - Test Circuit, Non-Saturated Operation

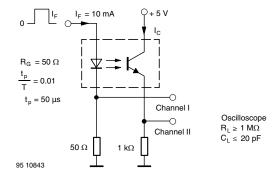


Fig. 2 - Test Circuit, Saturated Operation



Vishay Semiconductors Optocoupler, Phototransistor Output, Single/Quad Channel, Half Pitch Mini-Flat Package

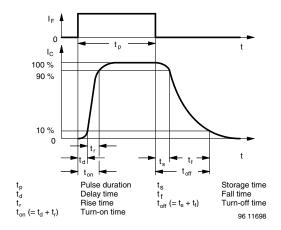


Fig. 3 - Switching Times

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification	IEC 68 part 1			40/110/21				
Comparative tracking index		CTI	175		399			
V _{IOTM}			6000			V		
V _{IORM}			707			V		
P _{SO}					265	mW		
I _{SI}					130	mA		
T _{SI}					150	°C		
Creepage distance			5			mm		
Clearance distance			5			mm		
Insulation thickness, reinforced rated	per IEC60950 2.10.5.1		0.4			mm		

Note

• As per IEC 60747-5-5, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

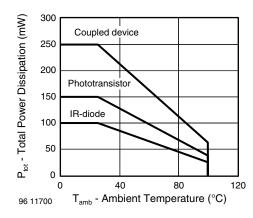


Fig. 4 - Total Power Dissipation vs. Ambient Temperature

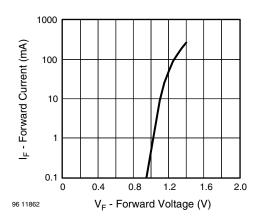


Fig. 5 - Forward Current vs. Forward Voltage





Optocoupler, Phototransistor Output, Vishay Semiconductors Single/Quad Channel, Half Pitch Mini-Flat Package

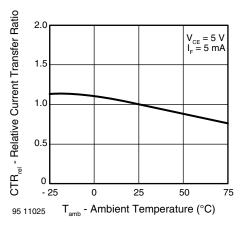


Fig. 6 - Relative Current Transfer Ratio vs.
Ambient Temperature

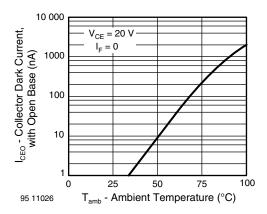


Fig. 7 - Collector Dark Current vs. Ambient Temperature

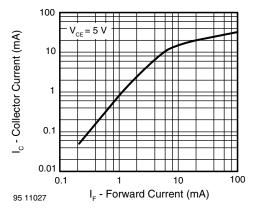


Fig. 8 - Collector Current vs. Forward Current

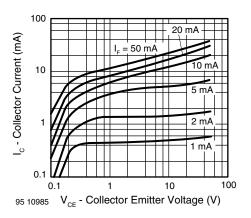


Fig. 9 - Collector Current vs. Collector Emitter Voltage

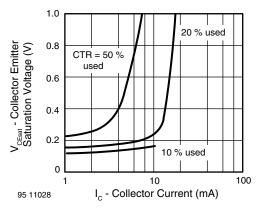


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current

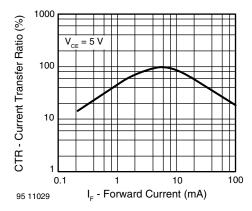


Fig. 11 - Current Transfer Ratio vs. Forward Current



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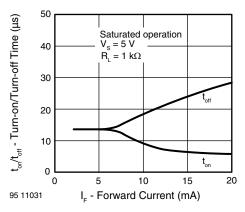


Fig. 12 - Turm-on/off Time vs. Forward Current

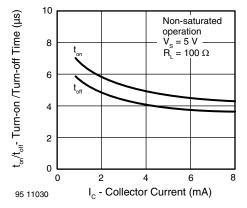
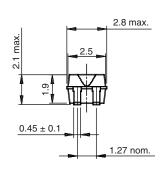
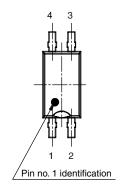


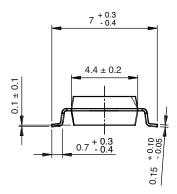
Fig. 13 - Turn-on/off Time vs. Collector Current

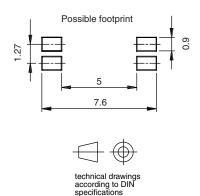
PACKAGE DIMENSIONS in millimeters





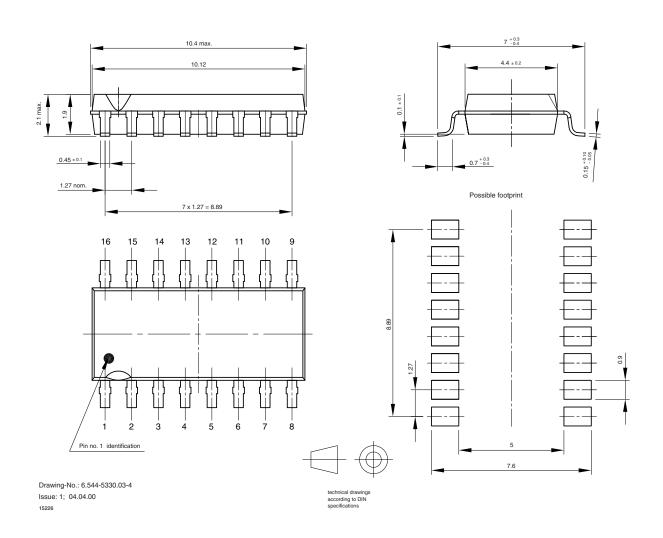
16283







Optocoupler, Phototransistor Output, Vishay Semiconductors Single/Quad Channel, Half Pitch Mini-Flat Package



PACKAGE MARKING



Note

This is an example of the marking of the TCMT1100

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