

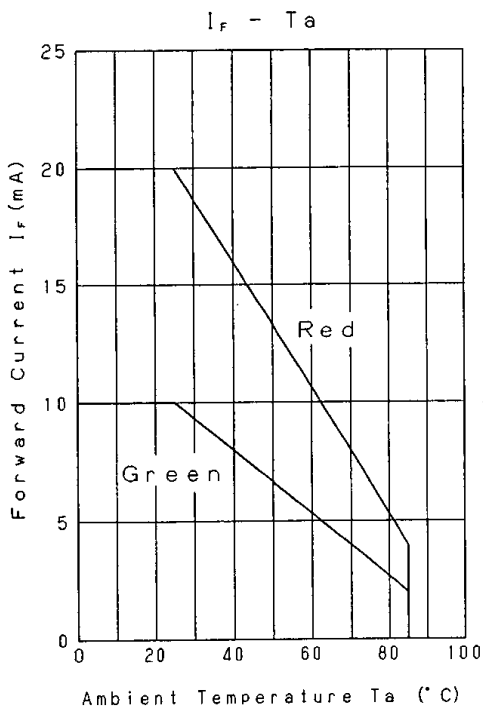
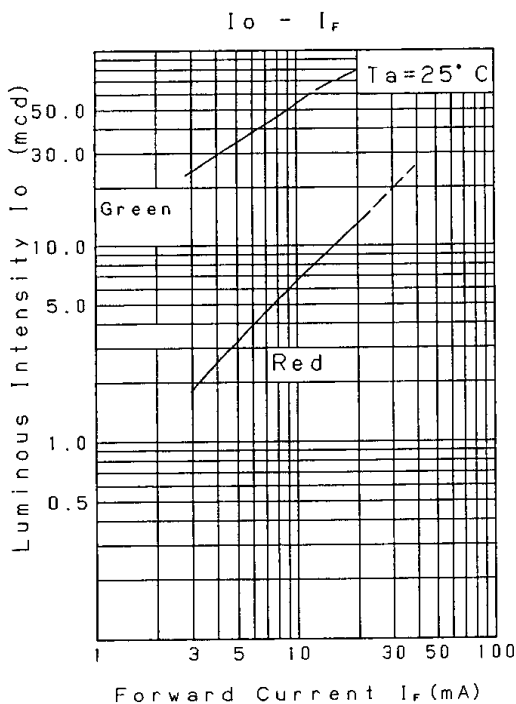
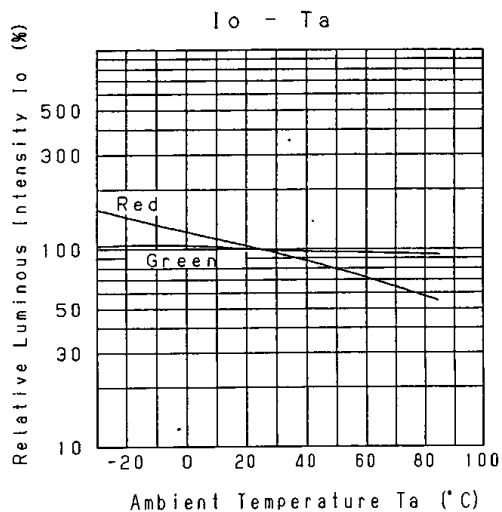
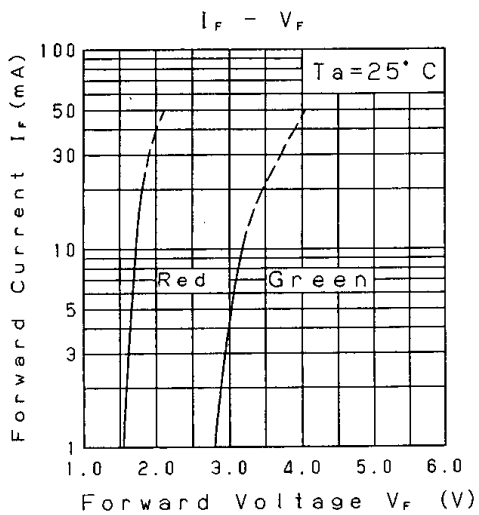
Approved	Checked	Designed	DEVELOPMENT SPECIFICATION					
<i>T. Shoda</i>	<i>M. W.</i>	<i>K. Sakurai</i>	P/N: LN J 1 1 5 W 8 6 R A				1 8	4
T	Y	P	E	Bi-color (Green / Red) Light Emitting Diode				
A P P L I C A T I O N			Indicators					
M A T E R I A L			Green (GaN) / Red (GaAlAs)					
O U T L I N E			Attached					
A B S O L U T E			※1 P	※2 I _{FP}	※3 J _{FKC}	V _R	Topr	Tstg
M A X I M U M			(G) (R)	(G) (R)	(G) (R)	(G) (R)		
R A T I N G S			40 55	50 60	10 20	5 3	-30~+85	-40~+100
			mW	mA	mA	V	°C	°C
C O N D I T I O N			T _a = 25 ± 3°C					
Test Specification								
Item		Symbol	Condition		Type	Limit		Unit
						Min	Max	
Green								
Forward Voltage		V _F	I _F = 5 mA		3.0		3.7	V
Reverse Leakage Current		I _R	V _R = 5 V				10	μA
Luminous Intensity ※4		I _o	I _F = 5 mA · DC		34	12.9		mc/d
Peak Emission Wavelength		λ _p	I _F = 5 mA · DC		525			nm
Spectral Line Half Width		Δλ	I _F = 5 mA · DC		45			nm
Red								
Forward Voltage		V _F	I _F = 10 mA		1.72		2.5	V
Reverse Leakage Current		I _R	V _R = 3 V				100	μA
Luminous Intensity ※4		I _o	I _F = 10 mA · DC		6.7	2.8		mc/d
Peak Emission Wavelength		λ _p	I _F = 10 mA · DC		655			nm
Spectral Line Half Width		Δλ	I _F = 10 mA · DC		20			nm
※1 (G) : Green (R) : Red ※2 The condition of I _{FP} is duty 10 %, pulse width 1 ms. Please contact the Panasonic local office if you design at low current (below 1mA DC) or pulse current operation and have any questions. ※3 When 2 light emitted at the same time, the total current (I _{FP} or I _{FR}) must be (G) I _{FP} < 50 mA, I _{FR} < 9 mA (R) I _{FR} < 60 mA, I _{FP} < 18 mA ※4 Measurement Tolerance is ±20%. ※5 Compositions of the lead···Cu/Ni/Au plating ※6 Package···Light white diffusion type. ※7 Soldering conditions···Refer to Handling note.								
Jun. 28. 2001								

Panasonic

KAGOSHIMA MATSUSHITA ELECTRONICS CO., LTD

KB-II-022-018B

T. Shida
M. Ito
K. Akashi



Jun. 28. 2001

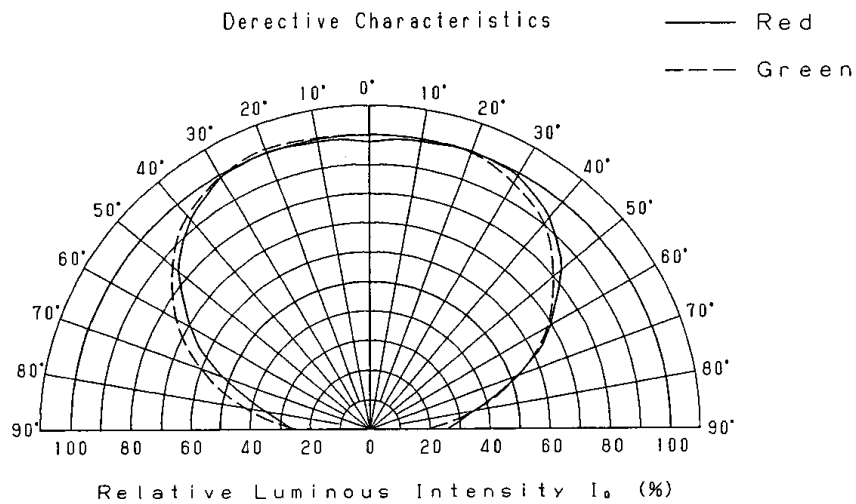
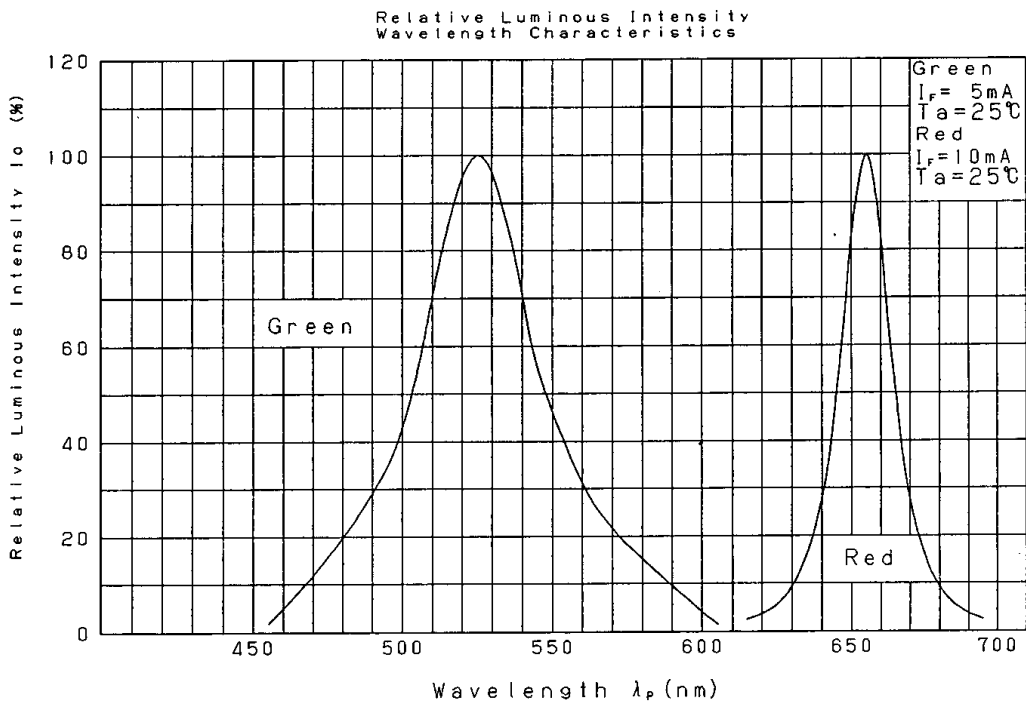
Approved <i>T. Shida</i>	Checked <i>M. Iw</i>	Designed <i>K. Sakai</i>
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DEVELOPMENT SPECIFICATION

P/N: LNJ115W86RA

18

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Jun. 28. 2001

Panasonic

KAGOSHIMA MATSUSHITA ELECTRONICS CO., LTD

KB-H-022-018B

Approved <i>T. Shida</i>	Checked <i>M. Ito</i>	Designed <i>K. Kobayashi</i>	DEVELOPMENT SPECIFICATION (OUTLINE) P/N: LNJI15W86RA		18	7
			<p>(Polarity)</p>			
<p>(Recommended Land Layout)</p>			<p>NOTE)</p> <ol style="list-style-type: none"> 1. Dimension of package doesn't include projections on electrode. 2. Unit: mm 			
Jun. 28, 2001						