

LXMG1627-12-6x

12V Dual 6W Programmable CCFL Inverter Module

#### **PRODUCTION DATASHEET**

#### **DESCRIPTION**

The LXMG1627-12-6x is a Dual 6W Output Direct Drive<sup>TM</sup> CCFL (Cold energizes the lamp Cathode Fluorescent Lamp) Inverter specifically to ensure that no premature Module specifically designed for driving lamp degradation occurs, while allowing LCD backlight lamps. It is ideal for significant power savings at lower dim driving typical 10.4" to 15" TFT panels.

LXMG1627 modules provide the

dimming input that permits brightness available (LXMG1627-05-6x), as well as control from either a DC voltage source or 4W versions (LXMG1627-xx-4x) for a PWM signal or external potentiometer. driving smaller lower voltage/power The maximum output current is externally panels. programmable over a range of 5mA to 8mA in 1mA steps to allow the inverter to Microsemi's properly match to a wide array of LCD panel lamp current specifications.

RangeMAX<sup>TM</sup> Digital Technique provides flicker-free brightness control in any wide range typically (50:1+) dimming application.

The resultant "burst drive" was designed levels.

The modules convert DC voltage from designer with a vastly superior display the system battery or AC adapter directly brightness range. This brightness range is to high frequency, high-voltage waves achievable with virtually any LCD display. required to ignite and operate CCFL The modules are available with a lamps. A 5V input inverter is also

> The module's design is based on LX6512 backlight controller, which provides a number of cost and performance advantages due to Dimming the controller's high level of integration.

> > Other benefits of this new topology are stable fixed-frequency operation, secondary-side strike-voltage regulation and both open and short protection with fault timeout.

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com Protected By U.S. Patents: 5,923,129; 5,930,121; 6,198,234; Patents Pending

#### **KEY FEATURES**

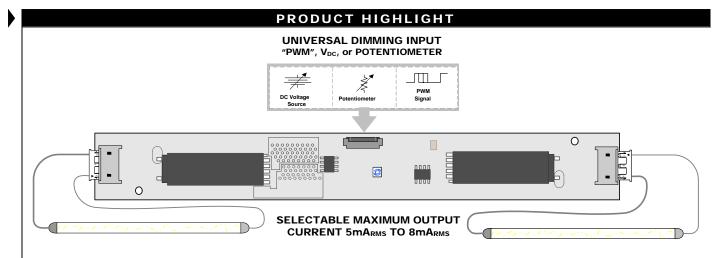
- **Externally Programmable** Maximum Output Current
- Easy to Use Brightness Control
- RangeMAX™ Wide Range Dimming
- Output Open/Short-Circuit Protection and Automatic Strike-Voltage Regulation and Timeout
- **Fixed Frequency Operation**
- Rated From -30°C to 80°C
- UL60950 E175910 Pending
- **RoHS Compliant**

# APPLICATIONS

- High Brightness Displays
- Portable Instrumentation
- **Desktop Displays**
- Industrial Display Controls

#### **BENEFITS**

- Smooth, Flicker Free 2%-100% Full-Range Brightness Control
- Programmable Output Current Allows Inverter To Mate With A Wide Variety Of LCD Panel's Specifications
- Output Open Circuit Voltage Regulation Minimizes Corona Discharge For High Reliability



	PACKAGE ORDER INFO						
	PART NUMBER	OUTPUT CONNECTOR	INVERTER MATES DIRECTLY TO				
		OUTPUT CONNECTOR	PANEL CONNECTORS				
	LXMG1627-12-61	JST SM02(8.0)B-BHS-1-TB(LF)(SN) or Yeon Ho 20015WR-05A00	JST BHR-03VS-1				
	LXMG1627-12-62	JST SM02B-BHSS-1-TB(LF)(SN) or Yeon Ho 35001WR-02A00	JST BHSR-02VS-1				

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#### Microsemi



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ABSOLUTE MAXIMUM RATINGS						
Input Signal Voltage (V <sub>IN</sub> )Input Power	0.3V to 15V					
Output Voltage, no load	Internally Limited to 1800V <sub>RMS</sub>					
Output Current						
Output Power (each output)	6.0W					
Input Signal Voltage (SLEEP Input)	0.3V to V <sub>IN</sub>					
Input Signal Voltage (BRITE)						
Ambient Operating Temperature, zero airflow						
Operating Relative Humidity, non-condensing						
Storage Temperature Range	40°C to 85°C					

Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal.

### RECOMMENDED OPERATING CONDITIONS (R.C.)

This module has been designed to operate over a wide range of input and output conditions. However, best efficiency and performance will be obtained if the module is operated under the condition listed in the 'R.C.' column. Min. and Max. columns indicate values beyond which the inverter, although operational, may not function optimally.

Parameter	Symbol	Recommended Operating Conditions			Units
r ai ailletei	Зуппоот	Min	R.C.	Max	Onits
Input Supply Voltage Range (Fully Regulated Lamp Current)	V <sub>IN</sub>	10.8	12	13.2	V
Input Supply Voltage Range (Functional)		10.2	12	13.8	
Output Power (each output)	Po		5.5	6.0	W
Linear BRITE Control Input Voltage Range	$V_{BRT\ ADJ}$	0		2.5	V
Lamp Operating Voltage	$V_{LAMP}$	480	600	720	$V_{RMS}$
Lamp Current (Full Brightness)	I <sub>OLAMP</sub>	5.0		8.0	$mA_RMS$
Operating Ambient Temperature Range	T <sub>A</sub>	-30		80	°C

### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of  $0^{\circ}$ C to  $60^{\circ}$ C except where otherwise noted; BRITE  $\geq$  2.5V,  $\overline{\text{SLEEP}} \geq$  2.0V,  $V_{IN}$  = 12V.

Parameter	Symbol	Test Conditions	LXMG1627-12-6x			Units
Farameter	Syllibol	rest Conditions	Min		Max	Units
OUTPUT PIN CHARACTERISTICS						
Full Bright Lamp Current (each output)	I <sub>L(MAX)</sub>	SET <sub>1</sub> = Ground, SET <sub>2</sub> = Ground	4.4	5.0	5.6	mA <sub>RMS</sub>
Full Bright Lamp Current (each output)	I <sub>L(MAX)</sub>	SET <sub>1</sub> = Ground, SET <sub>2</sub> = Open	5.4	6.0	6.6	mA <sub>RMS</sub>
Full Bright Lamp Current (each output)	I <sub>L(MAX)</sub>	SET <sub>1</sub> = Open, SET <sub>2</sub> = Ground	6.4	7.0	7.6	mA <sub>RMS</sub>
Full Bright Lamp Current (each output)	I <sub>L(MAX)</sub>	SET <sub>1</sub> = Open, SET <sub>2</sub> = Open	7.4	8.0	8.6	mA <sub>RMS</sub>
Output Current Lamp to Lamp Deviation	I <sub>LL%DEV</sub>	SET <sub>1</sub> = Open, SET <sub>2</sub> = Open		2	5	%
Min. Average Lamp Current (each output)	I <sub>L(MIN)</sub>	BRITE = 0V SET <sub>1</sub> = SET <sub>2</sub> = Ground $I_{L(MIN)} = I_{LMAX} * \sqrt{Burst Duty Cycle}$		1.0		mA <sub>RMS</sub>
Lamp Start Voltage	$V_{LS}$	-30°C < T <sub>A</sub> < 80°C, V <sub>IN</sub> > 10.8V	1500	1650		$V_{RMS}$
Operating Frequency	fo		45	50	55	kHz
Burst Frequency	f <sub>BURST</sub>	Output Burst Frequency	165	200	235	Hz



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# **ELECTRICAL CHARACTERISTICS (CONTINUED)**

Unless otherwise specified, the following specifications apply over the recommended operating condition and ambient temperature of  $0^{\circ}$ C to  $60^{\circ}$ C except where otherwise noted; BRITE  $\geq 2.5$ V,  $\overline{\text{SLEEP}} \geq 2.0$ V,  $V_{IN} = 12$ V

Parameter	Symbol Test Conditions		LXMG1627-12-6x			Units
Farameter	Syllibol	Test Conditions	Min	Тур	Max	Units
▶ BRITE INPUT						
Input Current		BRITE = 0V		-13.4		μΑ
Input Current	I <sub>BRT</sub>	BRITE = 3V		-5		μΑ
Minimum Input for Max. Lamp Current	V <sub>BRT_ADJ</sub>	I <sub>O(LAMP)</sub> = Maximum Lamp Current	2.1	2.3	2.5	V
Maximum Input for Min. Lamp Current	V <sub>BRT_ADJ</sub>	I <sub>O(LAMP)</sub> = Minimum Lamp Current	0			V
SLEEP INPUT						
RUN Mode	V <sub>SLEEP</sub>		2.0		V <sub>IN</sub>	V
SLEEP Mode	V <sub>SLEEP</sub>		0		0.8	V
SET <sub>1,2</sub> INPUT						
SET <sub>1,2</sub> Low Threshold	VL		0		0.4	V
Input Current	I <sub>SET</sub>	SETx = 0V		-85		μΑ
POWER CHARACTERISTICS						
Sleep Current	I <sub>IN(MIN)</sub>	SLEEP ≤ 0.8V		2	20	μΑ
Run Current	I <sub>IN(RUN)</sub>	SET <sub>1</sub> = Open, SET <sub>2</sub> = Ground, V <sub>LAMP</sub> = 600V <sub>RMS</sub>		875		mA
Strike (Open Lamps)	T <sub>S_DWELL</sub>		1.0	1.5		Sec
Supply Current After Fault Timeout	I <sub>FAULT</sub>	Fault Timeout		7		mA
Efficiency	η	SET <sub>1</sub> = Open, SET <sub>2</sub> = Ground, $V_{LAMP} = 600V_{RMS}$		80		%

	FUNCTIONAL PIN DESCRIPTION							
Conn	Pin	DESCRIPTION						
CN1 (Molex 53261-0871) Mates with 51021-0800 housing, 50079-8100 pins. Mates with LX9501G input cable assembly								
CN1-1	$V_{IN}$	Main Input Power Supply ( $10.8V \le V_{IN} \le 13.2V$ )						
CN1-2	V IIN							
CN1-3	GND	Power Supply Return						
CN1-4	OND	Fower Supply Neturn						
CN1-5	SLEEP	ON/OFF Control. (0V ≤ SLEEP ≤ 0.8 = OFF, SLEEP ≥ 2.0V = ON						
CN1-6	BRITE	Brightness Control (0V to 2.5V). 2.5V gives maximum lamp current; 500k manual pot; PWM signal.						
CN1-7 SET <sub>1</sub> MSB Connecting this pin to ground decreases the output current (see Table 1)								
CN1-8 SET <sub>2</sub> SET <sub>2</sub> LSB Connecting this pin to ground decreases the output current (see Table 1)								
CN2, CN3 for LXMG1627-12-61 and -62 (JST SM02(8.0)B-BHS-1-TB(LF)(SN)   Yeon Ho 20015WR-05A00 or SM02B-BHSS-1-TB(LF)(SN)   Yeon Ho 35001WR-02A00)								
CN2-1 CN3-1	V <sub>HI</sub>	High voltage connection to high side of lamp. Connect to lamp terminal with shortest lead length. <b>DO NOT</b> connect to Ground.						
CN2-2 CN3-2	$V_{LO}$	Connection to low side of lamp. Connect to lamp terminal with longer lead length. <b>DO NOT</b> connect to Ground						



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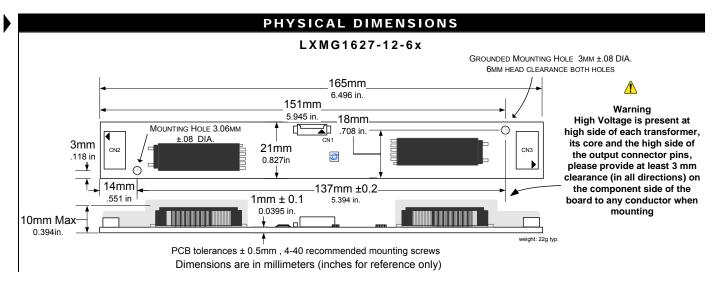
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# TABLE 1

#### **OUTPUT CURRENT SETTINGS**

SET₁ (Pin 7)	SET₂ (Pin 8)	Nominal Output Current
Open*	Open*	8.0mA
Open*	Ground	7.0mA
Ground	Open*	6.0mA
Ground	Ground	5.0mA

<sup>\*</sup> If driven by a logic signal it should be open collector or open drain only, not a voltage source.



# SIMPLIFIED BLOCK DIAGRAM High Voltage Controller Transformer 249k Comparator BRITE Transformer 49.9k Ramp Driver LAMP Ramp Gen SET<sub>1</sub> +4.2V $\mathsf{OV}_{\mathsf{SENSE}}$ SENSE SET<sub>2</sub> $V_{LO}$ One of two



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### TYPICAL APPLICATION

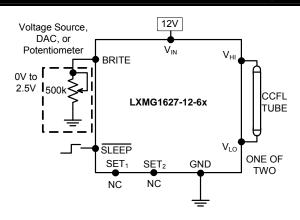


Figure 1 – Brightness Control (Output current set to maximum)

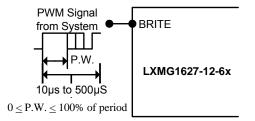
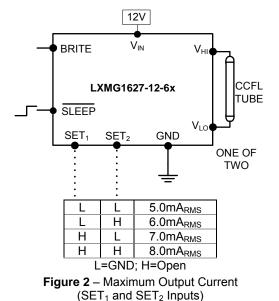


Figure 1A - PWM Brightness Control



to bias the pot. PWM Logic levels (figure 1A) may be used up to 5V, however the inverter will reach maximum current at less than 100% duty cycle. This can be calculated as approximately 2.3V divided by the logic high voltage level; with 3.3V logic level this corresponds to about 70% duty cycle for maximum lamp current.

If you need to turn the inverter ON/OFF remotely, connect to TTL logic signal to the SLEEP input.

The brightness control may be a voltage output DAC or

other voltage source, a digital pot or 500k manual pot. The

inverter contains an internal 300k pull-up to typically 4.2V

- Connect  $V_{HI}$  to high voltage wire from the lamp. Connect  $V_{LO}$  to the low voltage wire (wire with thinner insulation). Never connect  $V_{LO}$  to circuit ground as this will defeat lamp current regulation. If both lamp wires have heavy high voltage insulation, connect the longest wire to  $V_{LO}$ . This wire is typically white.
- Use the SET₁ and SET₂ (see Figure 2) inputs to select the desired maximum output current. Using these two pins in combination allows the inverter to match a wide variety of panels from different manufacturers. Generally the best lamp lifetime correlates with driving the CCFL at the manufacturer's nominal current setting. However the SET₁ and SET₂ inputs allow the user the flexibility to adjust the current to the maximum allowable output current to increase panel brightness at the expense of some reduced lamp life.
- Although the SET pins are designed such that just leaving them open or grounding them is all that is needed to set the output current, they can also be actively set. Using a open collector or open drain logic signal will allow you to reduce the lamp current for situations where greater dim range is required, as an example in nighttime situations. Since the dim ratio is a factor of both the burst duty cycle and the peak output current, using this technique the effective dim ratio can be increased greater than the burst duty cycle alone. Conversely the SET inputs could be used to overdrive the lamp temporarily to facilitate faster lamp warm up at initial lamp turn on. Of course any possible degradation on lamp life from such practices is the users responsibility since not all lamps are designed to be overdriven.
- The inverter has a built in fault timeout function. If either or both outputs are open (lamp disconnected or broken) the inverter will attempt to strike for about a 1.5 seconds and then shutdown for safety purposes. In order to restart the inverter it is necessary to toggle the sleep input or cycle the V<sub>IN</sub> input supply.



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NOTES

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