			First Edition	Approved by	Production Div
10	TD Modulo Sp	ngification	June 2, 1997	Checked by	Quality Assurance Div
L	CD Module Sp	ecincation	Final Revision	Oh a alvad hv	Section Franks and a Physical
			*****	Checked by	Design Engineering Div
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		<u>Re</u>	vision History		
Rev.	Date	Page		Comment	
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# 1. General Specifications

Operating Temp. : min.  $10^{\circ}$ C  $\sim$  max.  $40^{\circ}$ C

Storage Temp. : min.  $-20^{\circ}$ C  $\sim$  max.  $60^{\circ}$ C

Dot Pixels :  $256 \text{ (W)} \times 128 \text{ (H) dots}$ 

Dot Size :  $0.43 \text{ (W)} \times 0.43 \text{ (H)} \text{ mm}$ 

Dot Pitch :  $0.47 \text{ (W)} \times 0.47 \text{ (H)} \text{ mm}$ 

Viewing Area :  $127.0 \text{ (W)} \times 70.0 \text{ (H)} \text{ mm}$ 

Outline Dimensions : 167.0 (W)  $\times$  116.0 (H)  $\times$  24.0 (D) mm

Weight : 250g max.

LCD Type : NSD-7451

(STN / Blue-mode / Transflective)

Viewing Angle : 6:00

Data Transfer : 4-bit parallel data transfer

Backlight : Cold Cathode Fluorescent Lamp (CFL)  $\times$  1

Drawings : Dimensional Outline UE-32050B

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# 2. Electrical Specifications

# 2.1. Absolute Maximum Ratings

 $V_{SS}=0V$ 

Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage	V <sub>CC</sub> -V <sub>SS</sub>	_	-0.3	6.0	V
(Logic)					
Supply Voltage	V <sub>CC</sub> -V <sub>EE</sub>	_	0	30.0	V
(LCD Drive)					
Input Voltage	VI	_	-0.3	V <sub>CC</sub> +0.3	V

# 2.2.DC Characteristics

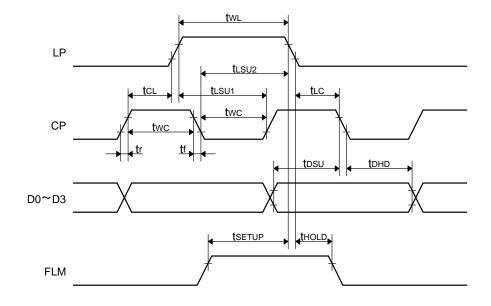
 $Ta=25^{\circ}C$ ,  $V_{SS}=0V$ 

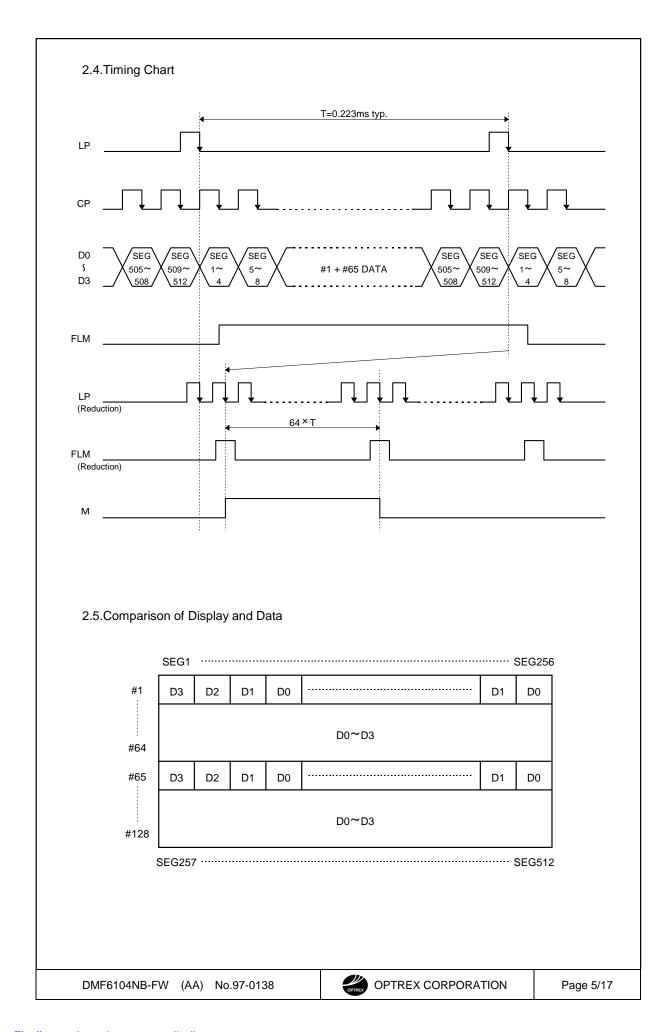
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V <sub>CC</sub> -V <sub>SS</sub>	_	4.5	_	5.5	V
(Logic)						
Supply Voltage	V <sub>CC</sub> -V <sub>EE</sub>		Shown in 3.	1		V
(LCD Drive)						
High Level	$V_{\mathrm{IH}}$	$V_{CC}=5.0V\pm10\%$	$0.8 \times V_{CC}$	_	$V_{CC}$	V
Input Voltage						
Low Level	$V_{\rm IL}$	$V_{CC}=5.0V\pm10\%$	0	_	$0.2 \times V_{CC}$	V
Input Voltage						
High Level	$V_{\mathrm{OH}}$	I <sub>OH</sub> =-0.2mA	Vcc-0.4	_	$V_{CC}$	V
Output Voltage						
Low Level	Vol	IoL=0.2mA	0		0.4	V
Output Voltage						
	Icc	V <sub>CC</sub> -V <sub>SS</sub> =5.0V		5.0	20.0	mA
Supply Current						
	IEE	V <sub>CC</sub> -V <sub>EE</sub> =13.8V	_	4.7	15.0	mA

# 2.3.AC Characteristics

 $V_{CC}\!\!=\!\!5.0V\!\pm\!10\%$ 

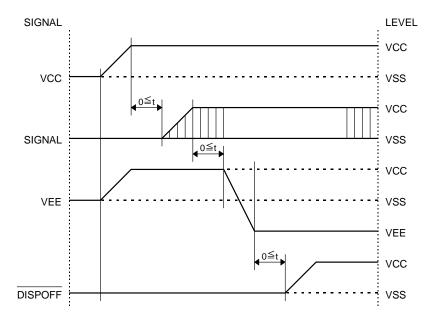
Parameter	Symbol	Min.	Max.	Units
Clock Pulse Width	$t_{ m WC}$	100	_	ns
Latch Pulse High Level Width	$t_{ m WL}$	125	_	ns
Clock Pulse Rise/Fall Time	tr, tf	_	50	ns
CP→LP Rise Time	$\mathbf{t}_{\mathrm{CL}}$	63	_	ns
CP→LP Fall Time	$t_{LSU2}$	90	_	ns
LP→CP Rise Time	t <sub>LSU1</sub>	90	_	ns
LP→CP Fall Time	$\mathbf{t}_{ ext{LC}}$	63	_	ns
Data Setup Time	$t_{ m DSU}$	50	_	ns
Data Hold Time	$t_{ ext{DHD}}$	80	_	ns
FLM Data Setup Time	$t_{ m SETUP}$	100	_	ns
FLM Data Hold Time	$t_{HOLD}$	100	_	ns



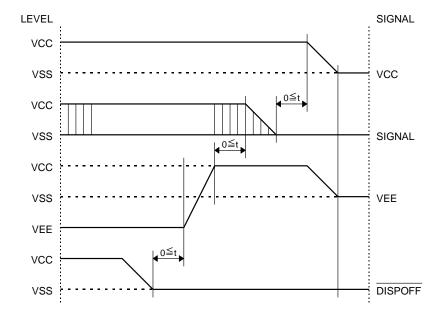


# 2.6. Power Supply ON/OFF Sequence

# 2.6.1.ON Sequence



# 2.6.2.OFF Sequence



Please maintain the above sequence when turning on and off the power supply of the module. If  $\overline{\text{DISPOFF}}$  is supplied to the module while alternate signal for LCD driving (M) is unstable, DC component will be supplied to the LCD panel. This may cause damage the LCD module.

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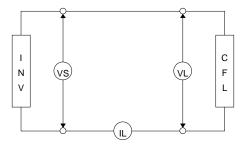
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#### 2.7.Lighting Specifications

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units	Notes
Lamp Voltage	$V_{\rm L}$	I	ı	250	_	Vrms	1
Lamp Current	$I_L$	I	4.0	5.0	6.0	mArms	2
Starting Voltage	Vs	_	600	_	_	Vrms	3
Surface Luminance	L	I <sub>L</sub> =5.0mA	50	_	_	cd/m²	4
Average Life	TAL	I <sub>L</sub> =5.0mA	_	20000	_	hrs	5

- Note 1 : The voltage ( r.m.s. ) to maintain the electric discharge of the lamp. It is measured after lighting for 3 minutes .
- Note 2 : The current (r.m.s.) to flow through the lamp with the electric discharge. It is measured after lighting for 3 minutes.
- Note 3: The voltage at starting the electric discharge when the voltage is increased gradually from 0V.
- Note 4 : Surface Luminance is specified by the initial data of luminance measured at the center of display surface after 20 minutes power on. ( All ON pattern )
- Note 5 : CFL life is defined as the time for which the initial luminance is attenuated by 50% of the luminance value. Average Life representes the time elapsed at the point of time when the residual ratio becomes below 50% when plural lamps are lighted in comparison with the definition of life mentioned above.



**CFL Testing Circuit** 

#### 3. Optical Specifications

#### 3.1.LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended		Ta=10°C	_	_	16.3	V
LCD Driving Voltage	V <sub>CC</sub> -V <sub>EE</sub>	Ta=25°C	12.8	13.8	14.8	V
Note 1		Ta=40°C	11.5	_	_	V

Note 1: Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

#### 3.2. Optical Characteristics

Ta=25°C, 1/64 Duty, 1/9 Bias,  $V_D=13.8V$  (Note 4),  $\theta = 0^{\circ}$ ,  $\phi = -^{\circ}$ 

			- / - /				
Parameter		Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast Rat	tio Note 1	CR	$\theta = 0^{\circ}$ , $\phi = -^{\circ}$	_	4.5	_	
Viewing Angle			Shown in 3.3				
Response	Rise Note 2	Ton	_	_	60	120	ms
Time	Decay Note 3	Toff	_	_	250	380	ms

Note 1: Contrast ratio is definded as follows.

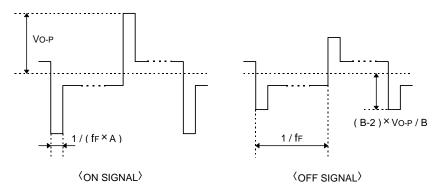
 $CR = L_{ON} / L_{OFF}$ 

Lon: Luminance of the ON segments

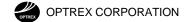
Loff: Luminance of the OFF segments

- Note 2: The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.
- Note 3 : The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.
- Note 4: Definition of Driving Voltage V<sub>D</sub>

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias ( A: Duty Number, B: Bias Number ). Driving voltage  $V_D$  is definded as the voltage  $V_{O-P}$  when the contrast ratio (CR= $L_{ON}$  /  $L_{OFF}$ ) is at its maximum.

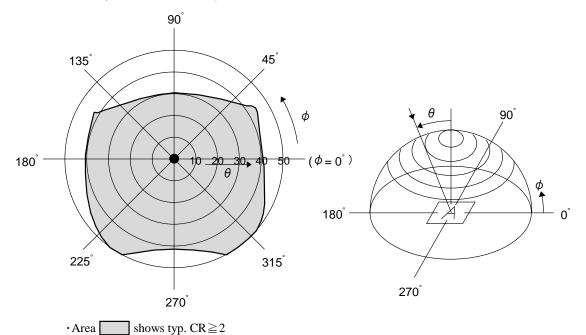


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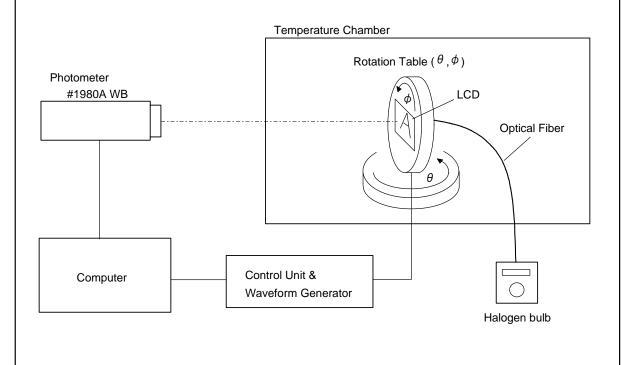


# 3.3. Definition of Viewing Angle and Optimum Viewing Area

- •Point  $\bullet$  shows the point where contrast ratio is measured. :  $\theta = 0^{\circ}$ ,  $\phi = -^{\circ}$
- Driving condition : 1/64 Duty, 1/9 Bias,  $V_D$ =13.8V,  $f_F$ =70Hz



3.4. System Block Diagram



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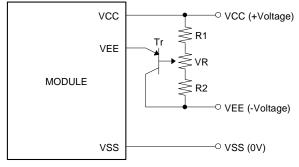
# 4.I/O Terminal

# 4.1.Pin Assignment

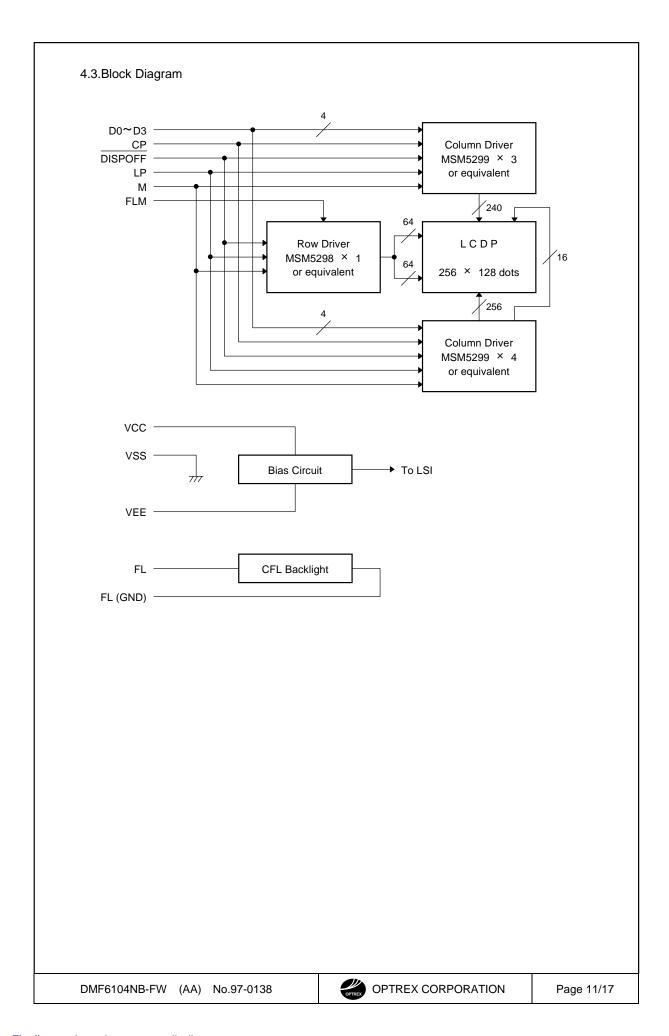
No.	Symbol	Level	Function
1	$V_{CC}$		Power Supply for Logic
2	Vss	_	Power Supply (0V, GND)
3	$V_{\rm EE}$		Power Supply for LCD Drive
4	LP	$H{ ightarrow}L$	Data Latch Signal
5	M	H/L	Alternate Signal for LCD Drive
6	DISPOFF	H/L	Display Control Signal H: Display on L: Display off
7	NC	ı	Non-connection
8	FLM	H/L	First Line Marker
9	СР	$H{ ightarrow}L$	Clock Signal for Shifting Data
10	NC	_	Non-connection
11	D0	H/L	Display Data
12	D1	H/L	Display Data
13	D2	H/L	Display Data
14	D3	H/L	Display Data
15	FL		Power Supply for CFL (HOT)
16	FL (GND)	_	Power Supply for CFL (GND)

# 4.2. Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.



R1+R2+VR=10 $\sim$ 20K  $\Omega$ Tr=2SA1202 or equivalent



# 5. Test

No change on display and in operation under the following test condition.

No.	Parameter	Conditions	Notes
1	High Temperature Operating	$40^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , 96hrs (operation state)	
2	Low Temperature Operating	$10^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , 96hrs (operation state)	3
3	High Temperature Storage	60°C ±2°C, 96hrs	4
4	Low Temperature Storage	-20°C ±2°C, 96hrs	3, 4
5	Damp Proof Test	40°C ±2°C, 90∼95%RH, 96hrs	3, 4
6	Vibration Test	Total fixed amplitude : 1.5mm  Vibration Frequency : $10\sim55$ Hz  One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes	5
7	Shock Test	To be measured after dropping from 60cm high on the concrete surface in packing state.  Dropping method corner dropping A corner : once  Edge dropping B,C,D edge : once Face dropping E,F,G face : once	

Note 1: Unless otherwise specified, tests will be conducted under the following condition.

Temperature :  $20\pm5^{\circ}$ C Humidity :  $65\pm5\%$ 

Note 2: Unless otherwise specified, tests will be not conducted under functioning state.

Note 3: No dew condensation to be observed.

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Note 4 : The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5: Vibration test will be conducted to the product itself without putting it in a container.

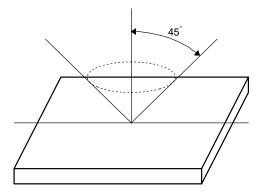
# 6. Appearance Standards

# 6.1.Inspection conditions

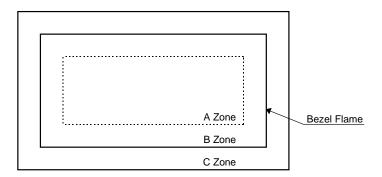
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



# 6.2. Definition of applicable Zones



A Zone: Active display area

B Zone: Area from outside of "A Zone" to validity viewing area

C Zone: Rest parts

A Zone + B Zone = Validity viewing area

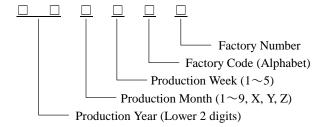
# 6.3.Standards

No.	Parameter				Criteria			
1	Black and	(1	) Round Shap	e				
	White Spots,			Zone	Acceptable Number			
	Foreign Substances		Dimension (r	nm)	A	В	С	
				D ≦0.1	*	*	*	
			0.1 < 1	D ≦0.2	3	5	*	
			0.2 < 1	D ≦0.25	2	3	*	
			0.25<	D ≦0.3	0	1	*	
			0.3 < 1	D	0	0	*	
			D = ( Long -	+ Short ) / 2	* : Disregar	d		
		(2	) Line Shape					
				Zone	Acceptable Number			
			X (mm)	Y (mm)	A	В	С	
			_	0.03≧W	*	*	*	
			2.0≧L	0.05≧W	3	3	*	
			1.0≧L	0.1 ≧W	3	3	*	
			_	0.1 < W	In	the same way (	(1)	
			X : Length	Y: Width	* : Disregard	l		
		To	otal defects sh	all not exceed	5.			
2	Air Bubbles				T			
	(between glass			Zone	Ac	ceptable Numb	per	
	& polarizer)		Dimension (r	nm)	A	В	С	
				$D \leq 0.3$	*	*	*	
			0.3 < 1	$D \leq 0.4$	3	*	*	
			0.4 < 1	$D \leq 0.6$	2	3	*	
			0.6 < 1	D	0	0	*	
			* : Disregard					
		To	otal defects sh	all not exceed	3.			

No.	Parameter	Criteria	
3	The Shape of Dot	(1) Dot Shape (with Dent)	
		0.15≧::	
		As per the sketch	n of left hand.
		(2) Dot Shape (with Projection)	
			.14
		Should not be connected	ed to next dot.
		(3) Pin Hole	
		_ <u></u>	
		(X+Y)	⁄ 2≦0.2mm
		(Less than 0.1mm is	
		(4) Deformation	
		$Y = \{X \in X \mid X \in X\}$	/ 2≦0.2mm
		X	
		Total acceptable number: 1/dot, 5/cell	
		(Defect number of (4): 1pc.)	
4	Polarizer Scratches	Not to be conspicuous defects.	
5	Polarizer Dirts	If the stains are removed easily from LCDP surface, the mo	dule is not
		defective.	
6	Complex Foreign	Black spots, line shaped foreign substances or air bubbles b	etween
	Substance Defects	glass & polarizer should be 5pcs maximum in total.	
7	Distance between	$D \leq 0.2: 20$ mm or more	
	Different Foreign	0.2 < D : 40mm or more	
	Substance Defects		

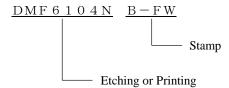
# 7. Code System of Production Lot

The production lot of module is specified as follows:



# 8. Type Number

The type number of module is specified on the back of module as follows:



# 9. Applying Precautions

Please contact us when questions and/or new problems not specified in this specifications arise.

#### 10. Handling Precautions

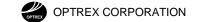
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- 1) LCD may be broken because it is made of glass.
- 2) Polarizer is a soft material and can easily be scratched.
- 3) Please avoid static electricity.
- ① Please be sure to ground human body and electric appliances during work.
- ② It is preferable to use conductive mat on table and wear cotton clothes or conduction processed fiber. Synthetic fiber is not recommended.
- ③ Please slowly peel off protective film, because static electricity may be charged.
- 4) If it is necessary to store LCD modules for a long time, please comply with the following procedures. If storage condition is not satisfactory, display (especially polarizer) may be deteriorated or soldering I/O terminals may become difficult (some oxide is generated at I/O terminals plating).
  - ① Store as delivered by Optrex
  - ② If you store as unpacked, put in anti-static bag, seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
  - ③ Store at temperature 0 to  $+35^{\circ}$ C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.
- 5) The module does not contain excess current limiter.

  Please design the limiter to cut excess current in your power supply circuit.
- 6) Liquid crystal may be leaked when display is broken. Never taste it. If your hands or clothes touch it, please immediately wash using soap.
- 7) The connection between the bezel and Vss (GND) is not specified in the module. (Some module do not maintain connection between them.) Please consult OPTREX to specify the connection.
- 8) A high voltage over 1000V is applied at the connector of CFL cable when the CFL inverter is connected and energized. Please do not touch there incidentally or accidentally to avoid a skin burn. And please set the cable properly in the housing to avoid a worn-out of isolated cover of cable wire.

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