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			First Edition	Approved by	Production Div.
		• 60	March 27, 1997	Checked by	Quality Assurance Div.
	CD Module Sp	ecification	Final Revision		
			*****	Checked by	Design Engineering Div.
Type No.	DMF-50202NY-1	LY	Л	Prepared by	Production Div.
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Rev.	Date	Page		Comment	

OPTREX CORPORATION

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DMF-50202NY-LY (AD) No.97-0075

1.General Specifications

Operating Temp. : min. 0° C ~ max. 50° C

Storage Temp. : $\min -20^{\circ}\text{C} \sim \max .60^{\circ}\text{C}$

Dot Pixels : $128 \text{ (W)} \times 64 \text{ (H)} \text{ dots}$

Dot Size : $0.40 \text{ (W)} \times 0.56 \text{ (H)} \text{ mm}$

Dot Pitch : $0.44 \text{ (W)} \times 0.60 \text{ (H)} \text{ mm}$

Viewing Area : $62.0 \text{ (W)} \times 44.0 \text{ (H)} \text{ mm}$

Outline Dimensions : $95.0 \text{ (W)} \times 58.0 \text{ (H)} \times 15.5 \text{ max. (D)} \text{ mm}$

LCD Type : NTD-7496

(STN / Yellow-mode / Transmissive)

Viewing Angle : 6:00

Data Transfer : Serial data transfer

Backlight : LED Backlight / Yellow-green

Drawings : Dimensional Outline UE-34641D

2. Electrical Specifications

2.1. Absolute Maximum Ratings

Vss=0V

Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage	V _{DD} -V _{SS}	_	-0.3	7.0	V
(Logic)					
Supply Voltage	VDD-VEE	_	0	30.0	V
(LCD Drive)					
Input Voltage	VI	_	-0.3	V _{DD} +0.3	V

2.2.DC Characteristics

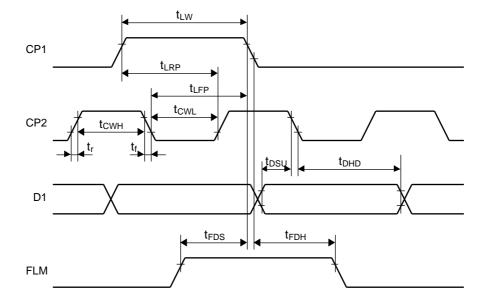
Ta=25°C, Vss=0V

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{DD} -V _{SS}	_	4.5	_	5.5	V
(Logic)						
Supply Voltage	VDD-VEE		Shown in 3	.1		V
(LCD Drive)						
High Level	Vih	V _{DD} =5.0V±10%	VDD-0.8	_	V_{DD}	V
Input Voltage						
Low Level	VIL	V _{DD} =5.0V±10%	0	_	0.8	V
Input Voltage						
High Level	Voh	Іон=-0.5mA	V _{DD} -0.3	_	V_{DD}	V
Output Voltage						
Low Level	Vol	IoL=0.5mA	0	_	0.3	V
Output Voltage						
	Idd	V _{DD} -V _{SS} =5.0V	_	1.0	1.5	mA
Supply Current						
	I EE	VDD-VEE=13.2V	_	0.8	1.2	mA

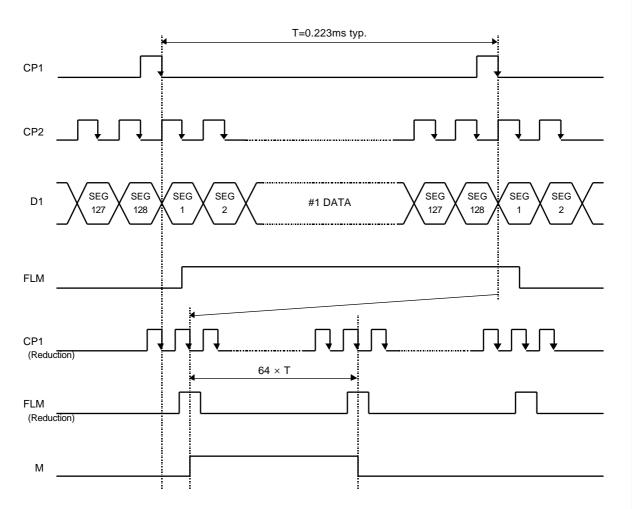
2.3.AC Characteristics

 $V_{DD}\!\!=\!\!5.0V\!\!\pm\!\!10\%$

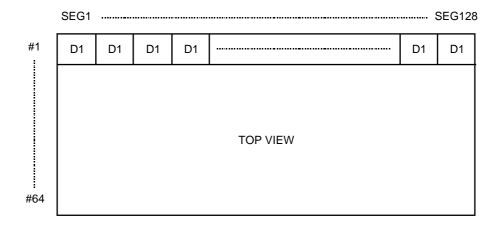
Parameter	Symbol	Min.	Max.	Units
Clock Pulse High Level Width	tсwн	95	ı	ns
Clock Pulse Low Level Width	t cwl	95	ı	ns
Clock Pulse Rise/Fall Time	tr, tf	_	30	ns
Latch Pulse High Level Width	tlw	40	ı	ns
Latch Pulse Rise Time	t lrp	20	ı	ns
Latch Pulse Fall Time	t lfp	40	ı	ns
Data Setup Time	t dsu	20	ı	ns
Data Hold Time	t dhd	40	ı	ns
Frame Data Setup Time	tfds	30		ns
Frame Data Hold Time	t fdh	50	-	ns





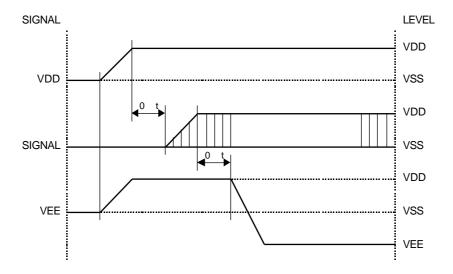


2.5. Comparison of Display and Data

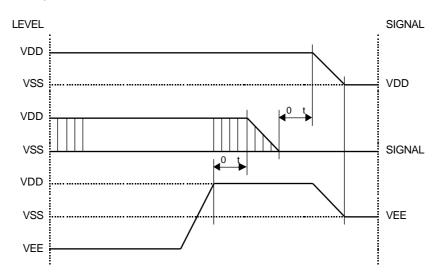


2.6. Power Supply ON/OFF Sequence

2.6.1.ON Sequence



2.6.2.OFF Sequence



Please maintain the above sequence when turning on and off the power supply of the module. If VEE is supplied to the module while alternate signal for LCD driving (M) is unstable, DC component will be supplied to the LCD panel. This may cause damage the LCD module.

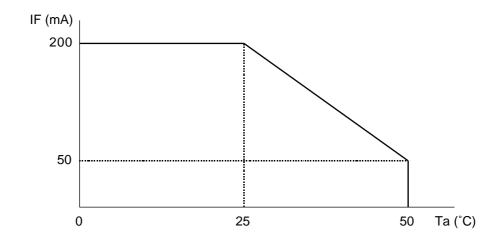
2.7.Lighting Specifications

2.7.1. Absolute Maximum Ratings

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Foward Current	I F	Note 1	_	ı	200	mA
Reverse Voltage	VR	_	_	_	16	V
LED Power Dissipation	PD	_	_	_	1.7	W

Note 1: Refer to the foward current derating curve.



2.7.2. Operating Characteristics

Ta=25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Foward Voltage	$V_{\rm F}$	$I_F = 100 \text{mA}$	7.6	8.2	8.8	V
Luminance of	L	$I_F = 100 \text{mA}$	60	-	-	cd/m ²
Backlight Surface						

3. Optical Specifications

3.1.LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended		Ta= 0°C	_	_	15.4	V
LCD Driving Voltage	Vdd-Vee	Ta=25°C	12.3	13.2	14.1	V
Note 1		Ta=50°C	10.9	_	_	V

Note 1: Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2. Optical Characteristics

 $Ta=25^{\circ}C$, 1/64 Duty, 1/9 Bias, VD=13.2V (Note 4), $=0^{\circ}$, $=-^{\circ}$

Pa	rameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast R	atio Note 1	CR	= 0°, = -°	ı	7	_	
Viewing An	gle		Shown in 3.3				
Response Rise Note 2		r	_	_	160	240	ms
Time Decay Note 3		d		_	160	240	ms

Note 1: Contrast ratio is definded as follows.

CR = Loff / Lon

Lon: Luminance of the ON segments

Loff: Luminance of the OFF segments

Note 2 : The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3: The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

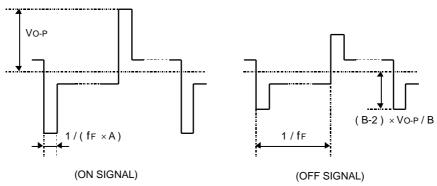
Note 4: Definition of Driving Voltage VD

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A: Duty Number, B: Bias Number). Driving voltage V_D is definded as follows.

 $V_D = (Vth1 + Vth2) / 2$

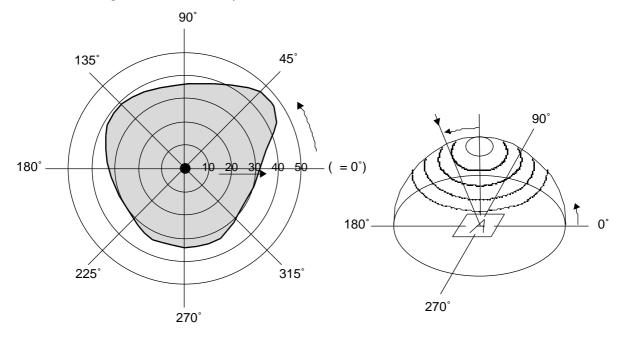
Vth1 :The voltage Vo-P that should provide 50% of the satulation level in the luminance at the segment which the ON signal is applied to.

 $\label{thm:continuous} Wth 2: The \ voltage \ Vo-P \ that \ should \ provide \ 50\% \ of \ the \ satulation \ level \ in \ the \ luminance \ at \ the \ segment \ which \ the \ OFF \ signal \ is \ applied \ to.$



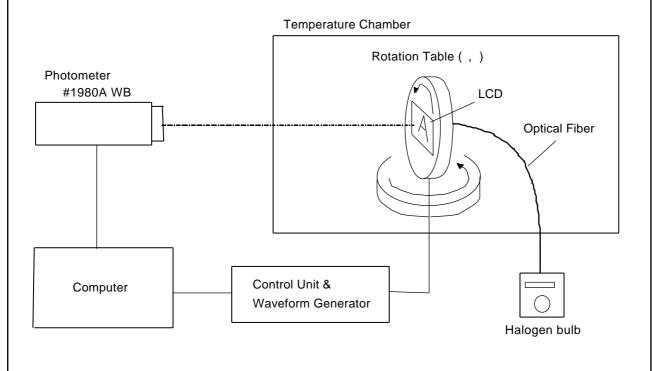
3.3. Definition of Viewing Angle and Optimum Viewing Area

- •Point \bullet shows the point where contrast ratio is measured. : = 0°, = -°
- •Driving condition: 1/64 Duty, 1/9 Bias, VD=13.2V, fF=70Hz



•Area shows typ. CR 2

3.4. System Block Diagram



4.I/O Terminal

4.1.Pin Assignment

CN1

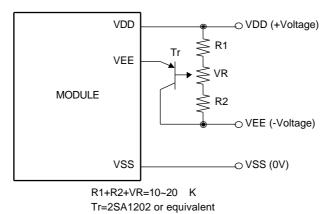
No.	Symbol	Level	Function
1	FLM	H/L	First Line Marker
2	CP1	H L	Data Latch Signal
3	CP2	H L	Clock Signal for Shifting Data
4	M	H/L	Alternate Signal for LCD Drive
5	D1	H/L	Display Data
6	V _{DD}	_	Power Supply for Logic
7	Vss	_	Power Supply (0V, GND)
8	VEE	_	Power Supply for LCD Drive

CN2

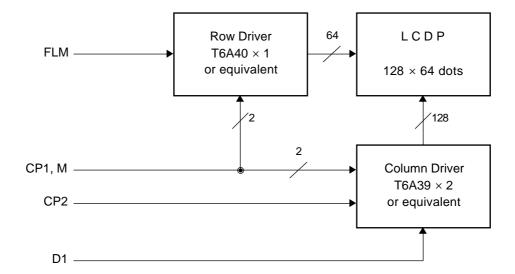
No.	Symbol	Level	Function		
1	LED: A	_	LED Anode Terminal		
2	LED: C	_	LED Cathode Terminal		

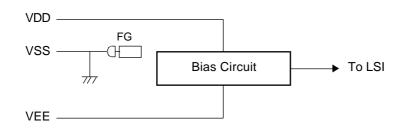
4.2. Example of Power Supply

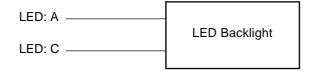
It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.



4.3.Block Diagram







5.Test

No change on display and in operation under the following test condition.

No.	Parameter	Conditions	Notes
1	High Temperature Operating	50°C±2°C, 96hrs (operation state)	
2	Low Temperature Operating	0°C±2°C, 96hrs (operation state)	3
3	High Temperature Storage	60°C±2°C, 96hrs	4
4	Low Temperature Storage	-20°C±2°C, 96hrs	3, 4
5	Damp Proof Test	40°C±2°C, 90~95%RH, 96hrs	3, 4
6	Vibration Test	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes	5
7	Shock Test	To be measured after dropping from 60cm high on the concrete surface in packing state. Dropping method comer dropping A corner: once Edge dropping B,C,D edge: once Face dropping E,F,G face: once	

Note 1: Unless otherwise specified, tests will be conducted under the following condition.

Temperature : $20\pm5^{\circ}$ C Humidity : $65\pm5\%$

Note 2: Unless otherwise specified, tests will be not conducted under functioning state.

Note 3: No dew condensation to be observed.

Note 4: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5: Vibration test will be conducted to the product itself without putting it in a container.

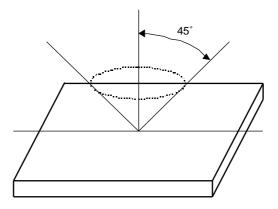
6. Appearance Standards

6.1.Inspection conditions

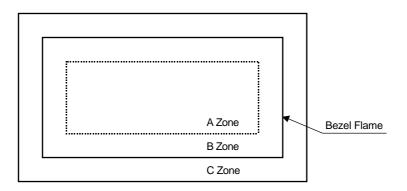
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



6.2. Definition of applicable Zones



A Zone : Active display area

B Zone: Area from outside of "A Zone" to validity viewing area

C Zone: Rest parts

A Zone + B Zone = Validity viewing area

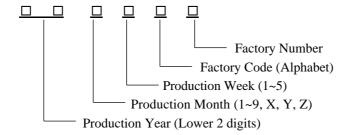
6.3.Standards

No.	Parameter		Criteria				
1	Black and	(1) Round Shap	e				
	White Spots,		Zone	Ac	ceptable Num	ber	
	Foreign Substances	Dimension (r	nm)	A	В	С	
		D	0.1	*	*	*	
		0.1 < D	0.2	5	5	*	
		0.2 < D	0.3	0	1	*	
		0.3 < D		0	0	*	
		D = (Long + (2) Line Shape	D = (Long + Short) / 2 *: Disregard (2) Line Shape				
			Zone	Ac	ceptable Num	ber	
		X(mm)	Y(mm)	A	В	С	
		_	0.02 W	*	*	*	
		2.0 L	0.03 W	3	3	*	
		1.0 L	0.04 W	1	2	*	
		1.0 L	0.05 W	0	2	*	
		_	0.05 <w< td=""><td colspan="4">In the same way (1)</td></w<>	In the same way (1)			
		X : Length ` Total defects sh		: Disregard 5.			
2	Air Bubbles						
	(between glass		Zone	Ac	ceptable Num	ber	
	& polarizer)	Dimension (r	nm)	A	В	С	
		D	0.15	*	*	*	
		0.15< D	0.3	2	3	*	
		0.3 < D	0.5	1	2	*	
		0.5 < D	1.0	0	1	*	
		* : Disregard Total defects sh	all not exceed	3.			

No.	Parameter	Criteria
3	The Shape of Dot	(1) Dot Shape (with Dent) 0.15 As per the sketch of left hand.
		(2) Dot Shape (with Projection) Should not be connected to next dot. (3) Pin Hole
		(X+Y)/2 0.2mm (Less than 0.1mm is no counted.) Total defects shall not exceed 5.
4	Polarizer Scratches	Not to be conspicuous defects.
5	Polarizer Dirts	If the stains are removed easily from LCDP surface, the module is not defective.
6	Color Variation	Not to be conspicuous defects.

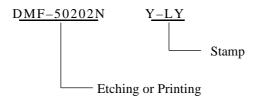
7.Code System of Production Lot

The production lot of module is specified as follows:



8. Type Number

The type number of module is specified on the back of module as follows:



9. Applying Precautions

Please contact us when questions and/or new problems not specified in this specifications arise.

10. Handling Precautions

Operex Products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment, measurement devices and etc..

Optrex Products are not designed, intended, or authorized for use in any application in which the failure of the product could result in a situation where personal injury or death may occur. These applications include, but are not limited to, life-sustaining equipment, nuclear control devices, aerospace equipment, devices related to hazardous or flammable materials, etc. (If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications, Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.) Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application (without such consent), Buyer shall indemnify and hold Optrex and its officers, employees, subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses, and reasonable attorney's fees, arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part.

- 1) LCD may be broken because it is made of glass.
- 2) Polarizer is a soft material and can easily be scratched.
- 3) Please avoid static electricity.
 - ① Please be sure to ground human body and electric appliances during work.
 - ② It is preferable to use conductive mat on table and wear cotton clothes or conduction processed fiber. Synthetic fiber is not recommended.
 - 3 Please slowly peel off protective film, because static electricity may be charged.
- 4) If it is necessary to store LCD modules for a long time, please comply with the following procedures. If storage condition is not satisfactory, display (especially polarizer) may be deteriorated or soldering I/O terminals may become difficult (some oxide is generated at I/O terminals plating).
 - ① Store as delivered by Optrex
 - ② If you store as unpacked, put in anti-static bag, seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
 - $^{\circ}$ Store at temperature 0 to +35 $^{\circ}$ C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.
- The module does not contain excess current limiter.
 Please design the limiter to cut excess current in your power supply circuit.
- 6) Liquid crystal may be leaked when display is broken. Never taste it. If your hands or clothes touch it, please immediately wash using soap.

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