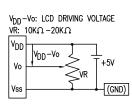


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PIN NO. SYMBOL LEVEL FUNCTION	/ 1	PIN CONFIGURATION	N /	A			
2	PIN NO.	SYMBOL	LEVEL	FUNCTION			
3	1	Vss	-		GND (OV)		
REGISTER SELECT SIGNAL	2	v_{DD}	-	POWER SUPPLY	5V		
4	3	Vo	-		FOR LCD DRIVE		
L: INSTRUCTION INPUT				REGISTER SELECT SIG	NAL		
5	4	Ao	H/L	H: DATA INPUT			
6 CS2 H,H->L CHIP SELECT SIGANL FOR IC2 7 CL H/L CLOCK INPUT (2kHz) 8 E H/L ENABLE SIGNAL (NO PULL-UP RESISTOR) 9 R/W H/L H: DATA READ (MODULE>MPU) L: DATA WRITE (MODULE <mpu) 0-7="" 10~17="" 18="" bit="" data="" db0~db7="" h="" l="" reset="" rst="" signal<="" td=""><td></td><td></td><td></td><td>L: INSTRUCTION INPU</td><td>Т</td></mpu)>				L: INSTRUCTION INPU	Т		
7 CL H/L CLOCK INPUT (2kHz) 8 E H/L ENABLE SIGNAL (NO PULL—UP RESISTOR) 9 R/W H/L H: DATA READ (MODULE—→MPU) 10~17 DB0~DB7 H/L DATA BIT 0−7 18 RST L RESET SIGNAL	5	CS1	H,H->L	CHIP SELECT SIGNAL FOR IC1			
8 E H/L ENABLE SIGNAL (NO PULL-UP RESISTOR) 9 R/W H/L H: DATA READ (MODULE>MPU) 10~17 DB0~DB7 H/L DATA BIT 0-7 18 RST L RESET SIGNAL	6	CS2	H,H->L	CHIP SELECT SIGANL FOR IC2			
9 R/W H/L H: DATA READ (MODULE>MPU) L: DATA WRITE (MODULE <mpu) 0-7="" 10~17="" 18="" bit="" data="" db0~db7="" h="" l="" reset="" rst="" signal<="" td=""><td>7</td><td>CL</td><td>H/L</td><td>CLOCK INPUT (2kHz)</td><td></td></mpu)>	7	CL	H/L	CLOCK INPUT (2kHz)			
9 R/W H/L L: DATA WRITE (MODULE <mpu) 0-7="" 10~17="" 18="" bit="" data="" db0~db7="" h="" l="" reset="" rst="" signal<="" td=""><td>8</td><td>E</td><td>H/L</td><td>ENABLE SIGNAL (NO</td><td>PULL-UP RESISTOR)</td></mpu)>	8	E	H/L	ENABLE SIGNAL (NO	PULL-UP RESISTOR)		
10~17 DB0~DB7 H/L DATA BIT 0-7 18 RST L RESET SIGNAL		p /W	ш /і	H: DATA READ (MODULE>MPU)			
18 RST L RESET SIGNAL	9	N/W	п/г	L: DATA WRITE (MODU	JLE <mpu)< td=""></mpu)<>		
	10~17	DB0~DB7	H/L	DATA BIT 0-7			
19 A - ANODE LED BACKLIGHT	18	RST	L	RESET SIGNAL			
	19	Α	-	ANODE	LED BACKLIGHT		
20 K – CATHODE LED BACKLIGHT	20	K	-	CATHODE	LED BACKLIGHT		



LOW ATZZOZOAA		
E.C.N. NUMBER AND REVISION COMMENTS	D/	ATE
SEE PAGE #1./		

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READ/WRITE TIMING FOR 68 PORT MPU

PART NUMBER

ICM-X12232GXX

Male/ White Himilite Folk do Folk in the								
PARAMETER	SIGNAL	SYMB0L	MIN	MAX	UNIT			
ADDRESS HOLD TIME	CS	tAH6	10	-	ns			
ADDRESS SETUP TIME	R/W	tas6	20	_	ns			
SYSTEM CYCLE TIME	Ao	tCYC6	1000	_	ns			
DATA SETUP TIME	DO~D7	tDS6	80	_	ns			
DATA HOLD TIME		tDH6	10	_	ns			
OUTPUT DISABLE TIME		tOH6	10	60	ns			
ACCESS TIME		tACC6	-	90	ns			
ENABLE DILICE WIDTH (READ)	E	4	100	_	ns			
ENABLE PULSE WIDTH (READ) (WRITE)		t _{EW}	80	_	ns			

ELECTRIC	CAL CHARAC	TERISTICS \	V _{DD} =4.5V to 5.5V, T _A =25°C					
ITEM		CAMBOI	CONDITION	STANDARD VALUE			UNIT	
IIEM		SYMBOL CO			MIN.	TYP.	MAX.	UNII
SUPPLY VO	LTAGE FOR	LOGIC	V _{DD} -Vss	-	-	5.0	-	٧
SUPPLY CURRENT FOR		LOGIC	I _{DD}	V _{DD} =5V	-	-	3.0	mA
INPUT VOLTAGE**		HIGH	VIH	-	0.2	-	V _{DD}	٧
		LOW	۷ _{IL}	-	0	-	0.8	٧
		HIGH	V _{OH}	$I_{OH} = -3mA$	2.4	-	-	٧
OUTPUT VO)LTAGE***	LOW	V _{OL}	I _{OL} =3ma	-	-	0.4	٧
	VOLTAGE		Vf	-	-	4.2	4.6	٧
*1 ED	CURRENT		If	-	-	170	330	mA

lf=170mA

710

70

*ONLY APPLIES TO MODULES WITH BACKLIGHT ▲ **APPLIED TO TERMINALS DB0~DB7

LUMINOUS

COLOR

POWER CUNSUMPTION

*LED

BACKLIGHT

***APPLIED TO TERMINALS E, Ao, DBO~DB7

/		,	١.
ABSOLUTE	MAXIMUM	RATINGS	/

REV.

	,					
ITEM	SYMBOL	TEST	STANDARD VALUE		UNIT	
IIEM		CONDITION	MIN	MAX	ONIT	
SUPPLY VOLTAGE FOR LOGIC	VDD-Vss	Ta=25*C	4.75	5.25	٧	
SUPPLY VOLTAGE FOR LCD DRIVE	VDD-Vo	-	4.8@50°C	5.2@0°C	٧	
INPUT VOLTAGE	٧١	Ta=25*C	Vss	۷DD	٧	
OPERATING TEMPERATURE	Topr	LCM-S	0	50	.c	
OPERATING TEMPERATURE		LCM-H	-20	70	.c	
STORAGE TEMPERATURE	Tstg	LCM-S	-20	70	.c	
STORAGE TEMPERATURE		LCM-H	-30	85	.c	

PART NUMBER REV. В LCM-X12232GXX

> 122 x 32 DOT MATRIX GHAPHIC MODULE, 1/16 DUTY, 1/5 BIAS.

PD

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cd/m²

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290 E. HELLEN ROAD [PALATINE, ILLINOIS 60067 PHONE: 1-847-359-2790 WEB: HTTP://WWW.LUMEX.COM

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