| | | | F | First Edition Jul 3, 2002 |
|----------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------|---------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| OPTREX | LCD Modu | ule Techni | cal Specification | Final Revision |
| Type No | C-51505N | IFQJ-LG-A | NF | |
| 1. 2. 3. 4. 5. 6. 7. 8. 9. 10 11 | Electrical Specification Optical Specifications I/O Terminal Test Appearance Standards Code System of Produ Type Number Applying Precautions . Precautions Relating | ns uction Lot Product Handling | Approved by (Production Checked by (Quality Assu- Checked by (Design Engin Prepared by (Production E K. Wague | m_{i} yru rance Div.) reering Di |
| Rev. | Date | Page Com | iment | |
| C- | 51505NFQJ-LG-AF (AF) | No. 2002-0183 | OPTREX CORPORATION | Page 1/18 |
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1.General Specifications

| Operating Temp. | : | min20°C ~ | max. 70°C | |
|--------------------------|---|------------------|-------------------------------------------------|-----------|
| Storage Temp. | : | min20°C ~ | max. 70°C | |
| Display Format | : | 20 characters | s × 2 lines | |
| Display Fonts | : | 5 × 8 dots | (1 character) | |
| Viewing Area | : | 83.0 (W) × 1 | 8.6 (H) mm | |
| Outline Dimensions | : | 116.0 (W) × | 37.0 (H) × (15.6) (D) mm | |
| Weight | : | 42.9g max. | | |
| LCD Type | : | | mode / Transmissive) | |
| Viewing Angle | : | 6:00 | | |
| Data Transfer | : | 8-bit parallel o | data transfer | |
| Backlight | : | LED Backligh | it / Green | |
| Additional Spec | : | | splay Specification ance Color Film is Used) | |
| Drawings | : | Dimensional (| Outline UE-311044 | |
| | | | | |
| | | | | |
| | | | | |
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| | | | | |
| | | | | |
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2.Electrical Specifications

2.1. Absolute Maximum Ratings

| | | | | | Vss=0V |
|----------------|---------|------------|------|---------|--------|
| Parameter | Symbol | Conditions | Min. | Max. | Units |
| Supply Voltage | Vcc-Vss | - | -0.3 | 7.0 | V |
| (Logic) | | | | | |
| Supply Voltage | Vcc-Vee | - | -0.3 | 13.5 | V |
| (LCD Drive) | | | | | |
| Input Voltage | Vi | - | -0.3 | Vcc+0.3 | V |
| | | | | | |

2.2. DC Characteristics

Ta=25°C, Vcc=5V, Vss=Vee=0V

| | | | | , | 00-01,100 | - |
|----------------|---------|--------------|------------|------|-----------|-------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
| Supply Voltage | Vcc-Vss | - | 4.5 | - | 5.5 | V |
| (Logic) | | | | | | |
| Supply Voltage | Vcc-Vee | | Shown in 3 | .1 | | V |
| (LCD Drive) | | | | | | |
| High Level | Vін | - | 2.2 | - | Vcc | V |
| Input Voltage | | | | | | |
| Low Level | Vil | - | -0.3 | - | 0.8 | V |
| Input Voltage | | | | | | |
| High Level | Vон | Іон=-0.25mA | 2.4 | - | - | V |
| Output Voltage | | | | | | |
| Low Level | Vol | loL=1.2mA | - | - | 0.4 | V |
| Output Voltage | | | | | | |
| Supply Current | lcc | Vcc-Vss=5.3V | - | 2.0 | 3.0 | mA |
| | | | | | | |

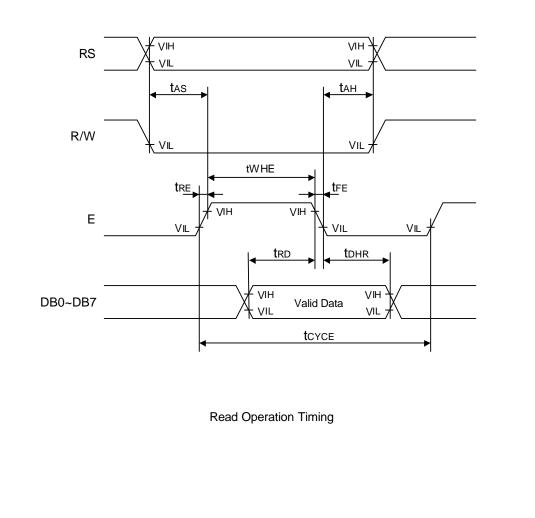
All voltage values are referenced to Vss =0V

 $V_1 \text{ to } V_5 \text{ must maintain } V_{DD} \geq V_1 \geq V_2 \geq V_3 \geq V_4 \geq V_5.$

2.3.AC Characteristic

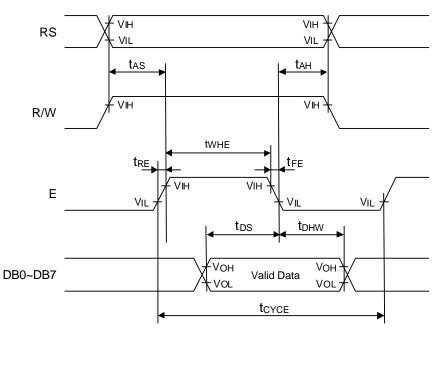
Read Operation

| | | | Vcc= | 5.0V±10% |
|--------------------------------------------------------------------|-------------------------------------|-----------|------|----------|
| Parameter | Symbol | Min. | Max. | Units |
| Enable Cycle Time | t _{CYCE} | 500 | - | ns |
| Enable "H" Level Pulse Width | t _{whe} | 300 | - | ns |
| Enable Rise/Fall Time | $\mathbf{t}_{RE}, \mathbf{t}_{FE}$ | - | 25 | ns |
| RS,R/W Setup Time (8-bit operation mode) (4-bit operation mode) | t _{AS} | 60 100 | - | ns |
| RS,R/W Address Hold Time | t _{AH} | 10 | - | ns |
| Read Data Output Delay | t _{RD} | - | 190 | ns |
| Read Data Hold Time | t _{DHR} | 20 | - | ns |



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| Write Operation | | | | |
|------------------------------------------|----------------------------------|------|------|----------|
| | | T | Vcc= | 5.0V±10% |
| Parameter | Symbol | Min. | Max. | Units |
| Enable Cycle Time | t _{CYCE} | 500 | - | ns |
| Enable "H" Level Pulse Width | twhe | 300 | - | ns |
| Enable Rise/Fall Time | t _{re,} t _{re} | - | 25 | ns |
| RS,R/W Setup Time (8-bit operation mode) | t _{AS} | 60 | _ | ns |
| (4-bit operation mode) | 4.0 | 100 | | 110 |
| RS,R/W Address Hold Time | t _{AH} | 10 | - | ns |
| Data Output Delay | t⊳s | - | - | ns |
| Data Hold Time | t _{DHR} | 10 | - | ns |



Write Operation Timing

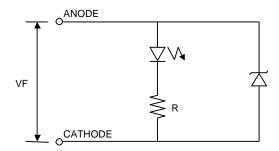
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2.4. Lighting Specifications

2.4.1. Absolute Maximum Ratings

| | - | | | | | Ta=25°C |
|-----------------------|--------|------------|------|------|-------|---------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
| Forward Voltage | VF | Note 1 | - | - | 6.0 | V |
| LED Power Dissipation | PD | - | - | - | 0.125 | mW |
| Reverse Voltage | Vr | - | - | - | 5.0 | V |

Note 1 : VF is defined as the voltage between ANODE and CATHODE as shown below.



2.4.2. Operating Characteristics

Ta=25°C

| | | | | | | 10-20 0 |
|-------------------|--------|------------|------|------|------|-------------------|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
| Foward Current | lF | VF=5.0V | - | 15 | 18 | mA |
| Luminance of | L | VF=5.0V | 160 | - | - | cd/m ² |
| Backlight Surface | | | | | | |

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3. Optical Specifications

3.1.LCD Driving Voltage

| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Units |
|---------------------|---------|------------|------|------|------|-------|
| Recommended | | Ta= -20°C | - | - | 5.5 | V |
| LCD Driving Voltage | Vcc-Vee | Ta=25°C | 4.9 | 5.3 | 5.5 | V |
| Note 1 | | Ta=70°C | 4.7 | - | - | V |

Note 1 : Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2. Optical Characteristics

Ta=25°C, 1/16 Duty, 1/5 Bias, Vod=5.5V (Note 4), θ= 0°C, φ=-°

| Parameter Symbol | | Conditions | Min. | Тур. | Max. | Units | |
|----------------------------------------------------------------------------------------|--------------|------------|-----------------------------|------|------|-------|----|
| Contrast Ra | atio Note 1 | CR | θ = 0°C , ϕ =-° | - | 80 | - | |
| Viewing Angle Shown in 3.3 | | | | | | | |
| Response | Rise Note 2 | Τον | - | - | 45 | 90 | ms |
| Time | Decay Note 3 | TOFF | - | - | 200 | 300 | ms |
| Note 1 : Contrast ratio is definded as follows (CR $- 1 \text{ on } / 1 \text{ ore}$) | | | | | | | |

Note 1 :Contrast ratio is definded as follows. (CR = LON / LOFF)

LON : Luminance of the ON segments

LOFF: Luminance of the OFF segments

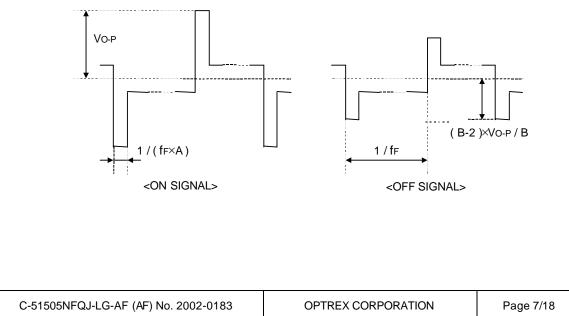
Measuring Spot:0.9mm

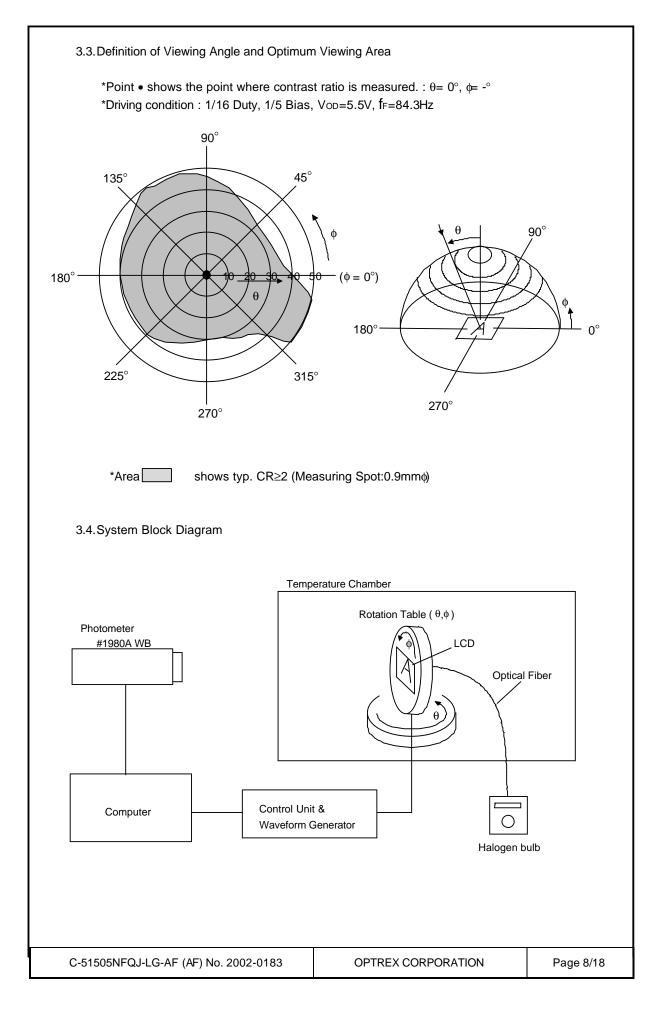
Note 2 :The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3 :The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

Note 4 :Definition of Driving Voltage VoD

Assuming that the typical driving waveforms shown below are applied to the LCD Panel at 1/A Duty - 1/B Bias (A: Duty Number, B: Bias Number). Driving voltage VoD is definded as the voltage VO-P when the contrast ratio (CR=LON / LOFF) is at its maximum.





4.I/O Terminal

4.1. Pin Assignment

<u>CN1</u>

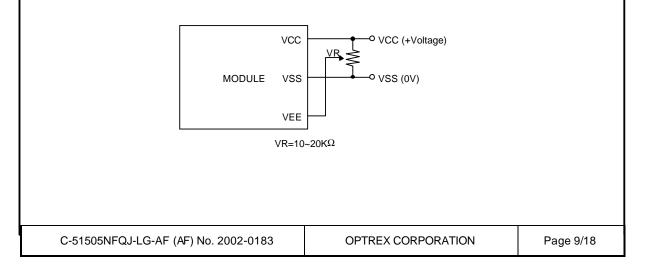
| No. | Symbol | Function |
|-----|---------|---------------------------------------------------|
| 1 | LED (A) | LED Anode Terminal |
| 2 | LED (K) | LED Cathode Terminal |
| 3 | Vss | Power Supply (0V, GND) |
| 4 | Vcc | Power Supply for Logic |
| 5 | Vee | Power Supply for LCD Drive |
| 6 | RS | Register Select Signal |
| 7 | R/W | Read/Write Select Signal H : Read L : Write |
| 8 | E | Enable Signal (No pull-up Resister) |
| 9 | DB0 | Data Bus Line / Non-connection at 4-bit operation |
| 10 | DB1 | Data Bus Line / Non-connection at 4-bit operation |
| 11 | DB2 | Data Bus Line / Non-connection at 4-bit operation |
| 12 | DB3 | Data Bus Line / Non-connection at 4-bit operation |
| 13 | DB4 | Data Bus Line |
| 14 | DB5 | Data Bus Line |
| 15 | DB6 | Data Bus Line |
| 16 | DB7 | Data Bus Line |

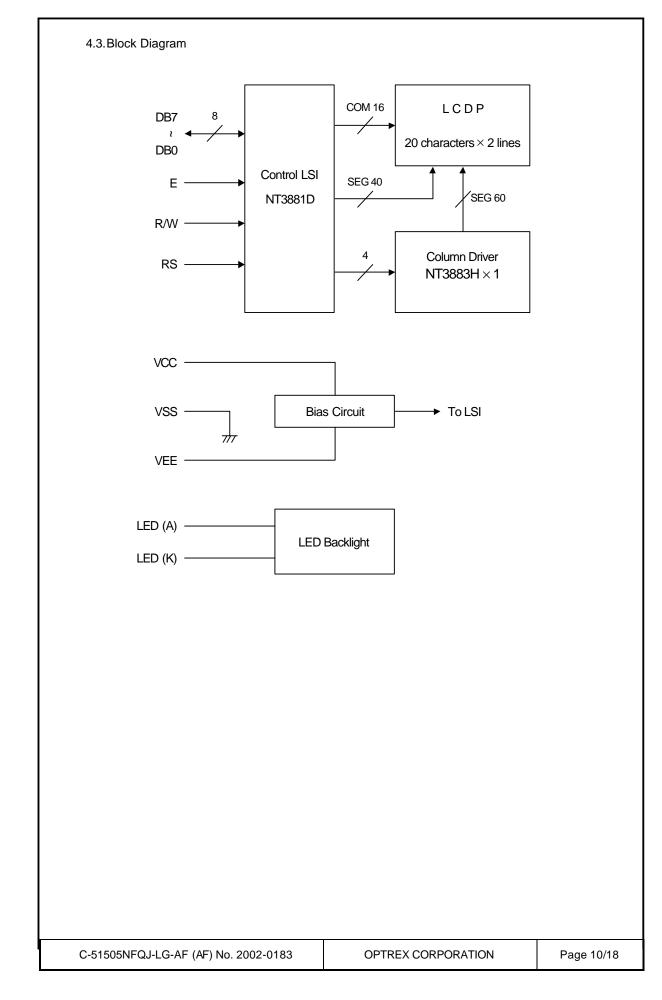
<u>CN2</u>

| No. | Symbol | Function |
|-----|---------|----------------------|
| 1 | LED (A) | LED Anode Terminal |
| 2 | LED (K) | LED Cathode Terminal |

4.2. Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.





<u>5.Test</u>

No change on display and in operation under the following test condition.

Conditions: Unless otherwise specified, tests will be conducted under the following condition. Temperature: 20±5°C Humidity : 65±5%RH tests will be not conducted under functioning state.

| Parameter | Conditions | Notes |
|----------------------------|---------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| High Temperature Operating | 70°C±2°C, 96hrs (operation state) | |
| Low Temperature Operating | -20°C±2°C, 96hrs (operation state) | 1 |
| High Temperature Storage | 70°C±2°C, 96hrs | 2 |
| Low Temperature Storage | -20°C±2°C, 96hrs | 1,2 |
| Damp Proof Test | 40°C±2°C,90~95%RH, 96hrs | 1,2 |
| Vibration Test | Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for | 3 |
| Shock Test | To be measured after dropping from 60cm high on the concrete surface in packing state. F E G G G G G G G G | |
| | High Temperature OperatingLow Temperature OperatingHigh Temperature StorageLow Temperature StorageDamp Proof TestVibration Test | High Temperature Operating $70^{\circ}C\pm 2^{\circ}C$, 96hrs (operation state)Low Temperature Operating $-20^{\circ}C\pm 2^{\circ}C$, 96hrs (operation state)High Temperature Storage $70^{\circ}C\pm 2^{\circ}C$, 96hrsLow Temperature Storage $-20^{\circ}C\pm 2^{\circ}C$, 96hrsDamp Proof Test $40^{\circ}C\pm 2^{\circ}C$, 90~95%RH, 96hrsVibration TestTotal fixed amplitude : 1.5mmVibration TestTotal fixed amplitude : 1.5mmVibration Frequency : 10~55HzOne cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutesShock TestTo be measured after dropping from 60cm high on the concrete surface in packing state.Image: Dropping method comer dropping A comer : onceDropping method comer dropping B,C,D edge : once |

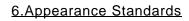
Note 1 :No dew condensation to be observed.

Note 2 :The function test shall be conducted after 4 hours storage at the normal

Temperature and humidity after removed from the test chamber.

Note 3 :Vibration test will be conducted to the product itself without putting it in a container.

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| | | |

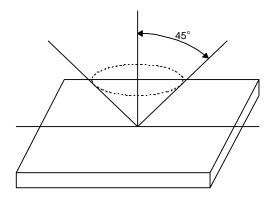


6.1. Inspection conditions

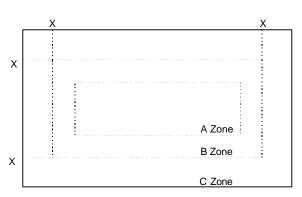
The LCD shall be inspected under 40W white fluorescent light.

The distance between the eyes and the sample shall be more than 30cm.

All directions for inspecting the sample should be within 45° against perpendicular line.



6.2. Definition of applicable Zones



X : Maximum Seal Line

A Zone : Active display area

B Zone : Out of active display area ~ Maximum seal line C Zone : Rest parts

A Zone + B Zone = Validity viewing area

| 1 The Shape of Dot (1) Pin Hole Dimension Acceptable Number 0 0 0 0 0 0 10 < D \leq 0.15 1 pc / dot(only segment)or le 0 0 0 1 pc / dot(only segment)or less (2) Pattern Shift A B $ A - B \leq 0.15$ Image: segment se | No. | Parameter | | | Crite |) / 2 * : Disregar ria | | |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------|-----------------------|--------------------|-----------------------------------------------------------|---------|---------------------------|----------------|--|
| $\begin{array}{ c c c c c } \hline \hline & $ | 1 | The Shape of Dot | (1) Pin Hole | | | | | |
| $\begin{array}{c c c c c c } \hline D \leq 0.10 & 1 \ \text{pc} \ / \ \text{dot}(\text{only segment})\text{or les} \\ \hline 0.10 < D \leq 0.15 & 1 \ \text{pc} \ / \ \text{dot}(\text{only segment})\text{or les} \\ \hline 0.10 < D \leq 0.15 & 1 \ \text{pc} \ / \ \text{cell or less} \\ \hline \end{array}$ $(2) \ \text{Pattern Shift}$ $(3) \ \text{Breakage or Chips / Deformation}$ $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | 1 | Dimensio | n | Acceptable | Number | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | | D ≤ 0 | .10 | * | | |
| $2 \text{ pcs / cell or less}$ (2) Pattern Shift (3) Breakage or Chips / Deformation (3) Breakage or Chips / Deformation $A = 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ 2.Dot Type 2.Dot Type $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $A \le 0.10 + 1.\text{Segment Type}$ $Dimension Acceptable Number$ $Dimension Acceptable Number = 1.\text{Segment Type}$ $Dimension Acceptable Number$ $Dimension Acceptable Number = 1.\text{Segment Type}$ $Dimension Acceptable Number$ $Dimension Acceptable Number = 1.\text{Segment Type}$ Dim | | | | | | 1 pc / dot(only se | gment)or les | |
| (3) Breakage or Chips / Deformation (3) Breakage or Chips / Deformation 1.Segment Type Dimension Acceptable Number $A \le 0.10$ $B \le 0.10$ 2.Dot Type Dimension Acceptable Number $A \le 0.10$ $B \le 0.10$ C $A \le 0.10$ $A \ge 0.1$ | | | $\wedge \forall L$ | $0.10 < D \le 0.$ | 15 | 2 pcs / cell or les | s | |
| (3) Breakage or Chips / Deformation (3) Breakage or Chips / Deformation 1.Segment Type Dimension Acceptable Number A ≤ 0.10 \times B ≤ 0.10 \times 2.Dot Type Dimension Acceptable Number A ≤ 0.10 \times B ≤ 0.10 \times (Should not be connected to next do 1 pc / dot(only segment)or less (Should not be connected to next do B ≤ 0.10 \times 3.Defective type extends over multiple numbers of dots Dimension Acceptable Number $d = 0.10$ \times 3.Defective type extends over multiple numbers of dots Dimension Acceptable Number Disc. 10 \times 1 pc / dot(only segment)or less 2 pcs / cell or less (ndividual dot must secure 1/2 area | | | | | | | | |
| (3) Breakage or Chips / Deformation (3) Breakage or Chips / Deformation 1.Segment Type Dimension Acceptable Number $A \le 0.10$ \times $B \le 0.10$ \times 2.Dot Type Dimension Acceptable Number $A \le 0.10$ \times $B \le 0.10$ \times (Should not be connected to next do 1 pc / dot(only segment)or less (Should not be connected to next do $B \le 0.10$ \times 3.Defective type extends over multiple numbers of dots $B \le 0.10$ \times 3.Defective type extends over multiple numbers of dots Dimension Acceptable Number $D \ge 0.10$ \times $D \ge 0.10$ -0.10 -0.10 -0.10 -0.10 -0.10 | | | (2) Pattern Shi | ift | | | | |
| A Dimension1.Segment TypeDimensionAcceptable Number $A \le 0.10$ \cdot $B \le 0.10$ \cdot $B \le 0.10$ \cdot DimensionAcceptable Number $A \le 0.10$ \cdot | | | A B | A − B ← | ≤ 0.15 | | | |
| $\begin{array}{ c c c c c } \hline Dimension & Acceptable Number \\ \hline A \leq 0.10 & * \\ \hline B \leq 0.10 & * \\ \hline B \leq 0.10 & * \\ \hline \end{array}$ | | | (3) Breakage o | | | | | |
| $A \leq 0.10$ | | | A → | | | | | |
| $A \le 0.10$ $B \le 0.10$ $*$ 2.Dot Type $2.Dot Type$ $A \le 0.10$ $A \le 0.10$ $*$ $A \le 0.10$ $A \le 0.10$ $*$ $A \le 0.10$ $*$ $A \le 0.10$ $*$ $A \le 0.10$ $*$ $(Should not be connected to next dother in the connected to next dother i$ | | | | | | | Number | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | | | | | | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | | B ≤ 0.10 | , | | | |
| $A \leq 0.10$ $A \leq 0.15$ $2 \text{ pcs / cell or less}$ $(Should not be connected to next dother or ext dother or$ | | | | 2.Dot Type | | | | |
| $(Should not be connected to next do 1 pc / dot(only segment)or less 2 pcs / cell or less (Should not be connected to next do B \le 0.10 * 3.Defective type extends over multiple numbers of dots Dimension Acceptable Number D \le 0.10 * 1 pc / dot(only segment)or less 2 pcs / cell or less (Should not be connected to next do B \le 0.10 * 1 pc / dot(only segment)or less 2 pcs / cell or less (Individual dot must secure 1/2 area$ | | | А | | | Acceptable Nu | mber | |
| $ \begin{array}{c ccccccccccccccccccccccccccccccccccc$ | | | | A≤0.10 | | * | | |
| $0.10 < A \le 0.15$ $2 \text{ pcs / cell or less}$ $(Should not be connected to next down of the conn$ | | | | | · | | | |
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | | 0.40 0.45 | - | | or less | |
| B ≤ 0.10 *3.Defective type extends over multiple numbers of dotsDimensionAcceptable NumberD ≤ 0.10 *0.10*0.10*0.10*0.10*0.10*0.10*0.10*0.10*0.10*0.10*0.10*2pcs / cell or less(Individual dot must secure 1/2 area) | | | B → B ← | 0.10 <a≤0.15< td=""><td></td><td></td><td></td></a≤0.15<> | | | | |
| 3.Defective type extends over multiple numbers of dots → → → → → → → → → → → → → → → → → → → | | | | D (0.40 | (Shou | | ed to next dot | |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $ | | | | B ≤ 0.10 | | ^ | | |
| $ \begin{array}{c c} \bullet & \bullet \\ \hline \hline \bullet & \bullet \\ \hline \hline \\ 0.10 < D \le 0.10 \\ \hline \\ \hline \\ 0.10 < D \le 0.15 \\ \hline \\ $ | | | 3.[| | extends | | | |
| Image: product of the second sector of the secure 1/2 area Image: product of the secure 1/2 area | | | | | | Acceptable Nu | mber | |
| ·→ · · · · · · · · · · · · · · · · · · | | | | D≤0.10 | | * | | |
| 0.10 <d≤0.15 (Individual dot must secure 1/2 area</d≤0.15 | | | | | | | or less | |
| | | | | 0.10 <d≤0.15< td=""><td></td><td></td><td></td></d≤0.15<> | | | | |
| or more) | | | | | | | re 1/2 area | |
| | | | | | or mo | ore) | | |
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| No. | Parameter | | (| Criteria | | | |
|-----|------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------|---------------|------------|------|--|
| 2 | Black and | (1) Round Shape | | | | | |
| | White Spots, | | Zone | Acce | ptable Nur | nber | |
| | Foreign Substances | Dimension | | А | В | С | |
| | | | $D \leq 0.10$ | * | * | * | |
| | | 0.10< | D ≤ 0.20 | 6 | 6 | * | |
| | | 0.20< | 0.20< D ≤ 0.30 2 2 | | | | |
| | | Individual dot must secure 1/2 area or more. (2) Line Shape | | | | | |
| | | | Zone | Acce | ptable Nur | nber | |
| | | Length | Width | A | В | С | |
| | | L ≤2.0 | W≤0.03 | * | * | * | |
| | | L ≤2.0 | 0.03 <w≤0.05< td=""><td>4</td><td>4</td><td>*</td></w≤0.05<> | 4 | 4 | * | |
| | | * | 0.05 <w< td=""><td>In the sam</td><td>e way (1)</td><td>*</td></w<> | In the sam | e way (1) | * | |
| | | | n 7pcs as total. mplex Foreign Su | ubstance Defe | ects") | | |
| 3 | Color Variation | Not to be cons | picuous defects. | | | | |
| 4 | Air Bubbles | | | | | | |
| | (between glass | Zone Acceptable Number | | | | | |
| | & polarizer) | Dimension | | А | В | С | |
| | | | D ≤ 0.15 | * | * | * | |
| | | 0.15< D ≤ 0.30 2 3 | | | | * | |
| | | 0.30< | D ≤ 0.50 | 1 | 2 | * | |
| | | 0.50< | D ≤ 1.00 | 0 | 1 | * | |
| | | | ore than 3pcs as total. r to "Complex Foreign Substance Defects") | | | | |
| 5 | Polarizer Scratches | Not to be cons | picuous defects. | | | | |
| 6 | Polarizer Dirts | If the stains are removed easily from LCDP surface, the module is not defective. | | | | | |
| 7 | Complex Foreign Substance Defects | Black spots, line shaped foreign substances or air bubbles between glass & polarizer should be 7pcs maximum in total. | | | | | |
| 8 | Distance between Different Foreign Substance Defects | 20mm or more | | | | | |
| | | | | | _ | | |
| | | | | | | | |

| No. | Parameter | | - (1 | Crit | | | rd Units : mm |
|-----|---------------|---------------------------|--------------|--------------|-----------------------------|---------------------------------------------|----------------------------|
| 9 | Chipped Glass | (1) Other that | n electrode | | | areas | |
| | | "Δ" siz | e:0.82or m | ore | "Δ" size | م.ا موم ا | than 0.82 |
| | | X | Y | Z | X | Y | Z |
| | | * | <u>≤</u> 0.4 | * | * | ≤0.3 | |
| | | ≤2.5 | ≤0.7 | =t | ≤2.0 | ≤0.5 | |
| | | ≤3.5 | ≤1.0 | ≤1/2t | ≤3.0 | ≤0.5 | 5 ≤1/2t |
| | | ≤5.5 | ≤1.5 | ≤1/3t | ≤4.5 | ≤1.2 | 2 ≤1/3t |
| | | (2) Corner An 1.Lead A | | | "^" oit | | "A" size |
| | | X | | Y/ | "A" siz | | Less than 0.82 |
| | | | | | | | |
| | | | \sim | | | X+Y X+Y ≤4.0 ≤3.0 | |
| | | 2.Other the | | ode pad Area | "A" siz 0.82or r | nore | "A" size Less than 0.82 |
| | | | \mathbf{V} | | X+Y ≤3.0 | | X+Y ≤2.0 |
| | | | glass sha | ll be judged | defects sha only from th | all be up ne size | |
| | | | | | | | |

| 7.Code System of Production Lot | | |
|--------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------|------------|
| The production lot of module is specifi | ed as follows. | |
| Produc | Factory Number (Numeral) Factory Code (Alphabet) tion Week (1~5) lonth (1~9, X, Y, Z) ver 2 digits) | |
| 8.Type Number | | |
| The type number of module is specifie | d as follows. | |
| C-51505NFQJ-LG-AF | | |
| | | |
| 9.Applying Precautions | | |
| Please contact us when questions and specificationsarise. | d/or new problems not specified in this | |
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10.Precautions Relating Product Handling

The Following precautions will guide you in handling our product correctly.

- 1) Liquid crystal display devices
- 1. The liquid crystal display device panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care.
- 2. The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.
- 2) Care of the liquid crystal display module against static electricity discharge.
- 1. When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.
- 2. Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- 3. Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.
- 3) When the LCD module alone must be stored for long periods of time:
- 1. Protect the modules from high temperature and humidity.
- 2. Keep the modules out of direct sunlight or direct exposure to ultraviolet rays.
- 3. Protect the modules from excessive external forces.
- 4) Use the module with a power supply that is equipped with an overcurrent protector circuit, since the module is not provided with this protective feature.
- Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- 6) Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.
- 7) For models which use CFL:
- 1. High voltage of 1000V or greater is applied to the CFL cable connector area. Care should be taken not to touch connection areas to avoid burns.
- 2. Protect CFL cables from rubbing against the unit and thus causing the wire jacket to become worn.
- 3. The use of CFLs for extended periods of time at low temperatures will significantly shorten their service life.
- 8) For models which use touch panels:
- 1. Do not stack up modules since they can be damaged by components on neighboring modules.
- 2. Do not place heavy objects on top of the product. This could cause glass breakage.
- 9) For models which use COG,TAB,or COF:
 - 1. The mechanical strength of the product is low since the IC chip faces out unprotected from the rear.
 - Be sure to protect the rear of the IC chip from external forces.
 - 2. Given the fact that the rear of the IC chip is left exposed, in order to protect the unit from electrical damage, avoid installation configurations in which the rear of the IC chip runs the risk of making any electrical contact.

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10)Models which use flexible cable, heat seal, or TAB:

- 1. In order to maintain reliability, do not touch or hold by the connector area.
- 2. Avoid any bending, pulling, or other excessive force, which can result in broken connections.
- 11) In case of buffer material such as cushion / gasket is assembled into LCD module, it may have an adverse effect on connecting parts (LCD panel-TCP / HEAT SEAL / FPC / etc., PCB-TCP / HEAT SEAL / FPC etc., TCP-HEAT SEAL, TCP-FPC, HEAT SEAL-FPC, etc.,) depending on its materials.
 Descent shark and evaluate these materials.

Please check and evaluate these materials carefully before use.

12)In case of acrylic plate is attached to front side of LCD panel, cloudiness (very small cracks) can occur on acrylic plate, being influenced by some components generated from polarizer film.. Please check and evaluate those acrylic materials carefully before use.

11.Warranty

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1. We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4. When the product is in CFL models, CFL service life and brightness will vary according to the performance of the inverter used, leaks, etc. We cannot accept responsibility for product performance, reliability, or defect, which may arise.
- 5. We cannot accept responsibility for intellectual property of a third party, which may arise through the application of our product to your assembly with exception to those issues relating directly to the structure or method of manufacturing of our product.
- 6. Optrex will not be held responsible for any quality guarantee issue for defect products judged as Optrex-origin longer than 2 (two) years from Optrex production or 1(one) year from Optrex, Optrex America, Optrex Europe delivery which ever comes later.

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