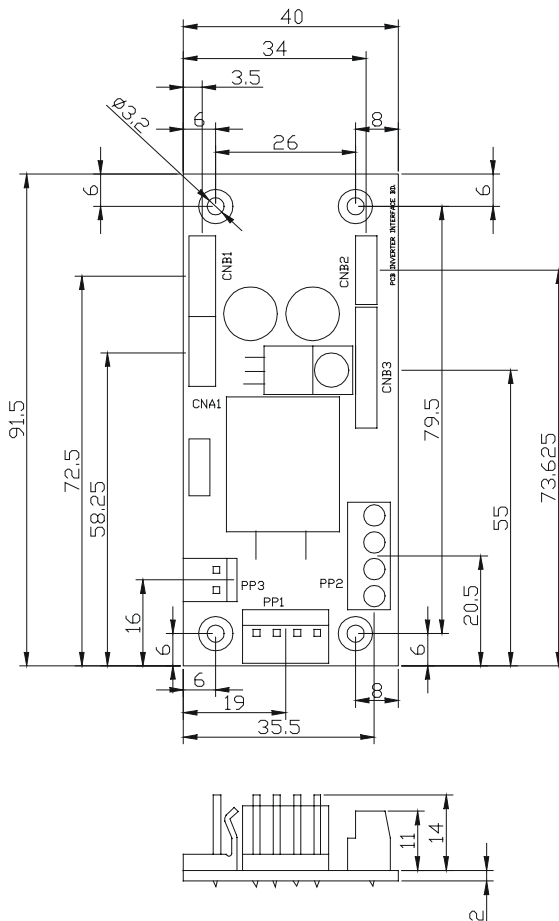


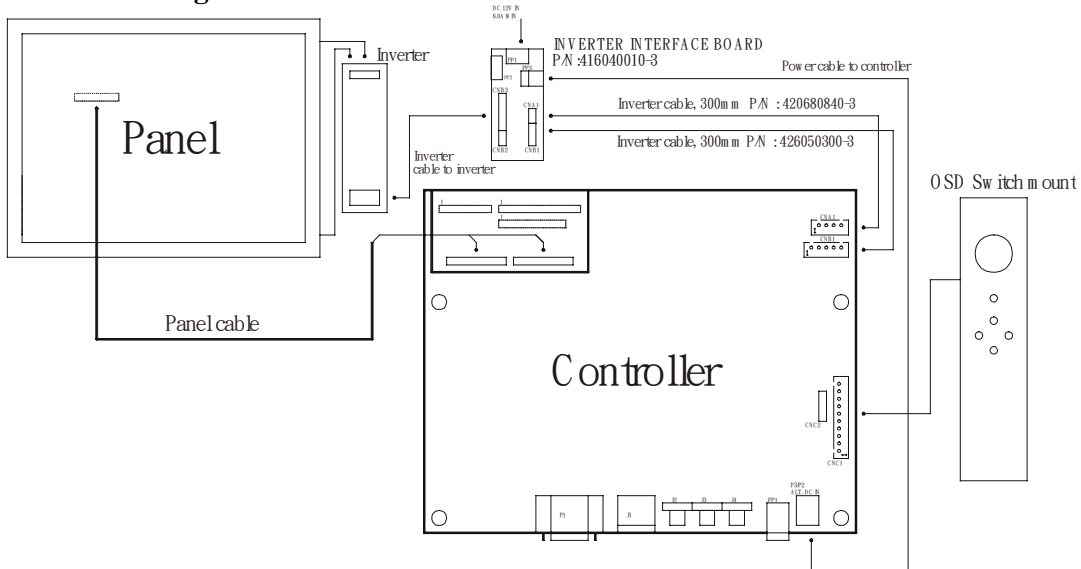
The Inverter interface board provides interface to drive up the high current consumption panel inverter in excess of 3.5A. This board enables current of up to 8 Amps to be managed safely while retaining the capability for inverter dimming and management of the inverter enable signal according to VESA DPMS standards. Additionally, the inverter interface board provides over-voltage protection via a resettable fuse when the input voltage exceeds 13V and reverse polarity protection against accidental misconnection.



Connector Type :

- CNA1, CNB2 : JST 4 ways, B4B-XH-A
- CNB1 : JST 5 ways, B5B-XH-A
- CNB3 : JST 8 ways, B8B-XH-A
- PP1 : PWR 4 way
- PP2 : Terminal Block 2 poles x 2
- PP3 : PWR 2 ways

Illustrated Diagram :



PP1 - 12VDC power supply – input, Power header 4 ways 0.156” pitch

PIN	DESCRIPTION
1	+12VDC
2	+12VDC
3	Ground
4	Ground

PP2 – Alternate 12VDC power supply – input, Terminal Block 2 poles

PIN	DESCRIPTION
1	+12VDC
2	+12VDC
3	Ground
4	Ground

PP3 – 12VDC power supply to controller – Output, Power header 2 ways, 0.156” pitch

PIN	DESCRIPTION
1	+12VDC
2	Ground

CNA1 - Inverter interface to controller, JST B4B-XH-A

PIN	SYMBOL	DESCRIPTION
1	NC	No connection
2	AUX_GND	Ground
3	AUX_GND	Ground
4	AUX_Vcc	+5V DC, 500mA max

CNB1 – Inverter interface to controller, JST B5B-XH-A

PIN	SYMBOL	DESCRIPTION
1	GND	Ground
2	BL_ON	Backlight power
3	BLCTRL	Backlight on/off control signal
4	BVR_WIP	Backlight brightness VR pin WIP
5	BVR_A	Backlight brightness VR pin A

CNB2 – Inverter interface to backlight inverter, JST B4B-XH-A

PIN	SYMBOL	DESCRIPTION
1	VLCD12	Panel power
2	AUX_GND	Ground
3	AUX_GND	Ground
4	AUX Vcc	+5V DC, 500mA max

CNB3 – Inverter interface to backlight inverter, JST B8B-XH-A

PIN	SYMBOL	DESCRIPTION
1	VLCD12	Panel power
2	VLCD12	Panel power
3	GND	Ground
4	GND	Ground
5	VLCD12	Panel power
6	BLCTRL	Backlight on/off control signal
7	BVR_WIP	Backlight brightness VR pin WIP
8	BVR_A	Backlight brightness VR pin A