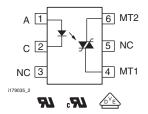


# Optocoupler, Phototriac Output, High dV/dt, Low Input Current





#### **DESCRIPTION**

The IL420 and IL4208 consists of a GaAs IRLED optically coupled to a photosensitive non-zero crossing TRIAC network. The TRIAC consists of two inverse parallel connected monolithic SCRs. These three semiconductors are assembled in a six pin dual in-line package.

High input sensitivity is achieved by using an emitter follower phototransistor and a cascaded SCR predriver resulting in an LED trigger current of less than 2 mA (DC).

The use of a proprietary dV/dt clam results in a static dV/dt of greater than 10 kV/ $\mu$ s. This clamp circuit has a MOSFET that is enhanced when high dV/dt spikes occur between MT1 and MT2 of the TRIAC. When conducting, the FET clamps the base of the phototransistors, disabling the first stage SCR predriver.

The 600 V, 800 V blocking voltage permits control of offline voltages up to 240 VAC, with a safety factor of more than two, and is sufficient for as much as  $380 \, V_{AC}$ .

The IL420, IL4208 isolates low-voltage logic from 120  $V_{AC}$ , 240  $V_{AC}$ , and 380  $V_{AC}$  lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

#### **FEATURES**

- High input sensitivity I<sub>FT</sub> = 2 mA
- 600 V, 800 V blocking voltage
- 300 mA on-state current
- High static dV/dt 10 kV/µs
- Very low leakage < 10 μA</li>
- Isolation test voltage 5300 V<sub>RMS</sub>
- iodianori toot voitago coco vai
- Small 6-pin DIP package
- Compliant to RoHS Directive 2002/95/EC and ir accordance to WEEE 2002/96/EC



- Solid state relays
- · Industrial controls
- Office equipment
- Consumer appliances

#### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code H, double protection
- CSA 93751
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1

ORDERING INFORMATION	
I L 4 2 0 #	- X 0 # # T
PART NUMBER	PACKAGE OPTION TAPE AND REEL 10.16 mm
	Option 9  9.27 mm  3.0.1 mm
AGENCY CERTIFIED/PACKAGE	BLOCKING VOLTAGE VDRM (V)

AGENCY CERTIFIED/PACKAGE	BLOCKING VOLTAGE V <sub>DRM</sub> (V)				
UL, cUL	600	800			
DIP-6	IL420	IL4208			
DIP-6, 400 mil, option 6	IL420-X006	-			
SMD-6, option 7	IL420-X007T (1)	IL4208-X007T (1)			
SMD-6, option 8	IL420-X008T	-			
SMD-6, option 9	IL420-X009T (1)	IL4208-X009T (1)			
VDE, UL, cUL	600	800			
DIP-6	IL420-X001	-			
DIP-6, 400 mil, option 6	IL420-X016	-			
SMD-6, option 7	IL420-X017T <sup>(1)</sup>	-			

#### Note

(1) Also available in tubes, do not put T on the end.

Document Number: 83629 Rev. 2.0, 29-Mar-11 For technical questions, contact: optocoupleranswers@vishay.com

## Optocoupler, Phototriac Output, High dV/dt, Low Input Current



PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT					
Reverse voltage			V <sub>R</sub>	6	V
Forward current			I <sub>F</sub>	60	mA
Surge current			I <sub>FSM</sub>	2.5	Α
Power dissipation			P <sub>diss</sub>	100	mW
Derate from 25 °C				1.33	mW/°C
OUTPUT					
Peak off-state voltage		IL420	$V_{DRM}$	600	V
reak on-state voltage		IL4208	V <sub>DRM</sub>	800	V
RMS on-state current			I <sub>TM</sub>	300	mA
Single cycle surge current			I <sub>TSM</sub>	3	Α
Power dissipation			P <sub>diss</sub>	500	mW
Derate from 25 °C				6.6	mW/°C
COUPLER					
Isolation test voltage between emitter and detector	t = 1 s		V <sub>ISO</sub>	5300	$V_{RMS}$
Pollution degree (DIN VDE 0109)				2	
Creepage distance				≥ 7	mm
Clearance distance				≥ 7	mm
Comparative tracking (1)				≥ 175	
lealation registance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C		R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C		R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Storage temperature range			T <sub>stg</sub>	- 55 to + 150	°C
Ambient temperature range			T <sub>amb</sub>	- 55 to + 100	°C
Soldering temperature (2)	max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T <sub>sld</sub>	260	°C

#### Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Index per DIN IEC60112/VDE 0303 part 1, group IIIa per DIN VDE 6110.

<sup>(2)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering condditions for through hole devices (DIP).



## Optocoupler, Phototriac Output, High dV/dt, Low Input Current

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PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT	TEST CONDITION	I AIII	OTIMIDOL	IVIIIV.		WIAX.	Oitii
Forward voltage	I <sub>F</sub> = 10 mA		VF		1.16	1.35	V
Reverse current	V <sub>R</sub> = 6 V		<u> </u>		0.1	1.33	-
	$V_R = 0 \text{ V}$ $V_F = 0 \text{ V}, f = 1 \text{ MHz}$		I <sub>R</sub>		40	10	μA pF
Input capacitance	V <sub>F</sub> = 0 V, I = 1 IVIH2		C <sub>IN</sub>				°C/W
Thermal resistance, junction to ambient			R <sub>thja</sub>		750		-0/00
OUTPUT	V V T 100.00					400	
Off-state current	$V_D = V_{DRM}, T_{amb} = 100 ^{\circ}C$		I <sub>DRM</sub>		10	100	μA
On-state voltage	I <sub>T</sub> = 300 mA		V <sub>TM</sub>		1.7	3	V
Surge (non-repetitive), on-state current	f = 50 Hz		I <sub>TSM</sub>			3	Α
Holding current			I <sub>H</sub>		65	500	μA
Latching current	$V_T = 2.2 \text{ V}$		IL			500	μA
LED trigger current	$V_D = 5 V$		I <sub>FT</sub>		1	2	mA
Trigger current temperature gradient			$\Delta I_{FT}/\Delta T_{j}$		7	14	μΑ/°C
Critical rate of rise off-state voltage	$V_D = 0.67 \ V_{DRM}, \ T_j = 25 \ ^{\circ}C$		dV/dt <sub>cr</sub>	10 000			V/µs
Chilical rate of fise on-state voltage	$V_D = 0.67 \ V_{DRM}, \ T_j = 80 \ ^{\circ}C$		dV/dt <sub>cr</sub>	5000			V/µs
Critical rate of rise of voltage at current	$V_D = 230 V_{RMS},$ $I_D = 300 \text{ mA}_{RMS}, T_J = 25 ^{\circ}\text{C}$		dV/dt <sub>crq</sub>		8		V/µs
commutation	$V_D = 230 V_{RMS},$ $I_D = 300 \text{ mA}_{RMS}, T_J = 85 \text{ °C}$		dV/dt <sub>crq</sub>		7		V/µs
Critical rate of rise of on-state current commutation			dl/dt <sub>crq</sub>		12		A/ms
Thermal resistance, junction to ambient			R <sub>thja</sub>		150		°C/W
COUPLER							
Critical rate of rise of coupled input/output voltage	$I_T = 0 A$ , $V_{RM} = V_{DM} = V_{DRM}$		dV/dt		5000		V/µs
Capacitance (input to output)	f = 1 MHz, V <sub>IO</sub> = 0 V		C <sub>IO</sub>		0.8		pF
location registeres	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C		R <sub>IO</sub>	≥ 10 <sup>12</sup>			Ω
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C		R <sub>IO</sub>	≥ 10 <sup>11</sup>			Ω

### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX. UNIT							
Turn-on time	$V_{RM} = V_{DM} = V_{DRM}$	t <sub>on</sub>		35		μs	

## Optocoupler, Phototriac Output, High dV/dt, Low Input Current



<b>SAFETY AND INSULATION RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Climatic classification (according to IEC68 part 1)				55/100/21			
Comparative tracking index		CTI	175		399		
V <sub>IOTM</sub>			8000			V	
V <sub>IORM</sub>			630			V	
P <sub>SO</sub>					500	mW	
I <sub>SI</sub>					250	mA	
T <sub>SI</sub>					175	°C	
Creepage distance	Standard DIP-8		7			mm	
Clearance distance	Standard DIP-8		7			mm	
Creepage distance	400 mil DIP-8		8			mm	
Clearance distance	400 mil DIP-8		8			mm	
Insulation thickness	For IL4208 only		0.4			mm	

#### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

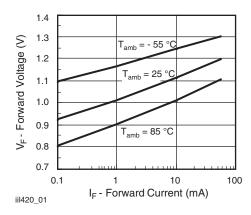


Fig. 1 - Forward Voltage vs. Forward Current

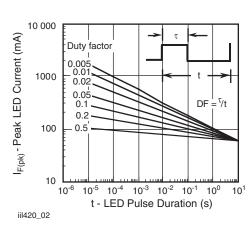


Fig. 2 - Peak LED Current vs. Duty Factor,  $\boldsymbol{\tau}$ 

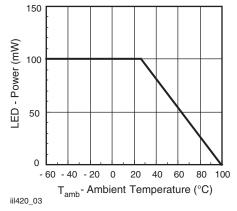


Fig. 3 - Maximum LED Power Dissipation

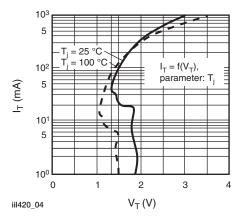


Fig. 4 - Typical Output Characteristics

<sup>•</sup> As per IEC60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



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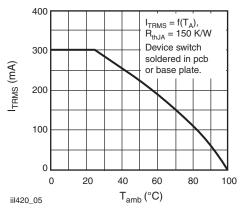


Fig. 5 - Current Reduction

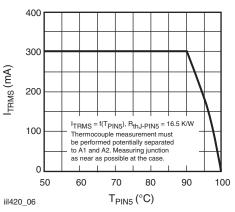


Fig. 6 - Current Reduction

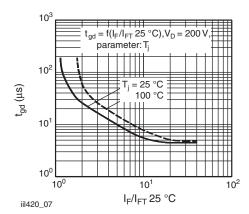


Fig. 7 - Typical Trigger Delay Time

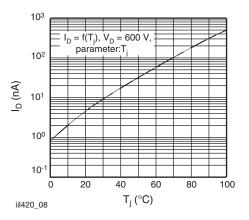


Fig. 8 - Typical Off-State Current

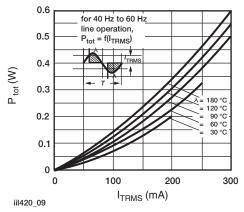


Fig. 9 - Power Dissipation

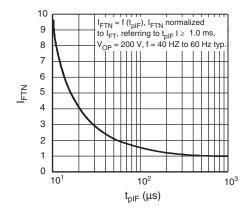


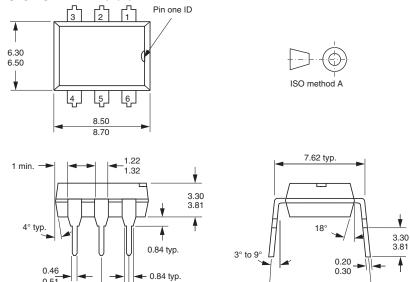
Fig. 10 - Pulse Trigger Current

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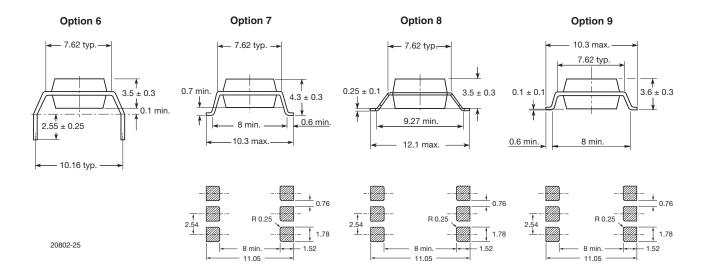


### **PACKAGE DIMENSIONS** in millimeters

i178014



2.54 typ.



7.62 to 8.81

### **PACKAGE MARKING** (example)



#### **Notes**

- Only options 1, 7, and 8 are reflected in the package marking.
- The VDE Logo is only marked on option 1 parts.
- Tape and reel suffix (T) is not part of the package marking.

# **Legal Disclaimer Notice**



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