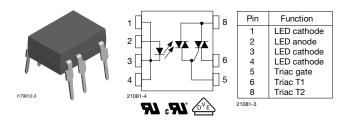


## Vishay Semiconductors

## **Power Phototriac**



#### **DESCRIPTION**

The VO2223 is an optically couple phototriac driving a power triac in a DIP-8 package. It provides a 5300 V of input to output isolation.

#### **FEATURES**

- Maximum trigger current (I<sub>FT</sub>): 10 mA
- Isolation test voltage 5300 V<sub>RMS</sub>
- Peak off-state voltage 600 V
- Load current 0.9 A<sub>RMS</sub>
- dV/dt of 210 V/µs
- DIP-8 package
- Pure tin leads
- Compliant to RoHS Directive 2002/95/EC

# Pb-free



## RoHS

#### **APPLICATIONS**

- Home appliances (air conditioners, microwave ovens, washing machines, personal hygiene systems, refrigerators, fan heaters, inductive heating cooker, water heaters, etc.)
- Industrial equipments

#### **AGENCY APPROVALS**

- UL E52744 system code H
- cUL E52744 system code H
- VDE DIN EN 60747-5-2 (VDE0884)

	752 Sint 211 307 17 3 2 (152330 1)			
ORDERING INFORMATION				
V O 2 2 2 PART NUMBER	3 - X 0 0 #  PACKAGE OPTION			
AGENCY CERTIFIED/PACKAGE	TRIGGER, CURRENT I <sub>FT</sub> (mA)			
UL, cUL	10			
DIP-8	VO2223			
VDE	10			
DIP-8	VO2223-X001			

<b>ABSOLUTE MAXIMUM RATINGS</b> (1) (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
LED continuous forward current		I <sub>F</sub>	50	mA		
LED reverse voltage		V <sub>R</sub>	6	V		
OUTPUT						
Repetitive peak off-state voltage	Sine wave, 50 Hz to 60 Hz, gate open	$V_{DRM}$	600	V		
On-state RMS current		I <sub>T(RMS)</sub>	0.9	А		
Peak non-repetitive surge current (60 Hz, 1 cycle)		I <sub>TSM</sub>	9	Α		
COUPLER						
Total power dissipation (3)		P <sub>diss</sub>	1.2	W		
Ambient temperature range		T <sub>amb</sub>	- 40 to + 85	°C		
Storage temperature range		T <sub>stg</sub>	- 40 to + 125	°C		
Soldering temperature (2)	t ≤ 10 s max.	T <sub>sld</sub>	260	°C		
Isolation test voltage	for 1 s	$V_{ISO}$	5300	$V_{RMS}$		

#### Notes

- (1) Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (2) Refer to wave profile for soldering conditions for through hole devices.
- (3) Total power dissipation value is based on 2S2P PCB.

## Power Phototriac



#### **ABSOLUTE MAXIMUM RATING CURVES**

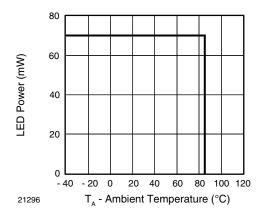


Fig. 1 - Power Dissipation vs. Temperature

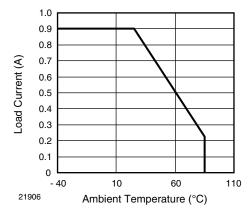


Fig. 2 - Allowable Load Current vs. Ambient Temperature

#### Note

• The allowable load current was calculated out under a given operating conditions and only for reference: LED power: Q<sub>E</sub> = 0.015 W,  $\theta_{BA}$  (4-layer) = 30 °C/W

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED trigger current	V <sub>T</sub> = 6 V	I <sub>FT</sub>			10	mA
LED reverse current	V <sub>R</sub> = 5 V	I <sub>R</sub>			10	μA
LED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	0.9		1.3	V
OUTPUT						
Peak on-state voltage	$I_F = 10 \text{ mA}, I_{TM} = \text{max}.$	$V_{TM}$			2.5	V
Peak off-state current	$I_F = 10 \text{ mA}, V_{DRM} = 600 \text{ V}$	I <sub>DRM</sub>			100	μA
Holding current	$R_L = 100 \Omega$	I <sub>H</sub>			25	mA
Critical rate of rise of off-state voltage	V <sub>IN</sub> = 400 V <sub>RMS</sub> (fig. 3)	dV/dt		210		V/µs
Critical rate of rise of commutating voltage	$V_{IN} = 240 V_{RMS},$ $I_{T} = 1 A_{RMS}$ (fig. 3)	dV/dt (c)		0.7		V/µs

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.

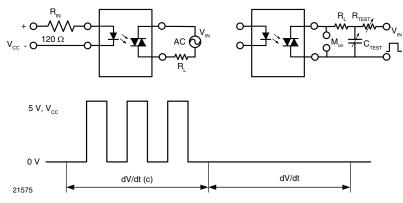


Fig. 3 - dV/dt Test Circuit

Vishay Semiconductors



## Power Phototriac

SAFETY AND INSULA	ATION RATING	S					
PARAMETER		TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification		IEC 68 part 1			40/85/21		
Pollution degree		DIN VDE0109			2		
Tracking resistance (compara	ative tracking index)	Insulation group Illa	CTI	175			
Highest allowable overvoltage		Transient overvoltage	V <sub>IOTM</sub>	8000			V <sub>peak</sub>
Maximum working insulation voltage		Recurring peak voltage	V <sub>IORM</sub>	890			V <sub>peak</sub>
Insulation resistance at 25 °C		V <sub>IO</sub> = 500 V	R <sub>IS</sub>			≥ 10 <sup>12</sup>	Ω
Insulation resistance at T <sub>S</sub>		V <sub>IO</sub> = 500 V	R <sub>IS</sub>			≥ 10 <sup>9</sup>	Ω
Insulation resistance at 100 °C		V <sub>IO</sub> = 500 V	R <sub>IS</sub>			≥ 10 <sup>11</sup>	Ω
Partial discharge test voltage		Method b, V <sub>pd</sub> = V <sub>IORM</sub> x 1.6	V <sub>pd</sub>			1424	V <sub>peak</sub>
Safety limiting values - maximum values allowed in the event of a failure	Case temperature		T <sub>SI</sub>			165	°C
	Input current		I <sub>SI</sub>			150	mA
	Output power		P <sub>SO</sub>			2000	mW
Minimum external air gap (clearance distance)		Measured from input terminals to output terminals, shortest distance through air		≥ 7			mm
Minimum external tracking (creepage distance)		Measured from input terminals to output terminals, shortest distance path along body		≥7			mm

#### Note

This phototriac coupler is suitable for "Safe Electrical Insulation" only within the safety ratings. Compliance with safety ratings shall be ensured by means of protective circuits.

## **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

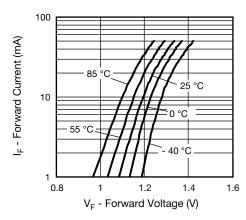


Fig. 4 - Forward Current vs. Forward Voltage

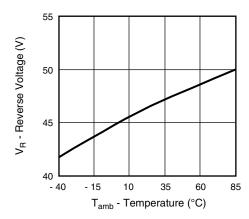


Fig. 5 - Reverse Voltage vs. Temperature

## Power Phototriac



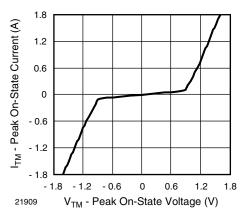


Fig. 6 - On-State Current vs. On-State Voltage

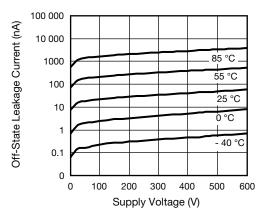


Fig. 7 - Off-State Leakage Current vs. Voltage

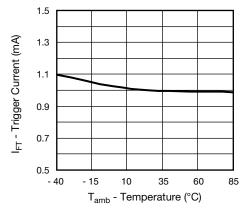


Fig. 8 - Normalized Trigger Input Current vs. Temperature

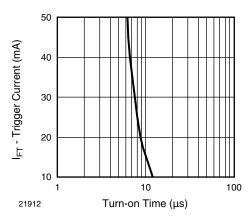


Fig. 9 - Trigger Input Current vs. Turn-on Time

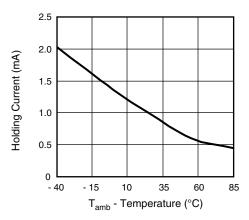


Fig. 10 - Normalized Holding Current vs. Temperature



Fig. 11 - Trigger Current vs. Trigger Pulse Width



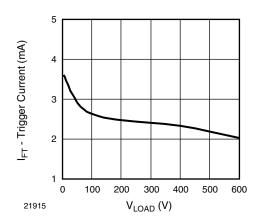
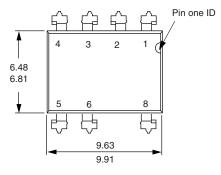


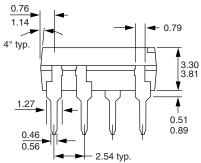
Fig. 12 - Trigger Current vs.  $V_{\text{LOAD}}$ 

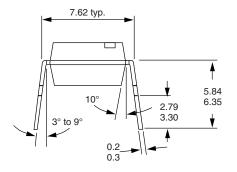
### **PACKAGE DIMENSIONS** in millimeters



**Power Phototriac** 







i178006-1

#### **PACKAGE MARKING**



## **Legal Disclaimer Notice**



Vishay

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