TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

# TLP626,TLP626-2,TLP626-4

#### **Programmable Controllers**

### AC / DC-Input Module

#### **Telecommunication**

The TOSHIBA TLP626, -2 and -4 consist of gallium arsenide infrared emitting diodes connected in inverse parallel, optically coupled to a photo–transistor.

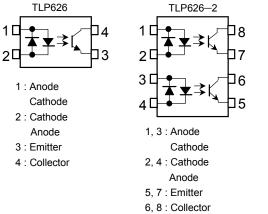
The TLP626-2 offers two isolated channels in an eight lead plastic DIP, while the TLP626-4 provides four isolated channels in a sixteen plastic DIP.

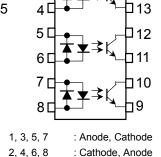
- Collector-emitter voltage: 55V(min.)
- Current transfer ratio

Classi– fication	Curre	Marking		
	Ta = 25°C		Ta = -25~75°C	Of Classi–
	I <sub>F</sub> = ±1mA V <sub>CE</sub> = 0.5V	I <sub>F</sub> = ±0.5mA V <sub>CE</sub> = 1.5V	I <sub>F</sub> = ±1mA V <sub>CE</sub> = 0.5V	fication
Rank BV	200%	100%	100%	BV
Standard	100%	50%	50%	BV, blank

- Isolation voltage: 5000V<sub>rms</sub> min.
- UL recognized: UL1577, file no.E67349
- BSI approved: BS EN60065: 2002 certificate no.7426 BS EN60950-1: 2002 certificate no.7427
- Note: Application type name for certification test, please use standard product type name, i.e. TLP626(BV): TLP626

## Pin Configuration (top view)

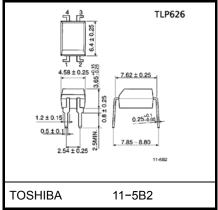




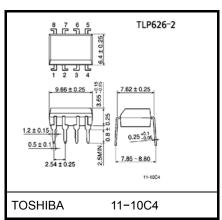
TLP626-4

1, 3, 5, 7 : Anode, Cathode 2, 4, 6, 8 : Cathode, Anode 9, 11, 13, 15 : Emitter 10, 12, 14, 16 : Collector

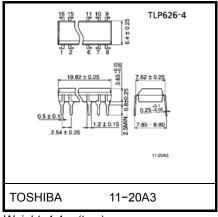
#### Unit in mm



Weight: 0.26 g (typ.)



Weight: 0.54 g (typ.)



Weight: 1.1 g (typ.)



## **Absolute Maximum Ratings (Ta = 25°C)**

				Rati	ng	11.7
	Characteristic		Symbol	TLP626	TLP626-2 TLP626-4	Unit
	Forward current		l <sub>F</sub>	60	50	mA
	Forward current derating		ΔI <sub>F</sub> / °C	–0.7(Ta ≥ 39°C)	–0.5(Ta ≥ 39°C)	mA / °C
	Pulse forward current		I <sub>FP</sub>	1(100µs pulse,100pps)		Α
LED	Power dissipation (1 circuit)		PD	100	70	mW
	Power dissipation derating (Ta ≥ 25°C, 1 circuit)		ΔP <sub>D</sub> / °C	-1.0	-0.7	mW / °C
	Junction temperature		Tj	125		°C
	Collector-emitter voltage		V <sub>CEO</sub>	55		<b>V</b>
	Emitter-collector voltage		V <sub>ECO</sub>	7		V
.to	Collector current		Ic	I <sub>C</sub> 50		mA
Detector	Collector power dissipation (1 circuit)		Pc	150	100	mW
	Collector power dissipation derating (Ta ≥ 25°C, 1 circuit)		ΔP <sub>C</sub> / °C	-1.5	-1.0	mW / °C
	Junction temperature		Tj	125		°C
Sto	rage temperature range		T <sub>stg</sub>	-55~125		O°
Оре	erating temperature range		Popr	-55~100		°C
Lead soldering temperature		T <sub>sol</sub>	260(10s)		O°	
Tot	Total package power dissipation (1 circuit)		PT	250	150	mW
	al package power dissipation derating ≥ 25°C, 1 circuit)		ΔP <sub>T</sub> / °C	-2.5	-1.5	mW / °C
Isol	ation voltage	(Note 1)	BVS	5000(AC, 1mir	n., RH≤60%)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal: LED side pins shorted together, and detector side pins shorted together.

## **Recommended Operating Conditions**

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V <sub>CC</sub>	_	5	24	V
Forward current	I <sub>F(RMS)</sub>	_	1.6	20	mA
Collector current	IC	_	1	10	mA
Operating temperature	T <sub>opr</sub>	-25	_	75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

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# Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = ±10mA	1.0	1.15	1.3	V
ED	Reverse current	lF	V <sub>F</sub> = ±0.7V	_	2.5	20	μΑ
	Capacitance	C <sub>T</sub>	V = 0, f = 1MHz	_	60	_	pF
	Collector–emitter breakdown voltage	V <sub>(BR)CEO</sub>	I <sub>C</sub> = 0.5mA	55	_	_	V
tor	Emitter–collector breakdown voltage	V <sub>(BR)ECO</sub>	I <sub>E</sub> = 0.1mA	7	_	_	V
Detector	Collector dark current	lana	V <sub>CE</sub> = 24V	_	10	10	nA
	Collector dark current	ICEO	V <sub>CE</sub> = 24V, Ta = 85°C	١	2	50	μA
	Capacitance collector to emitter	C <sub>CE</sub>	V=0, f=1MHz	_	12	_	pF

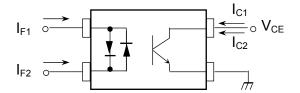
## **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Current transfer ratio	Ic/le	$I_F = \pm 1 \text{mA}, V_{CE} = 0.5 \text{V}$	100	_	1200	%
Current transfer ratio	IC / IF	rank BV	200	_	- 1200 - —	70
Low input CTR	I <sub>C</sub> / I <sub>F</sub> (low)	I <sub>F</sub> = ±0.5mA, V <sub>CE</sub> = 1.5V	50	_	_	%
	IC / IF(IOW)	rank BV	100		1200 — — — — — — — — — — — — — — — — — —	/0
		$I_C = 0.5 \text{mA}, I_F = \pm 1 \text{mA}$	_	_	0.4	
Collector–emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> = 1mA, I <sub>F</sub> = ±1mA	_	0.2	_	V
3.5		rank BV	_	_	0.4	
Off-state collector current	I <sub>C</sub> (off)	$V_F = \pm 0.7 V$ , $V_{CE} = 24 V$	_	1	10	μΑ
CTR symmetry *1	I <sub>C</sub> (ratio)	$I_{C}(I_{F} = -1mA) / I_{C}(I_{F} = 1mA)$	0.5	_	2	_

## Coupled Electrical Characteristics ( $Ta = -25 \sim 75$ °C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	I <sub>F</sub> = 1mA, V <sub>CE</sub> = 0.5V	50	_	_	%
	IC / IF	rank BV	100	_	_	/0
Low input CTD	la / l=/low)	I <sub>F</sub> = 0.5mA, V <sub>CE</sub> = 1.5V	_	50	_	- %
Low input CTR	I <sub>C</sub> / I <sub>F</sub> (low)	rank BV	_	100	_	70

\*1 
$$I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$$





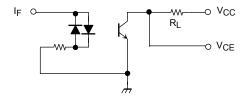
# Isolation Characteristics (Ta = 25°C)

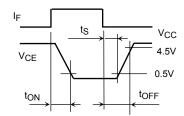
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	Cs	V <sub>S</sub> = 0, f = 1MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500V	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage		AC, 1 minute 5000	_	_	Vrms	
	$BV_S$	AC, 1 second, in oil	_	10000	_	VIIIIS
		DC, 1 minute, in oil	-	10000	-	Vdc

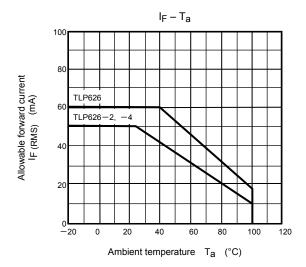
# **Switching Characteristics (Ta = 25°C)**

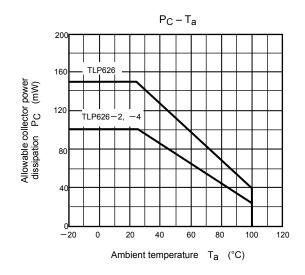
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Rise time	t <sub>r</sub>	$V_{CC}$ = 10V, $I_C$ = 2mA $R_L$ = 100 $\Omega$	_	8	_	
Fall time	t <sub>f</sub>		_	8	_	μs
Turn-on time	t <sub>on</sub>		_	10	_	μο
Turn-off time	t <sub>off</sub>		_	8	_	
Turn-on time	t <sub>ON</sub>	$R_L = 4.7 k\Omega \text{ (Fig.1)}$ $V_{CC} = 5 \text{ V, I}_F = \pm 1.6 \text{mA}$	_	10	_	
Storage time	ts		_	50	-	μs
Turn-off time	T <sub>OFF</sub>		_	300	_	

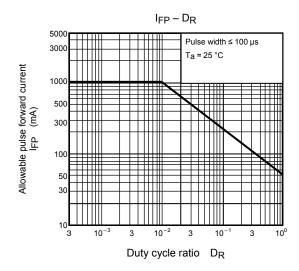
Fig. 1 Switching operating conditions

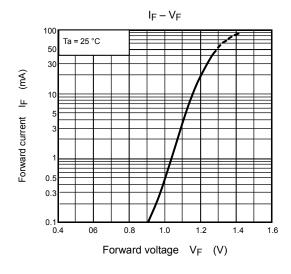


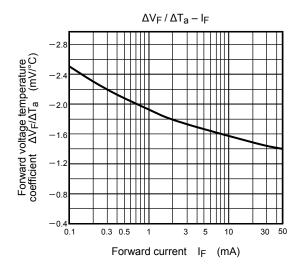


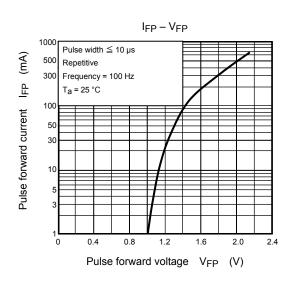


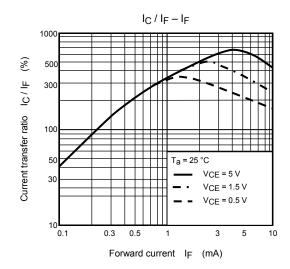


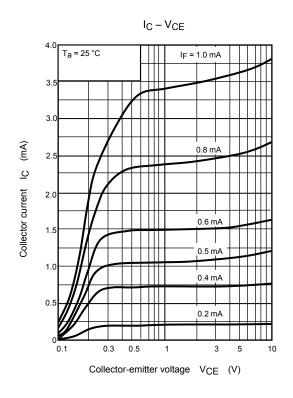


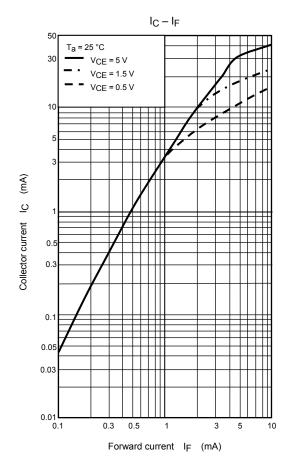


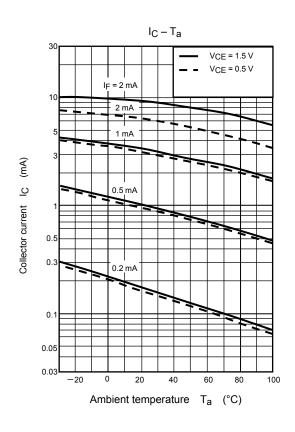




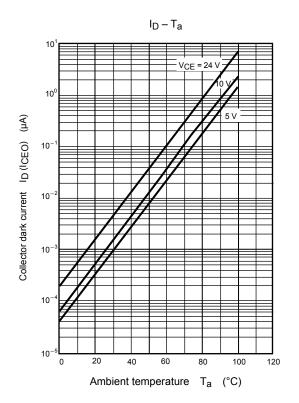


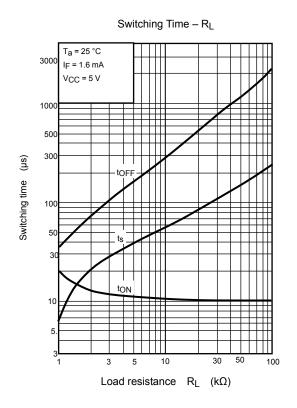






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