
HCS473 Rev. B Data Sheet Errata

The HCS473 Rev. B parts you have received conform functionally to the Device Data Sheet (DS40035B), except for the anomalies described below.

None.

All the problems listed here will be addressed in future revisions of the HCS473 silicon.

Clarifications/Corrections to the Data Sheet:

In the Device Data Sheet (DS40035B), the following clarifications and corrections should be noted.

1. Features: Encoder Operation

- Regardless if QUEUE is enabled, the 2-bit QUE field will always be transmitted giving a fixed 69-bit code word. The QUE value indicates button queue information if enabled with the Queue counter enable (QUEN) configuration bit, otherwise is fixed at 00_b.
- The Long Preamble Duty Cycle option (LPRD) formerly selected between 33% or 50% duty cycle for long preambles only. It now is the Preamble Duty Cycle (PRD) and sets the preamble duty cycle for every preamble, regardless of the duration.
- The four available guard time (TG) options are
 - 3 RFTE
 - 6.4 ms
 - 51.2 ms
 - 102.4 ms
- Limited SEED and Production SEED modes permanently disable seed transmission capability when the synchronization counter LSB increments from 7F(h) to 80(h).

2. Features: Transcoder Operation

- The Transcoder Select Acknowledge is now two pulses.
- LF data responses begin with a 1 LFTE low START pulse immediately followed by 32 bits of data, Least Significant bit first.
- Intelligent damping pulses, when enabled, are applied for 5 μ s, repeating every 44 μ s.
- Clamping, for LF responses, is now a low resistance connection across the antenna inputs.

HCS473

NOTES:

Note the following details of the code protection feature on PICmicro® MCUs.

- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the PICmicro microcontroller in a manner outside the operating specifications contained in the data sheet. The person doing so may be engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as “unbreakable”.
- Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our product.

If you have any further questions about this matter, please contact the local sales office nearest to you.

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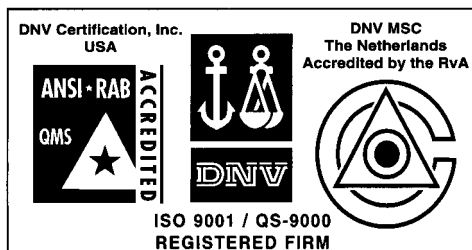
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08/01/02